

# Spatially Coupled LDPC Codes: From Theory to Practice

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The author gratefully acknowledges David Mitchell  
for help with the preparation of this presentation

## ● Introduction: From Shannon to Modern Coding Theory

➔ Channel capacity, structured codes, random codes, LDPC codes

## ● LDPC Block Codes

➔ Parity-check matrix and Tanner graph representations, minimum distance bounds, iterative decoding thresholds, protograph-based constructions

## ● Spatially Coupled LDPC Codes

➔ Protograph representation, edge-spreading construction, termination

➔ Iterative decoding thresholds, threshold saturation, minimum distance

## ● Practical Considerations

➔ Window decoding, performance, latency, and complexity comparisons to LDPC block codes, rate-compatibility, implementation aspects

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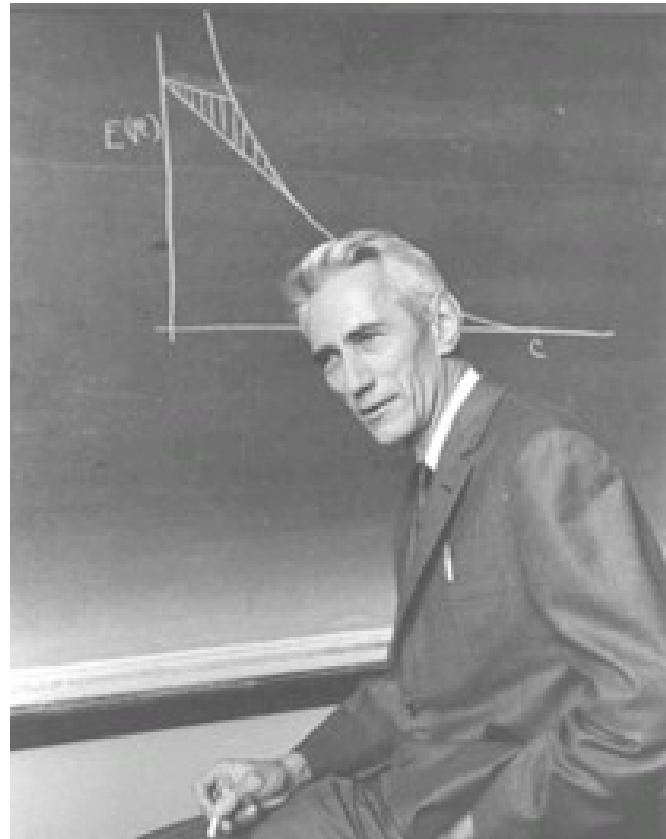
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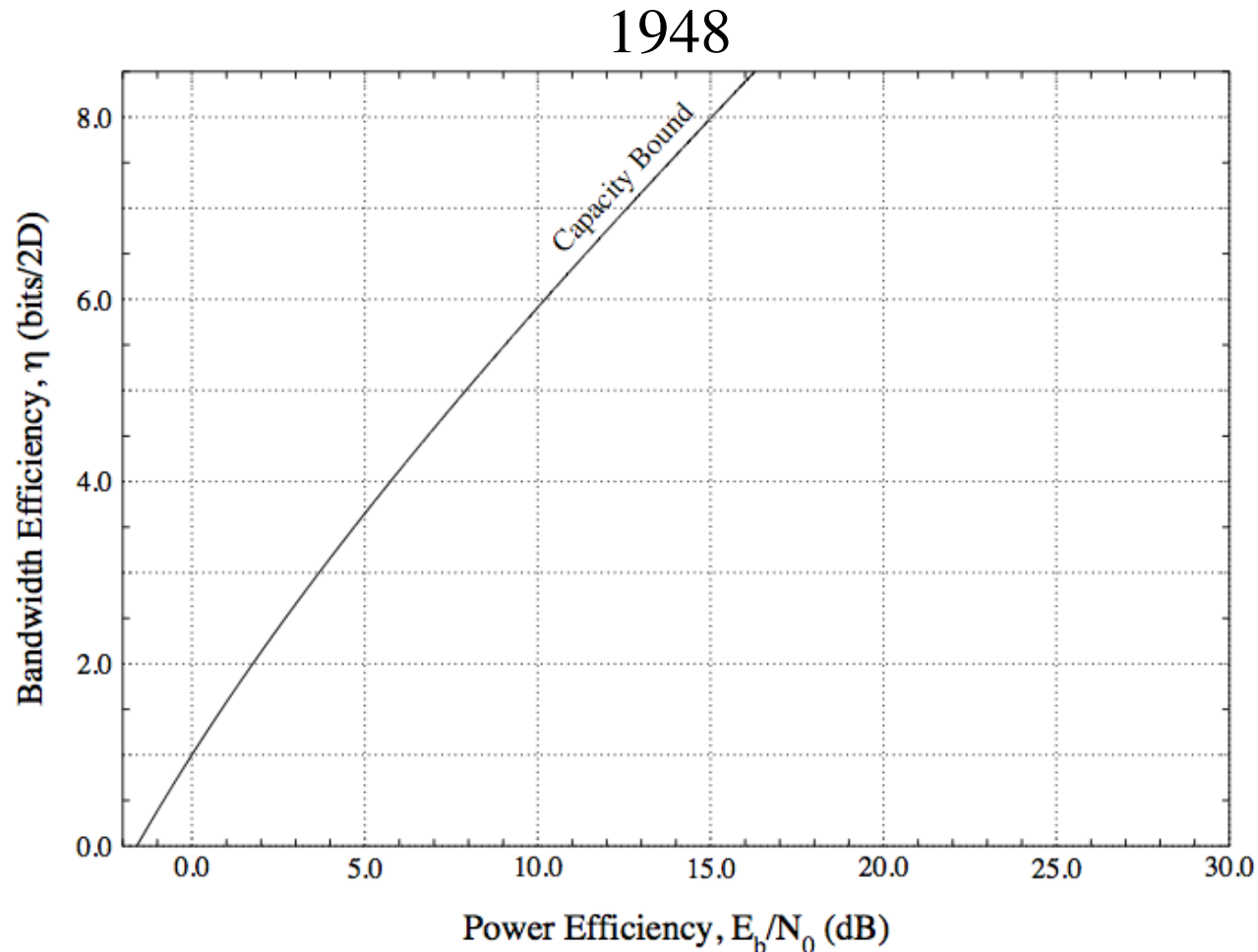
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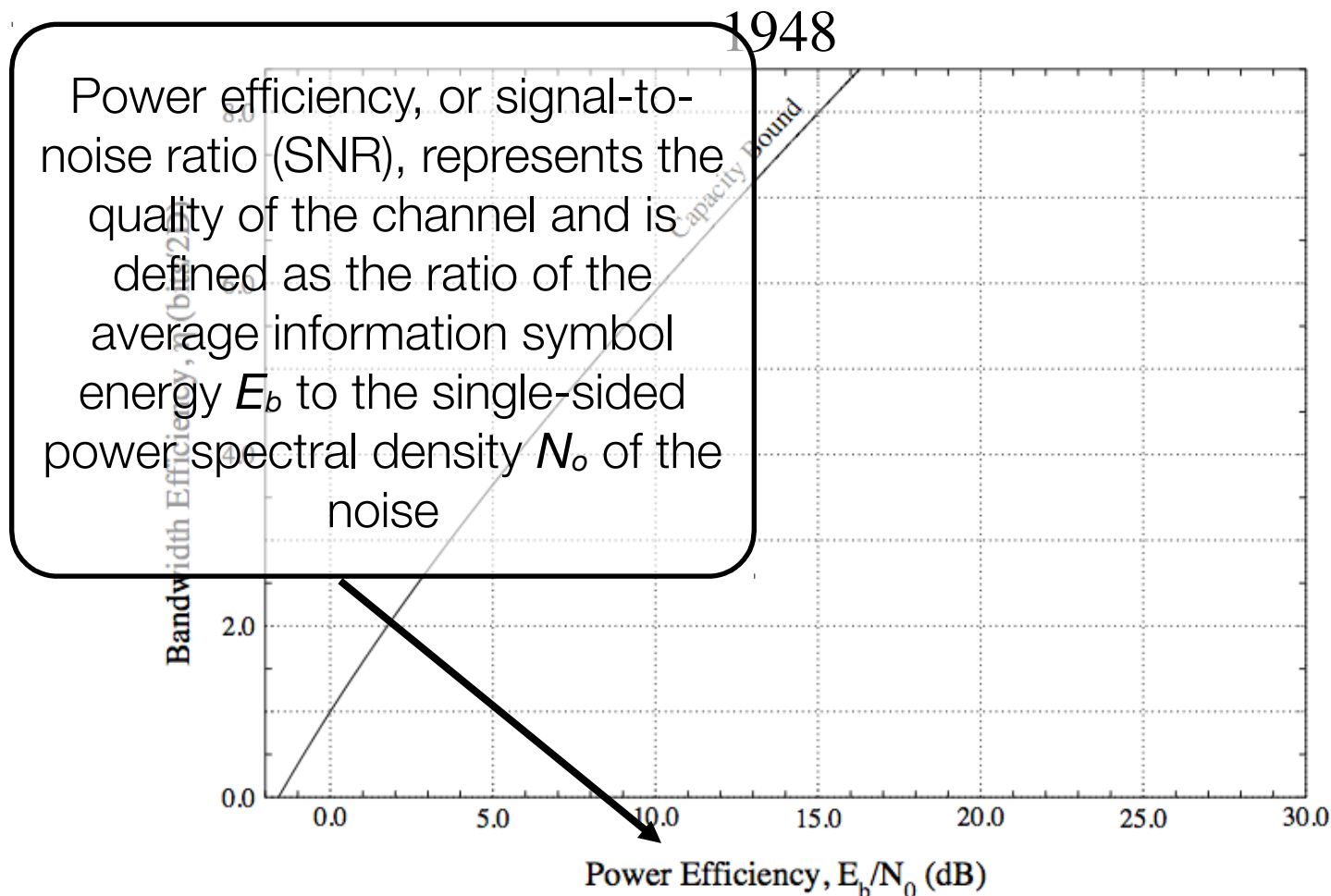


Claude Elwood Shannon  
Apr. 30, 1916 – Feb. 24, 2001  
Father of **Information Theory**

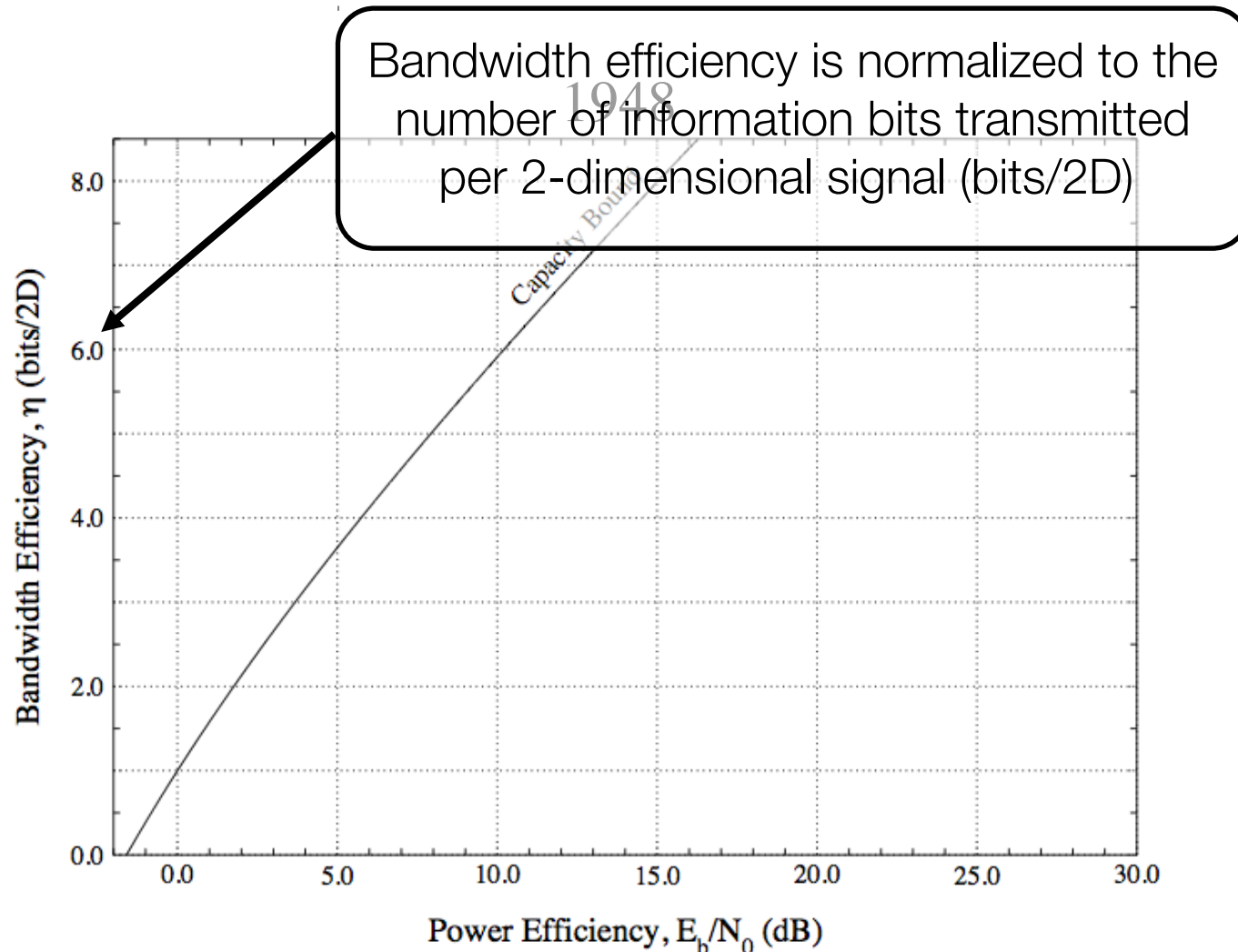
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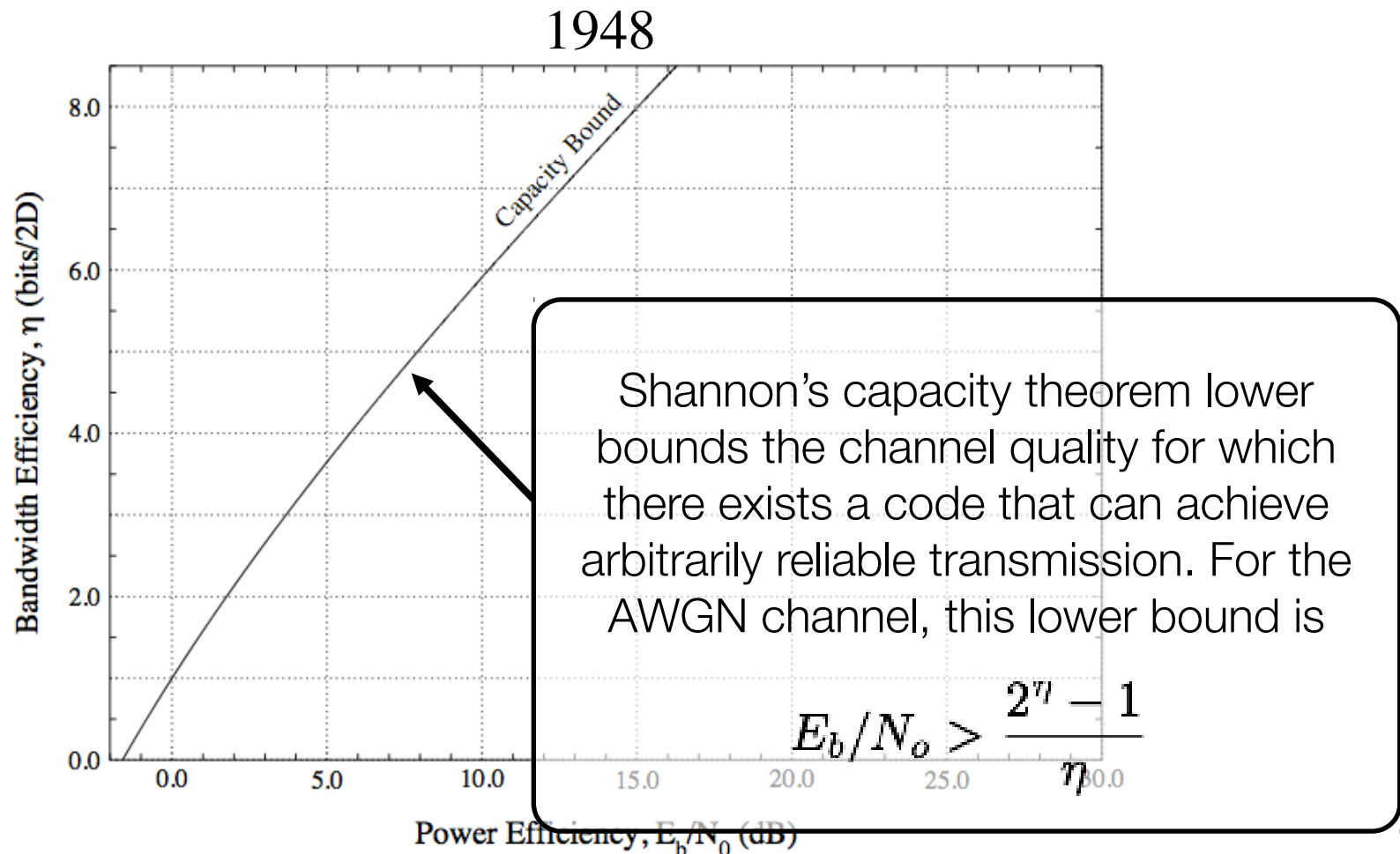
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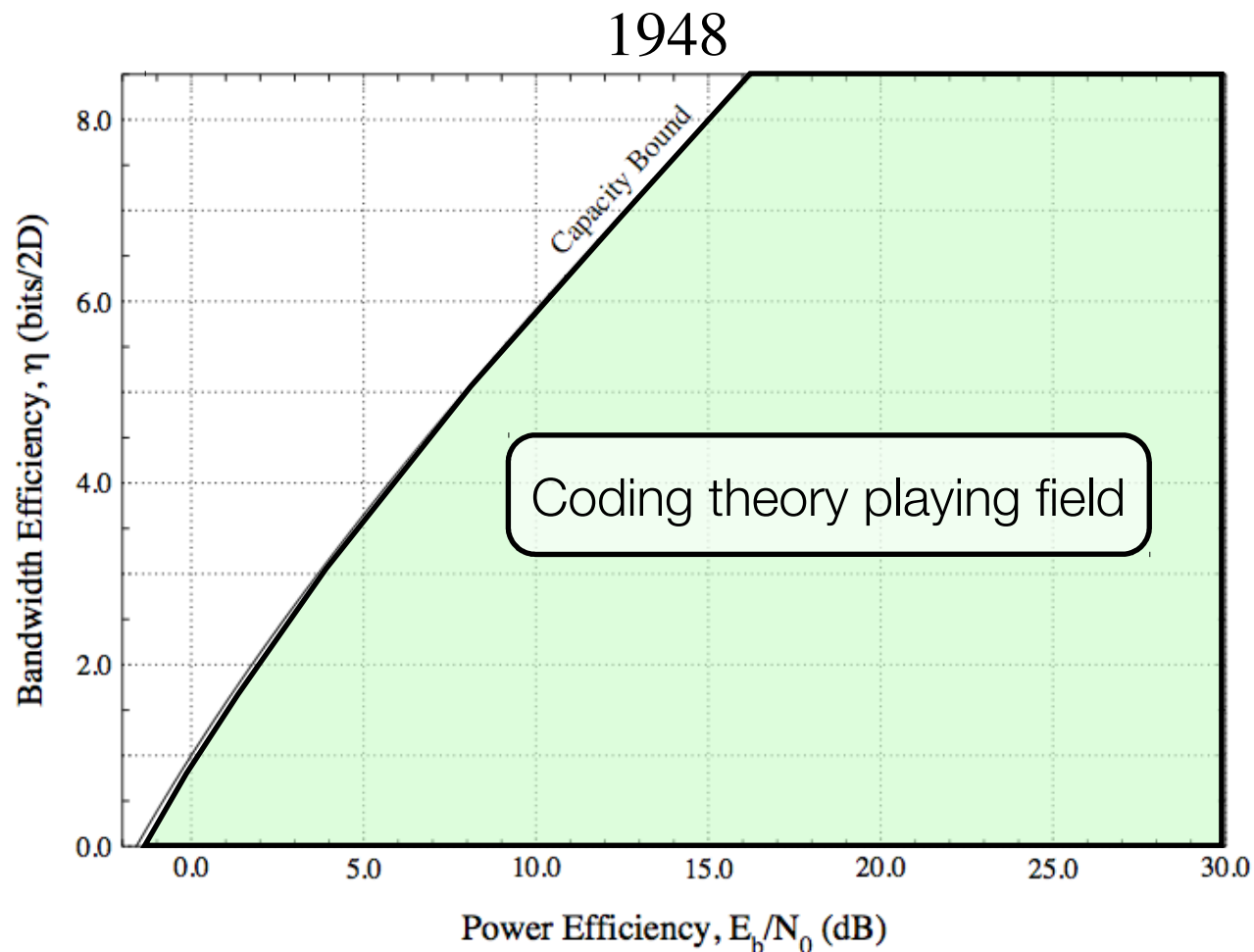


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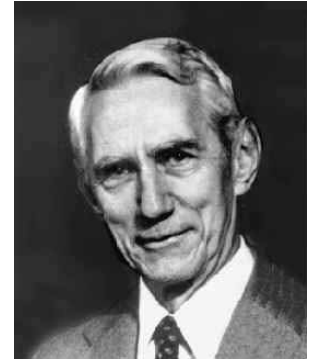


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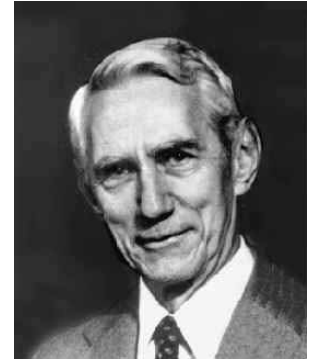


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- Shannon showed that **random codes** with large block length can **achieve capacity**, but...



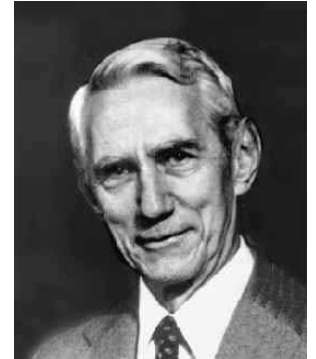
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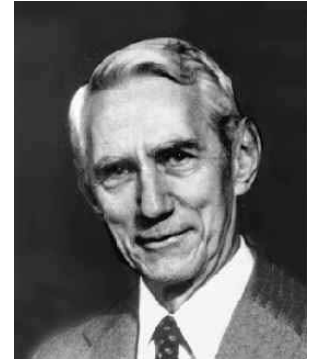
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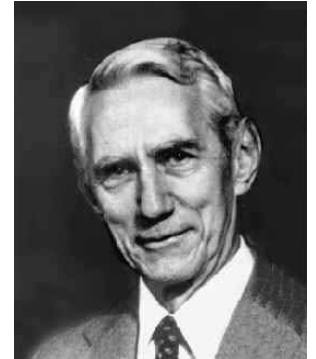
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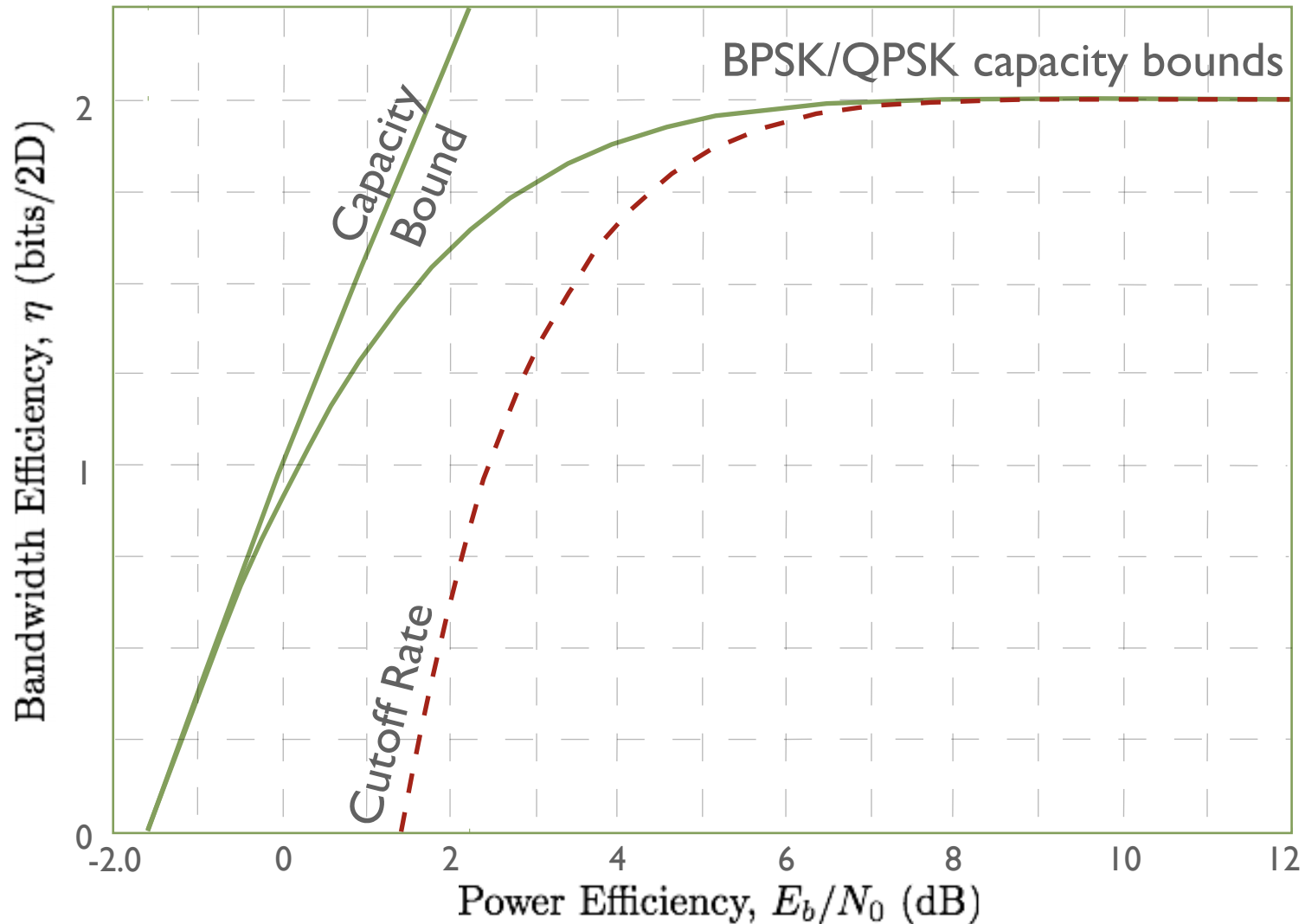
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**Solution:** Construct random-like codes with just enough structure to allow efficient decoding

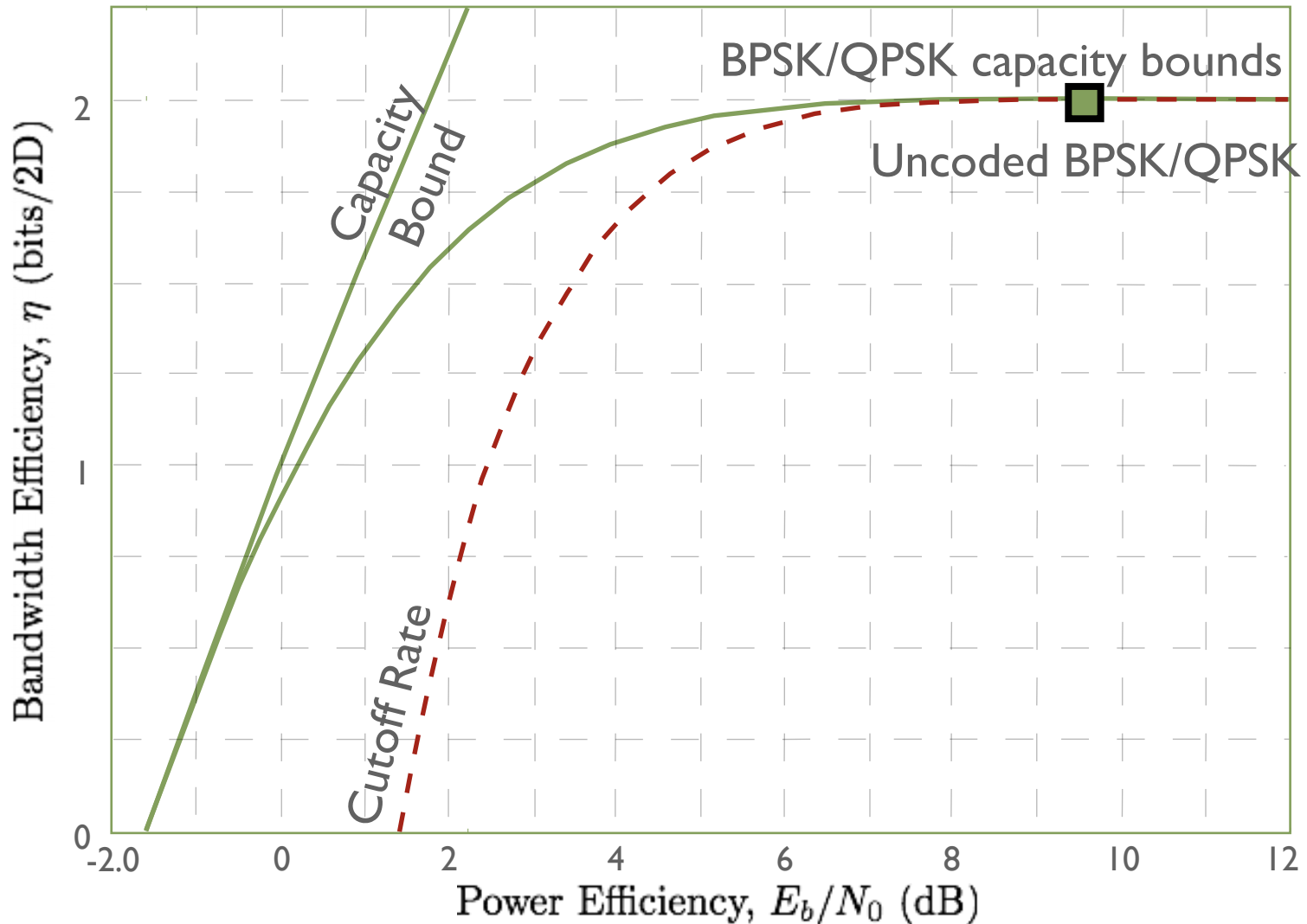
➔ Modern Coding Theory



# LDPC Codes: motivation (for a target BER $10^{-5}$ )

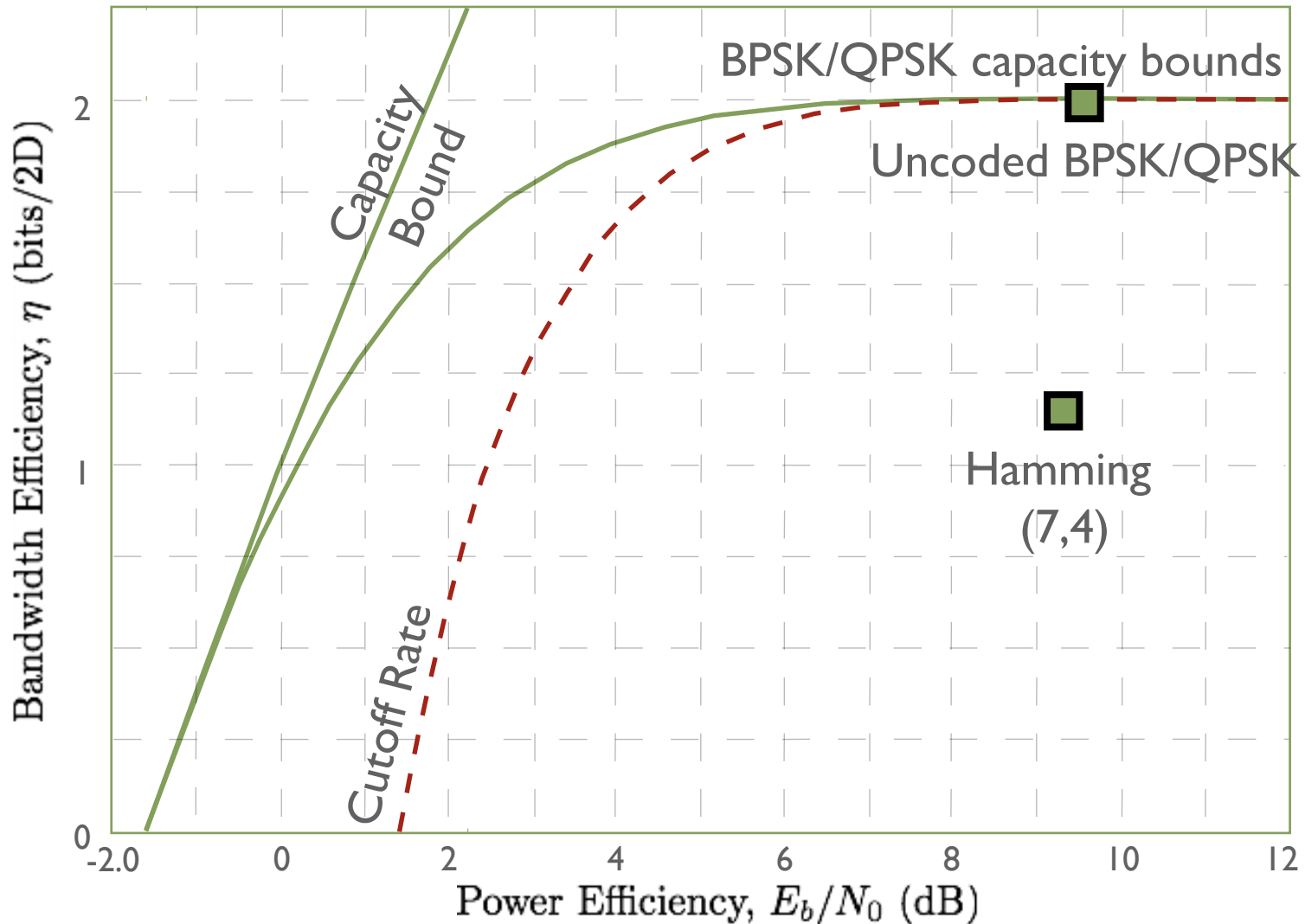


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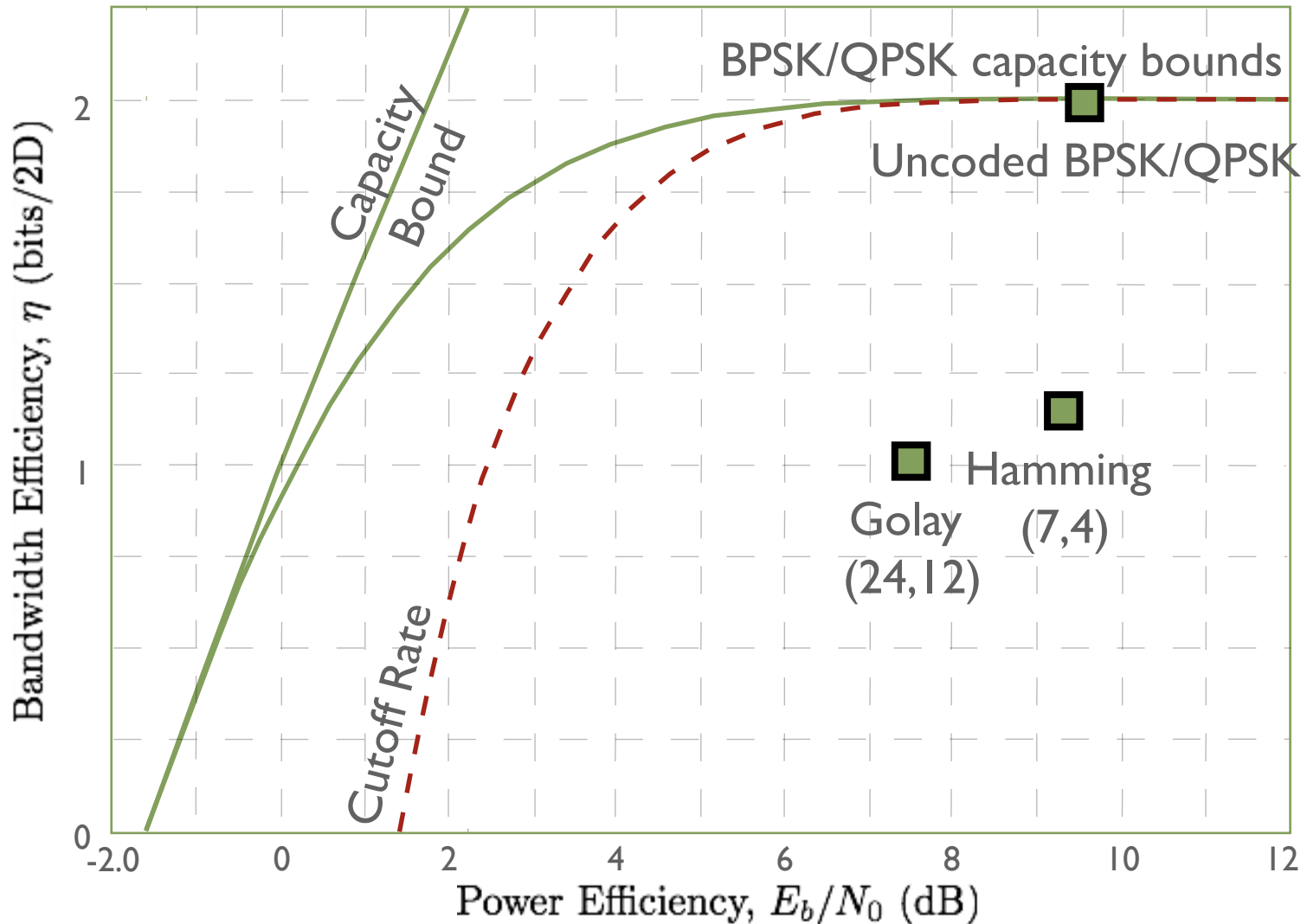




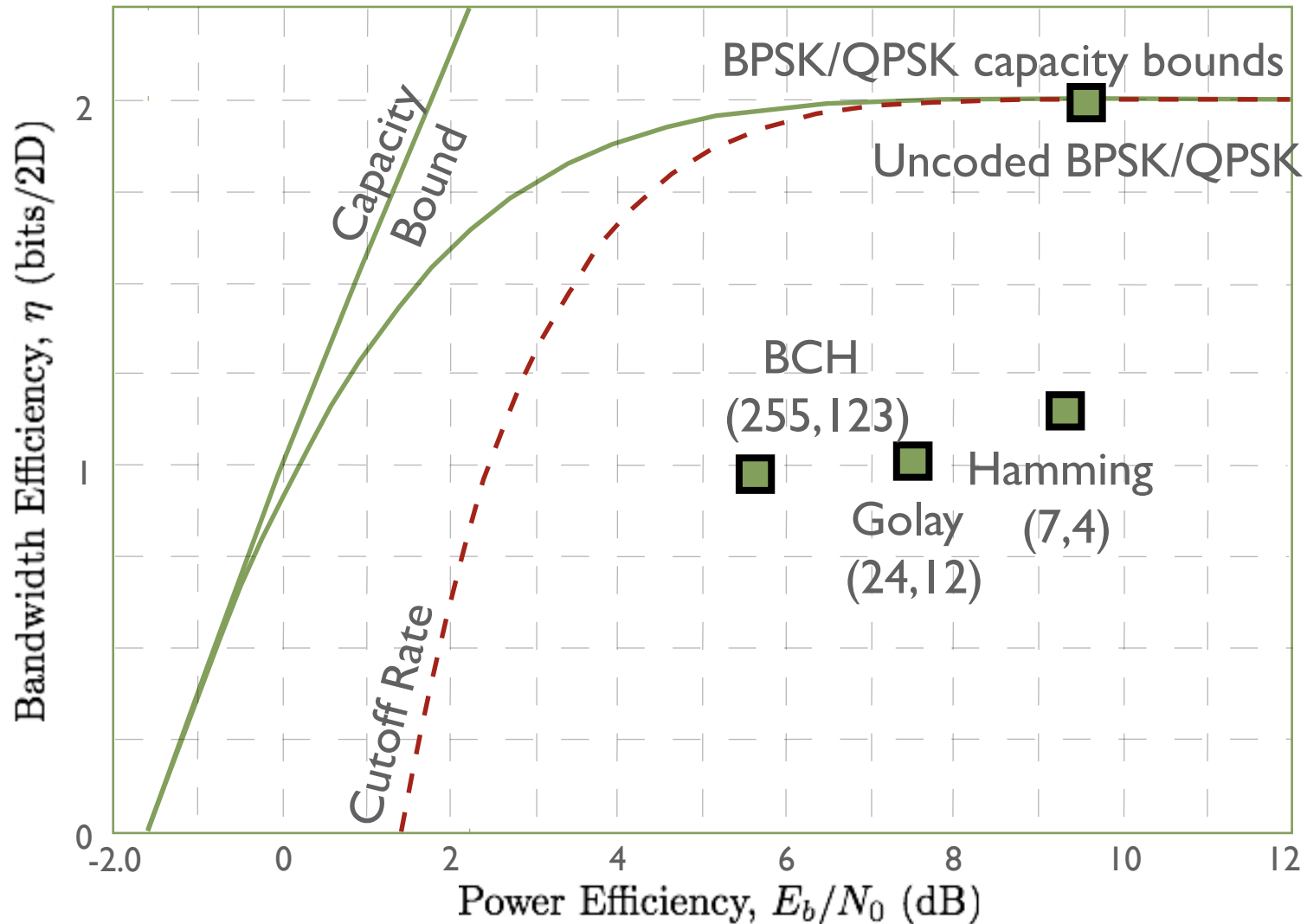
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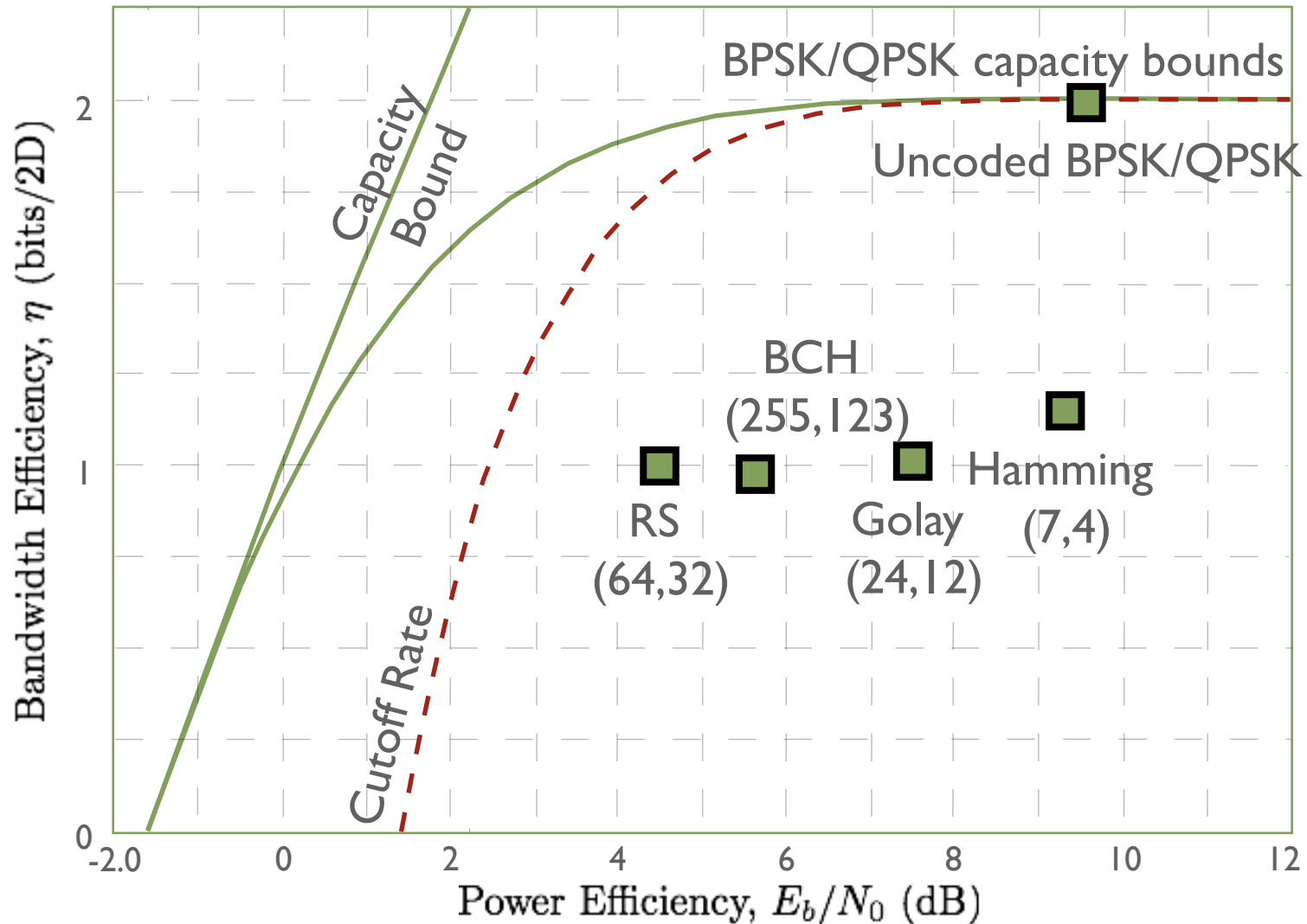
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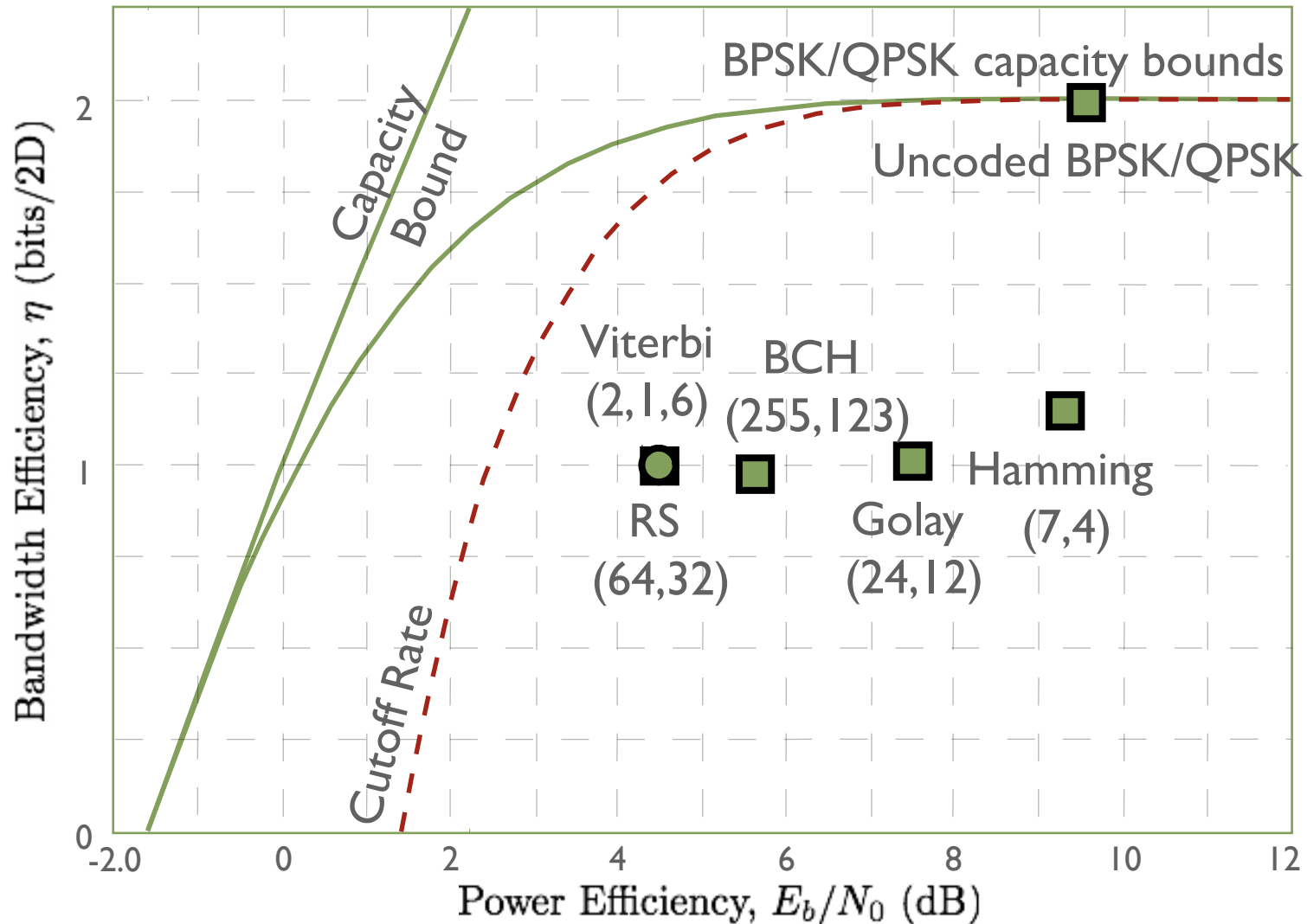
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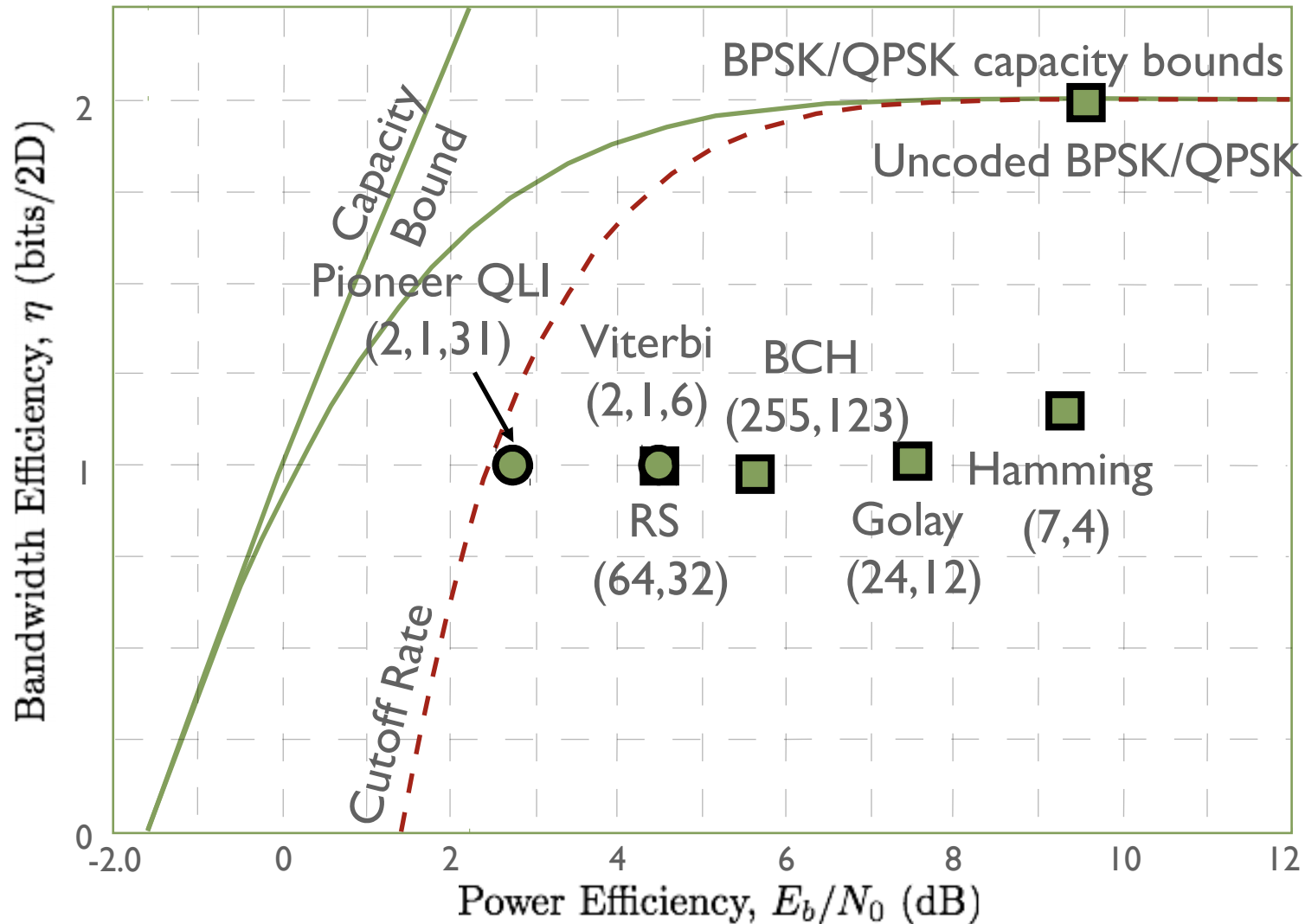
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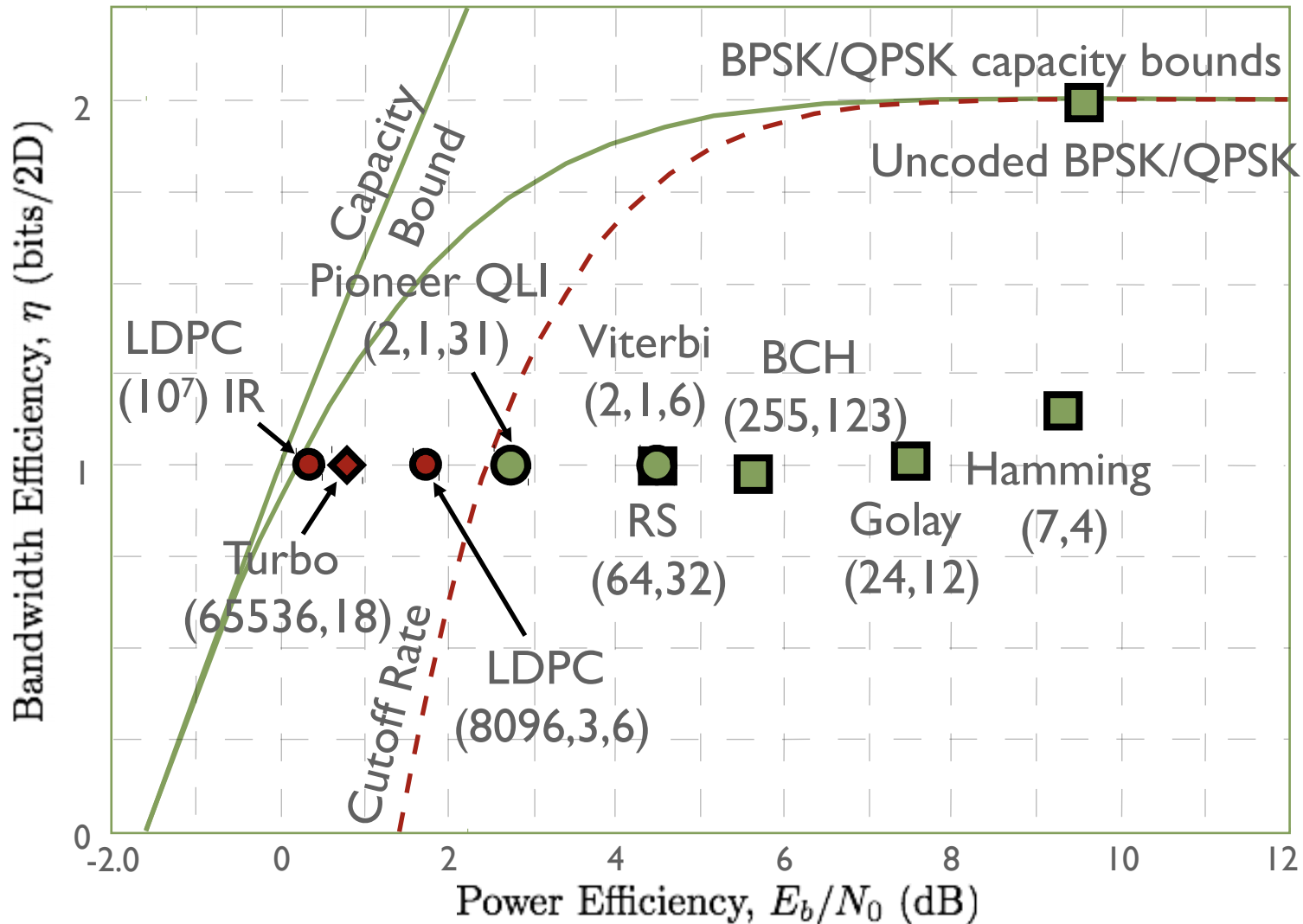
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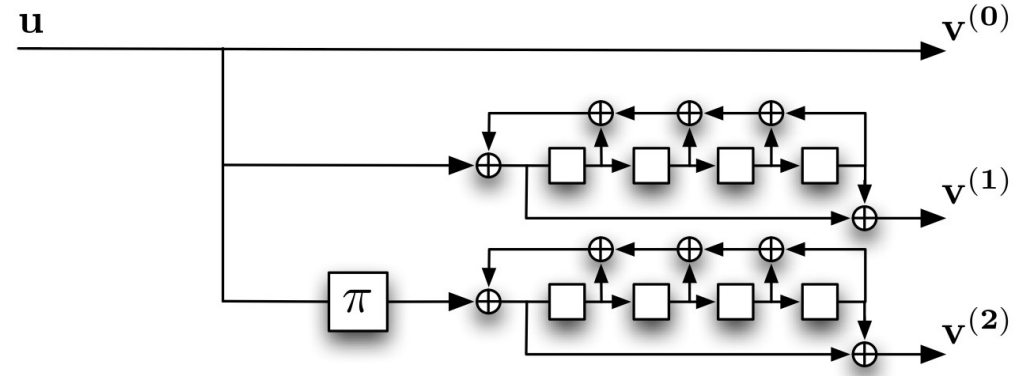
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# Random-like codes (2000s - today)

- Turbo codes use a long pseudorandom interleaver

➔ 3G and 4G telephony standards HSPA, EV-DO, LTE, satellite DVB-RCS, Mars Reconnaissance Rover, WiMAX, and so on.

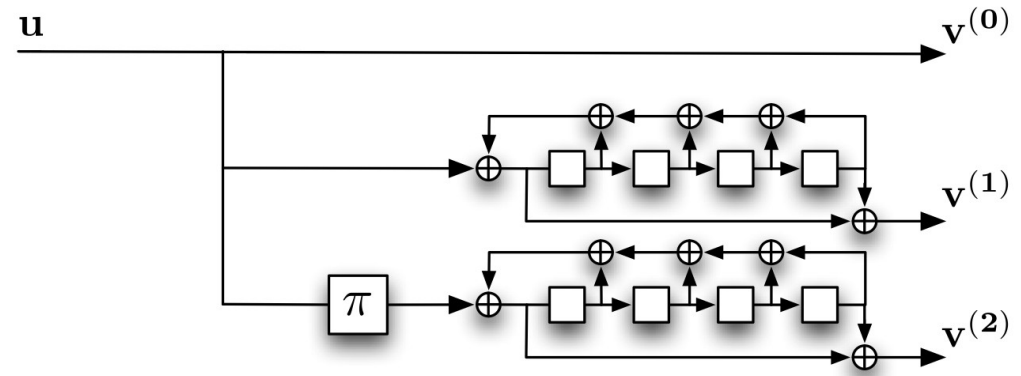




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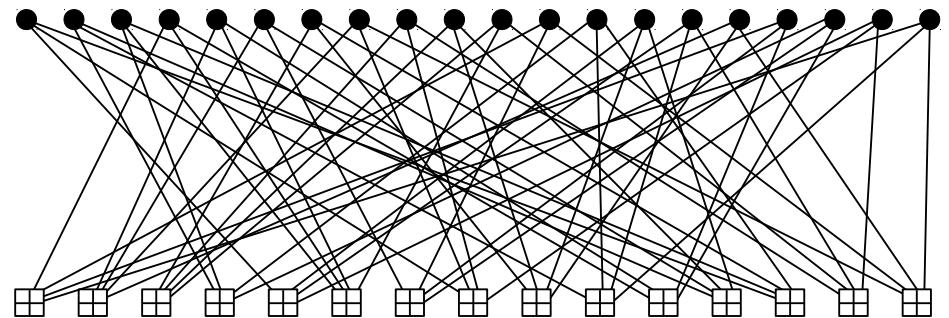
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- Low-density parity-check (LDPC) codes are defined on a large sparse graph

➔ DVB-S2, ITU-T G.hn standard (data networking over power lines, phone lines, and coaxial cables), 10GBase-T Ethernet, Wi-Fi standards 802.11, and so on.



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- **LDPC Block Codes**
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- Recall, an  $(n, k)$  binary linear code  $C$  is a  $k$ -dimensional subspace of  $\{0, 1\}^n$ 
  - ➔  $G$  is a generator matrix for  $C$  if its rows span  $C$ . ( $G$  is an  $l \times n$  matrix where  $l \geq k$ .)
  - ➔  $H$  is a parity-check matrix for  $C$  if its rows span  $C^\perp$ , i.e.,  $v \in C$  iff  $vH^T = 0$ . ( $H$  is an  $m \times n$  matrix where  $m \geq n - k$ .)

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**Definition:** A **low-density parity-check** (LDPC) code is a code for which the parity-check matrix of interest has a low density of ones.

- ➔ LDPC refers to the **representation** of a code, rather than the code itself.
- ➔ “low” is a vague term. (Complexity of decoding increases with the density of ones.)

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**(2) Definition:** An **irregular**  $(n,k)$  LDPC code is one in which the row and/or column weights of the parity-check matrix are not constant.

## Definition by parity-check matrix: [Gallager, '62]

$$H = \begin{bmatrix} 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 1 \\ 0 & 0 & 0 & 0 & 1 & 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ \hline 0 & 0 & 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ \hline 0 & 0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \end{bmatrix}$$

15 × 20

**Code:**  $\{ \mathbf{v} \mid \mathbf{v} \mathbf{H}^T = \mathbf{0} \}$   
 (null space of a **sparse** parity-check matrix  $\mathbf{H}$ )

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 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 & 1 & 0 \\
 \hline
 0 & 0 & 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 \\
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0	0	0	0	0	0	1	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	1	0	0
0	0	1	1	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	1	1	0	0	0	1	1	0	0
1	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	0	0	0	0	1	0	0	1	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	1
0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0	1	0	0	0	1	0	0	0	1
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- If the row and column weights  $J$  and  $K$  are not constant, then the LDPC code is **irregular** (more later)

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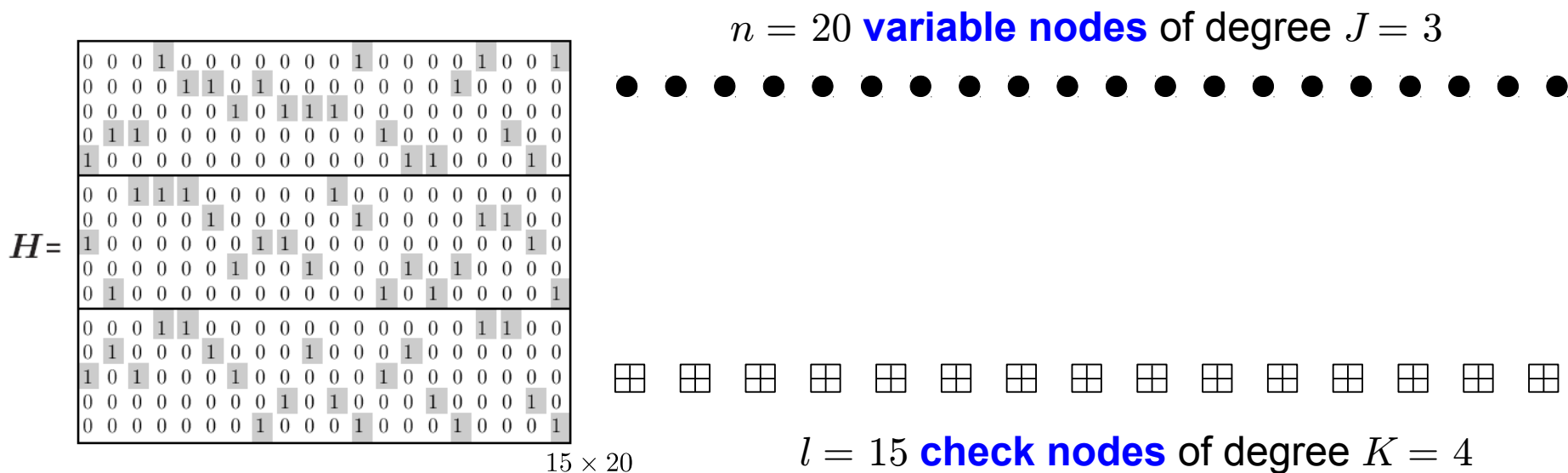
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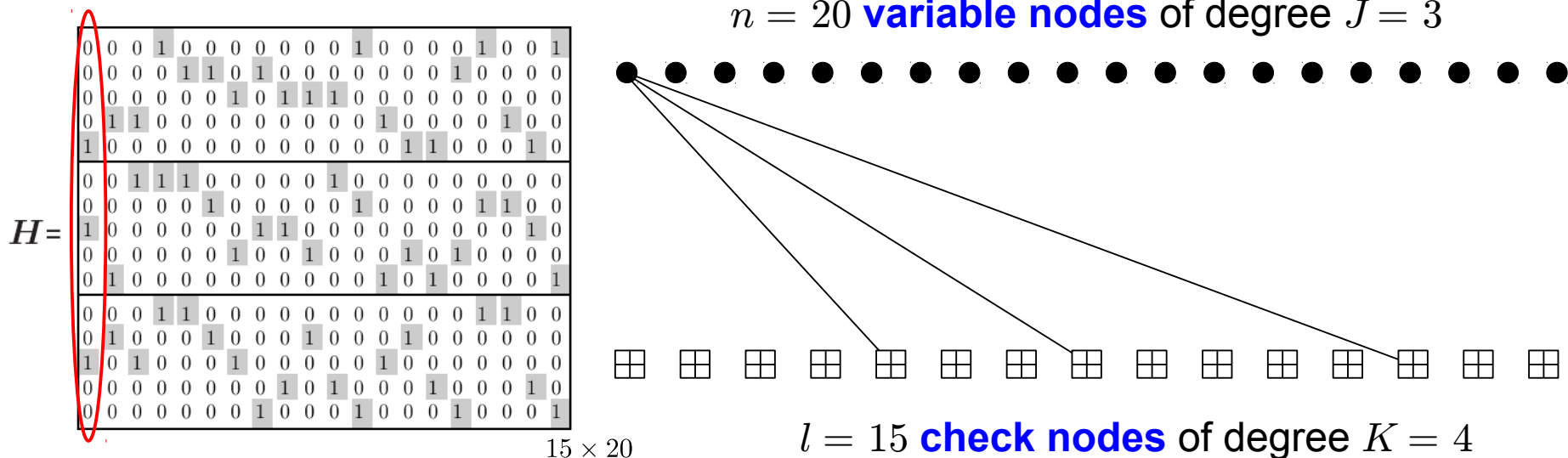
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- In the second class of nodes, there is one node for each of the  $m$  parity-checks (these are called the “check nodes”, “constraint nodes”, or “function nodes”).
- An edge connects a variable node to a check node if and only if that bit is included in that parity-check, i.e., iff  $H$  has a one in the associated position.

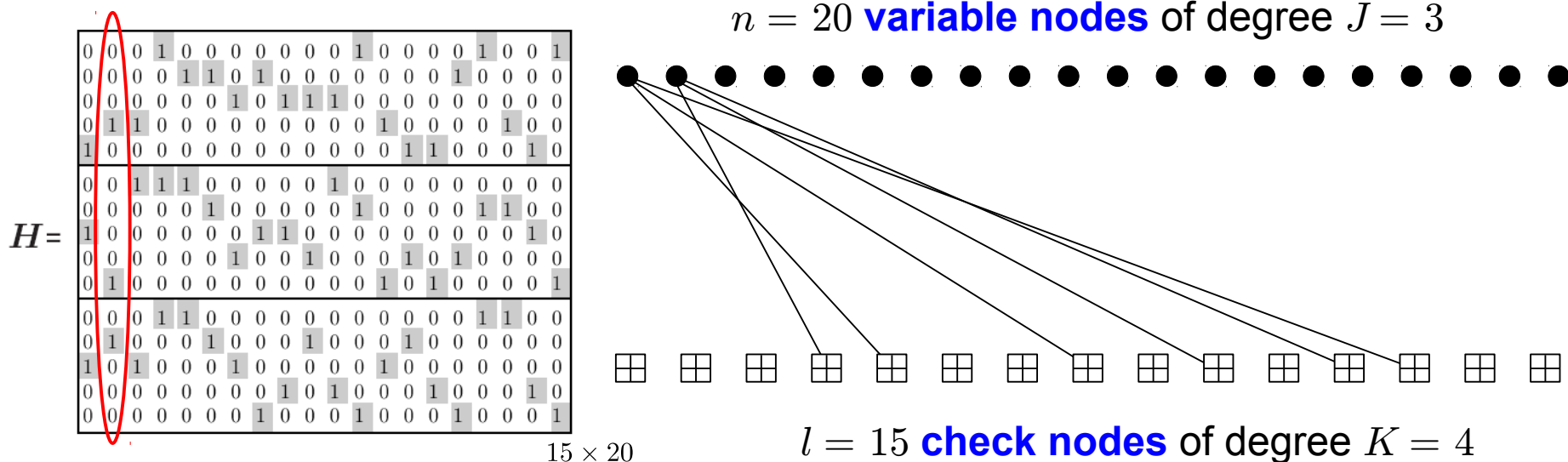
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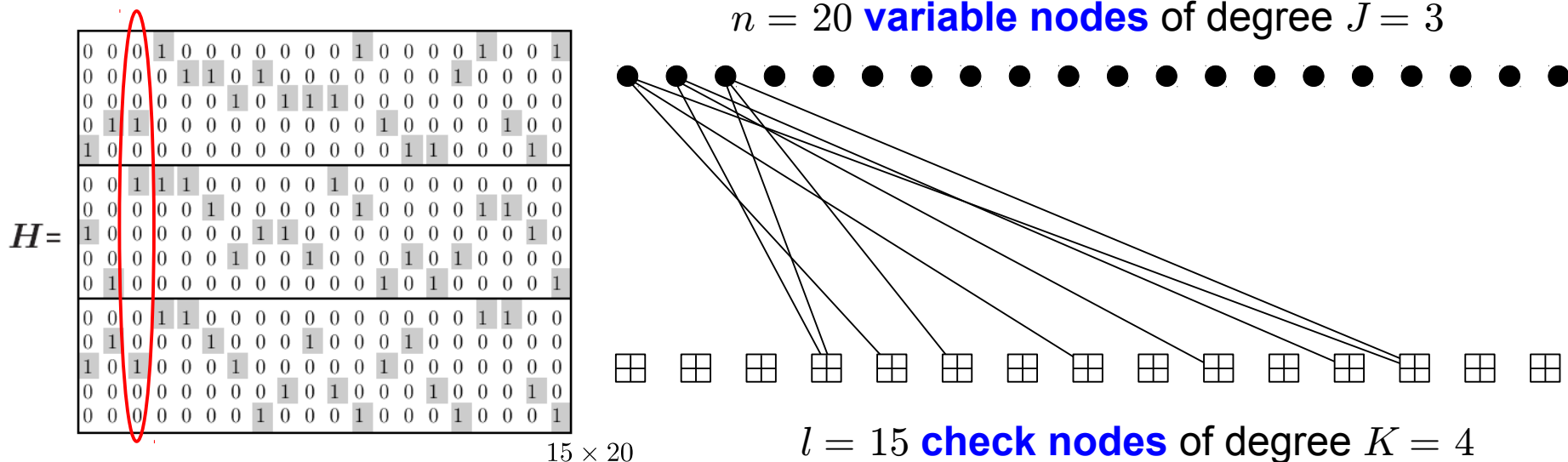
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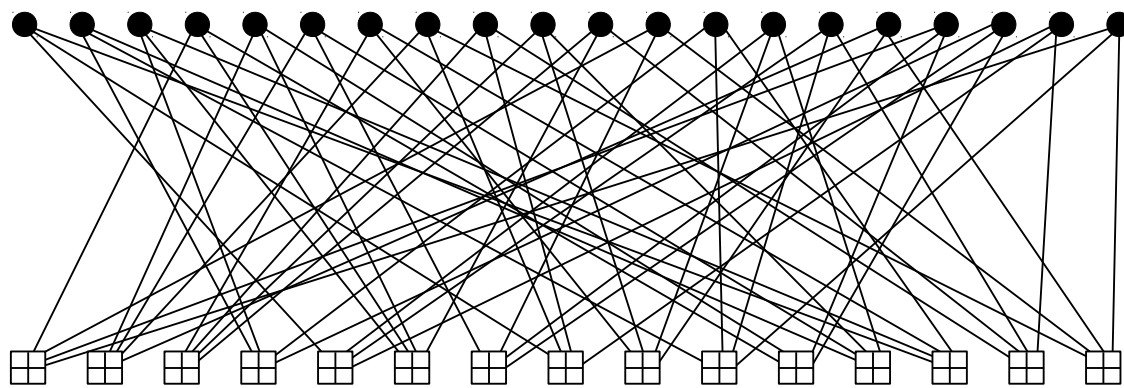
## Representation by bipartite graph: [Tanner, '81]

$$H =$$

0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	1	0	0	1
0	0	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
0	0	0	0	0	0	1	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	1	0	0	0	1
0	0	1	1	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	1	1	0	0	0	0	0
1	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	1	0	0	1	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	1
0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
1	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	0	1	1

15 × 20

$n = 20$  variable nodes of degree  $J = 3$



$l = 15$  check nodes of degree  $K = 4$

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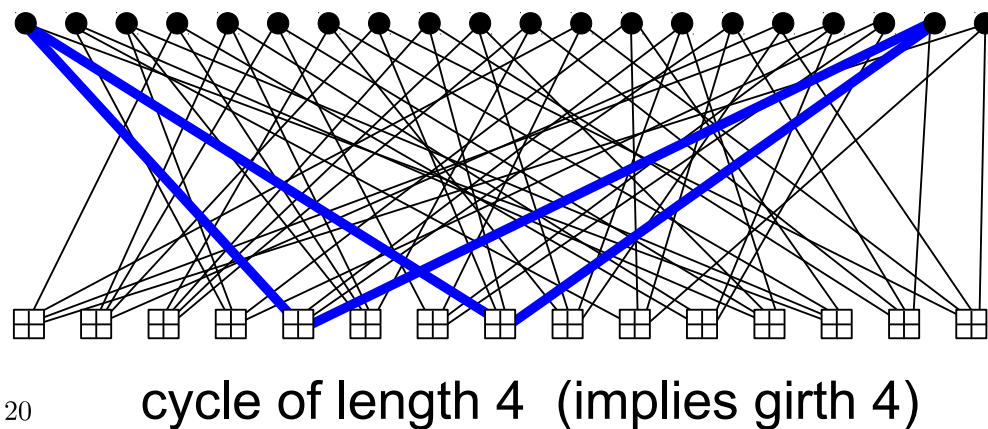
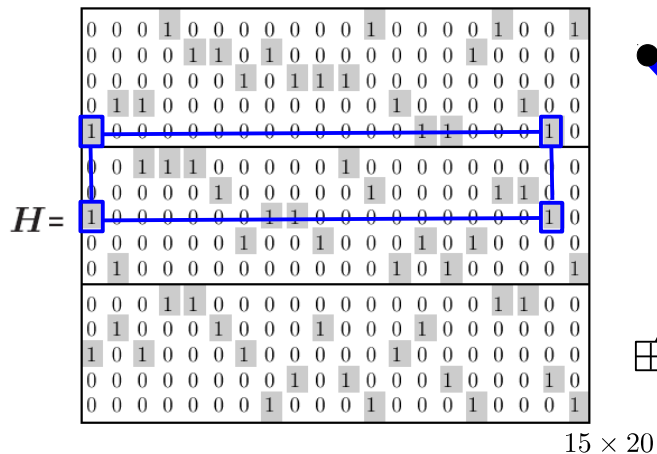
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- ➡ The shortest possible cycle in any graph is 4 (indicated by a “rectangle” of four ones in  $H$ ).
- ➡ Short cycles are usually considered to be bad for message passing decoding (more later).

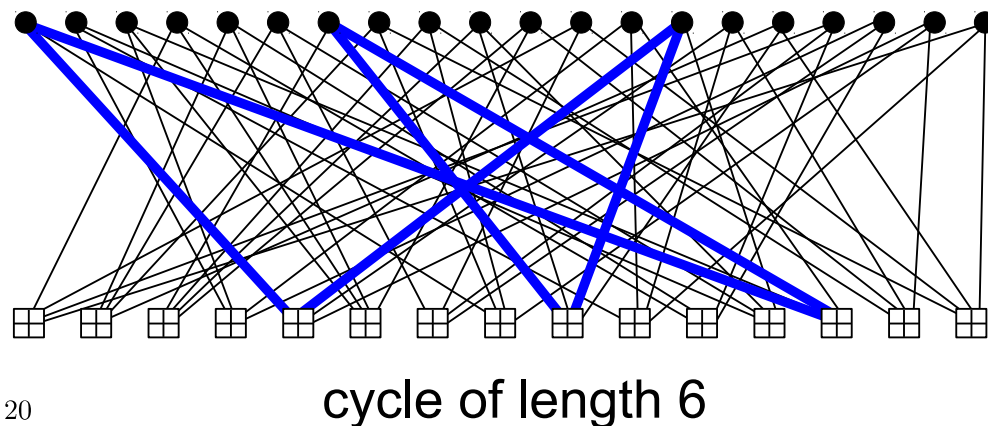
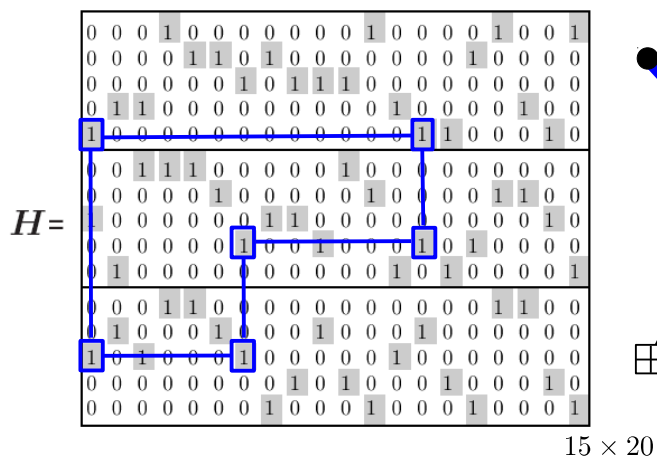
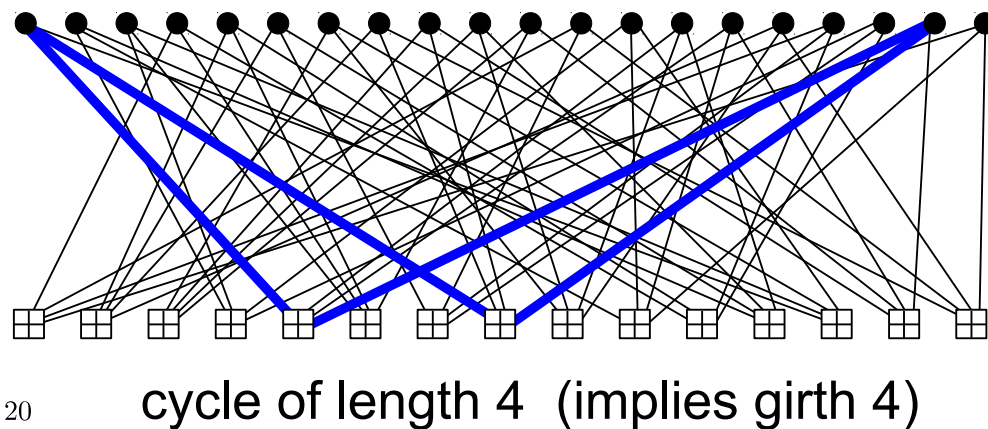
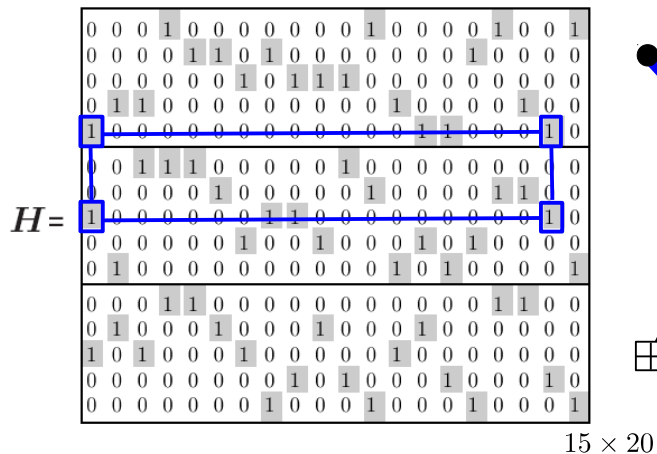
# Cycles in Tanner graphs

- Tanner graphs typically contain **cycles**. The shortest cycle is called the **girth**.



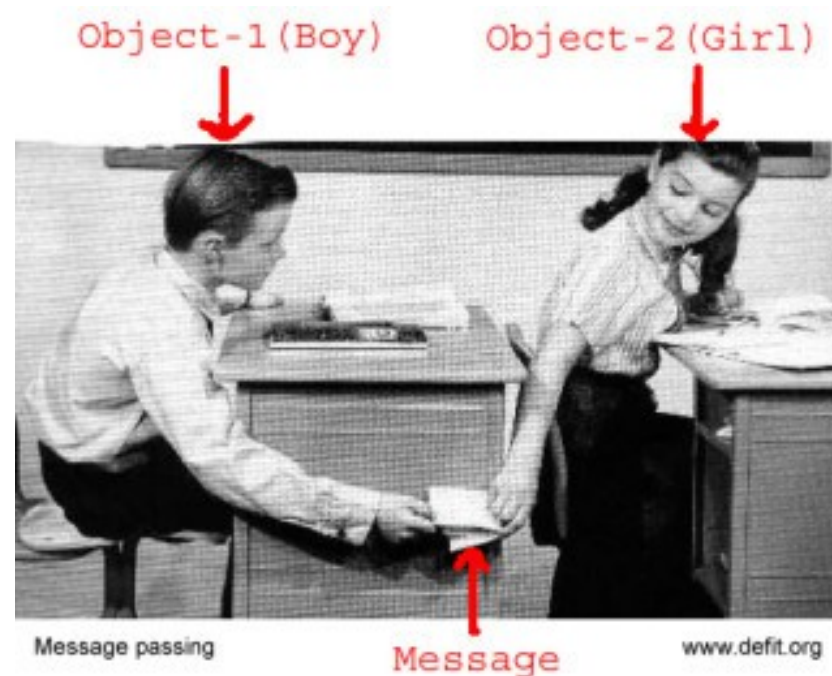
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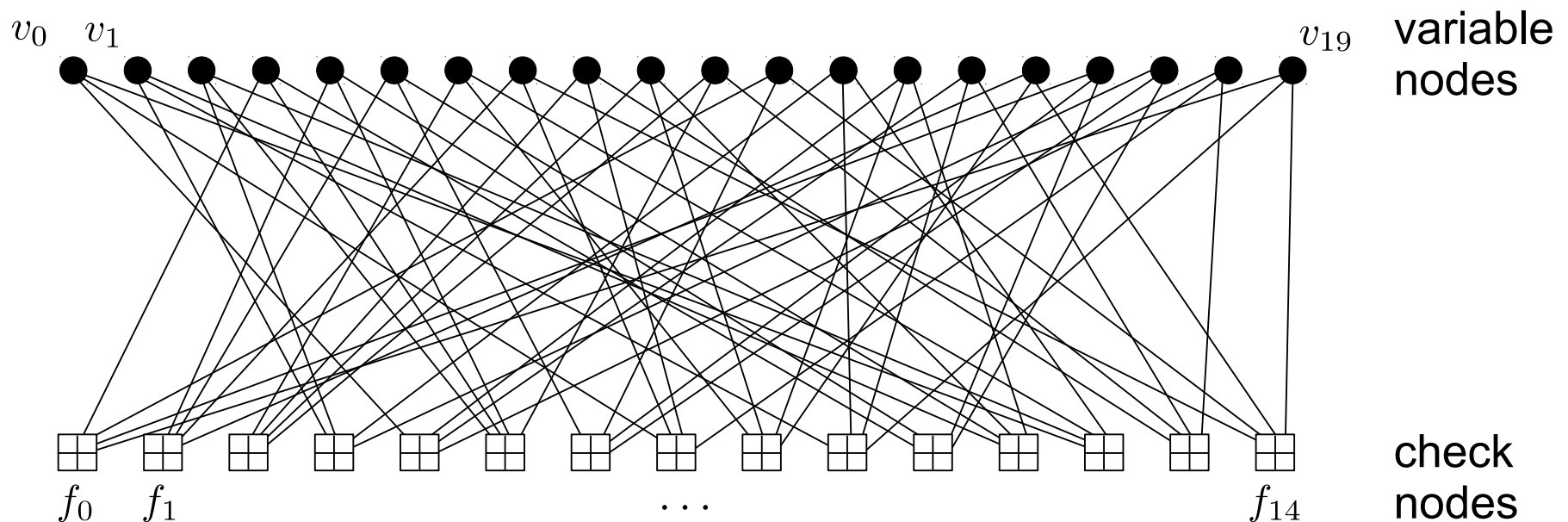
# Message passing decoding

- The class of decoding algorithms used to decode LDPC codes are collectively called **message passing (MP)** algorithms since their operation can be explained by the iterative passing of messages between nodes in the Tanner graph.



# Message Passing Decoding

- Graph-based codes can be decoded **iteratively** with **low-complexity**
- ➔ **Iterative decoding:** exchange of messages in Tanner graph
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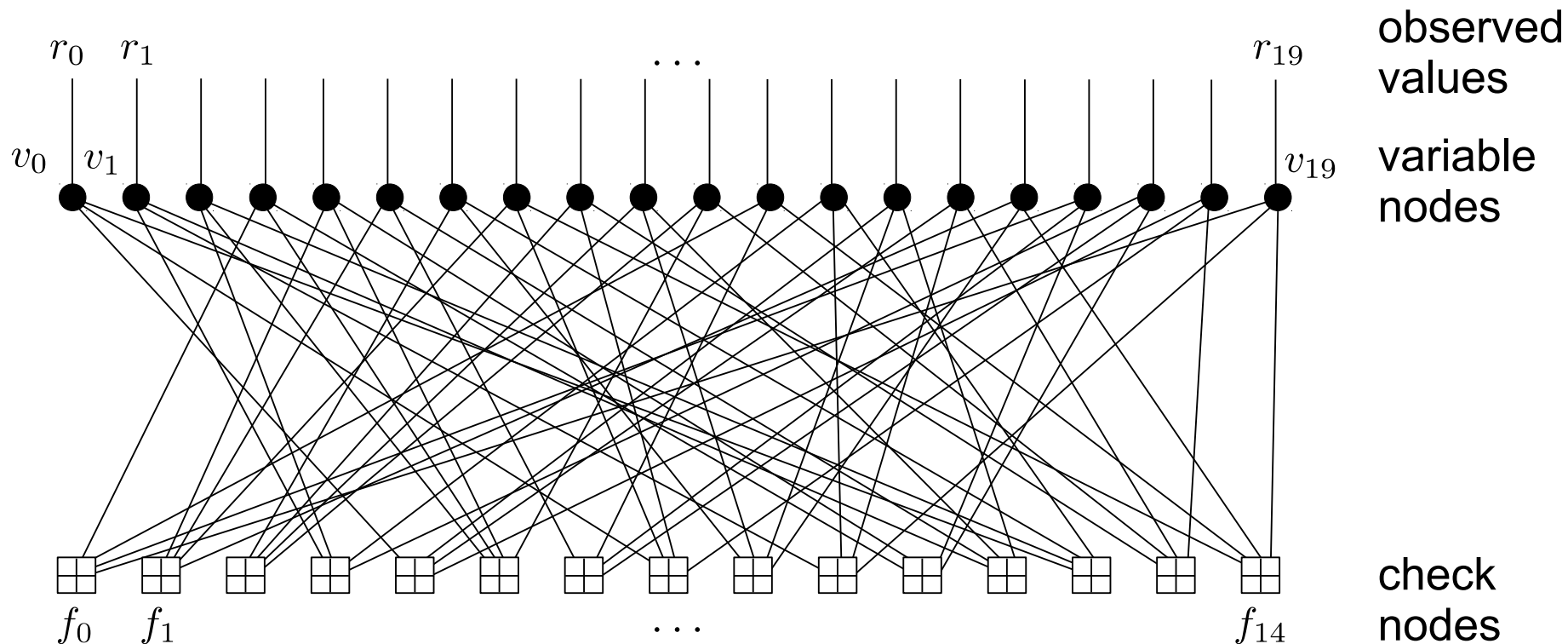


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- In the log domain, the algorithm is also referred to as the **sum product algorithm (SPA)**.

- The **log-likelihood ratio (LLR)** of a binary variable is

$$L(x) = \log \left( \frac{\mathbb{P}(x = 0)}{\mathbb{P}(x = 1)} \right) = \log(\mathbb{P}(x = 0)) - \log(\mathbb{P}(x = 1))$$

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➡  $\text{sgn}(L(x))$  = hard decision

$|L(x)|$  determines the reliability

# Belief Propagation (BP) decoding



**Input:** A priori probabilities on code bits,  $H$ ,  $I_{max}$

**Output:** A posteriori probabilities (APPs) on code bits

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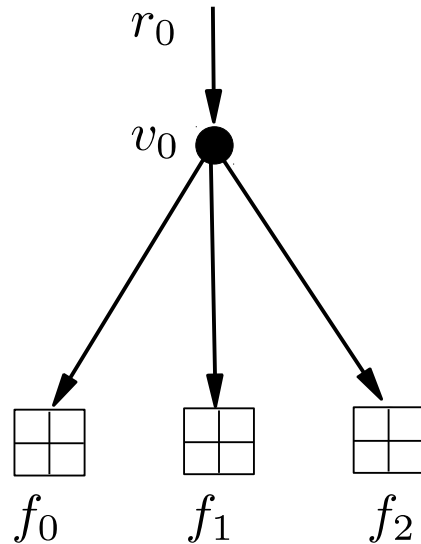
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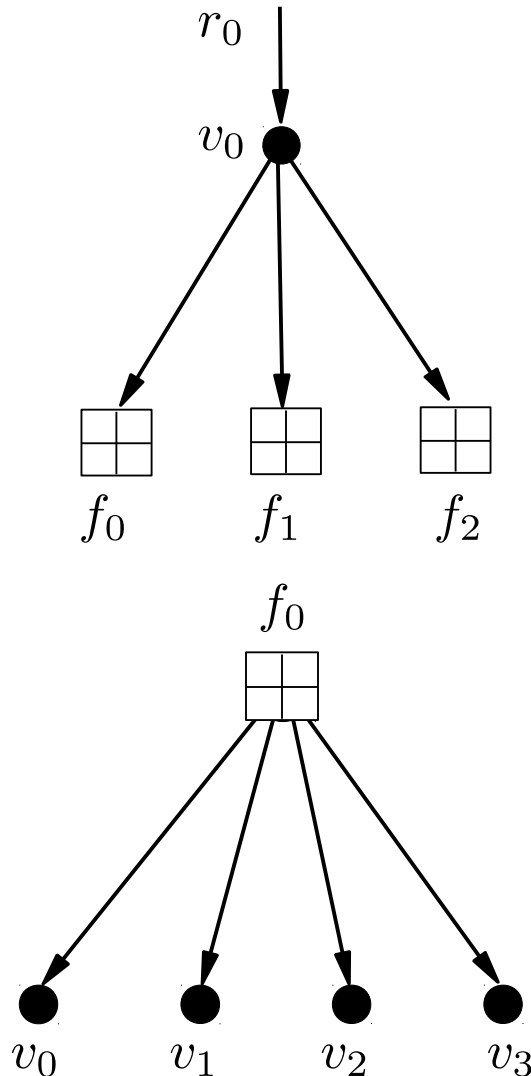
(4) We make hard decisions and terminate if  $vH^T = 0$  or if  $I_{max}$ , the maximum number of iterations, is reached. Otherwise go to (1).

# Belief Propagation (BP) decoding



$p_{ij}$  = message passed from variable node  $v_i$  to check node  $f_j$   
= probability  $v_i$  has a certain value given the observed value  $r_i$  and all the other messages passed to  $v_i$  in the last round from check nodes other than  $f_j$

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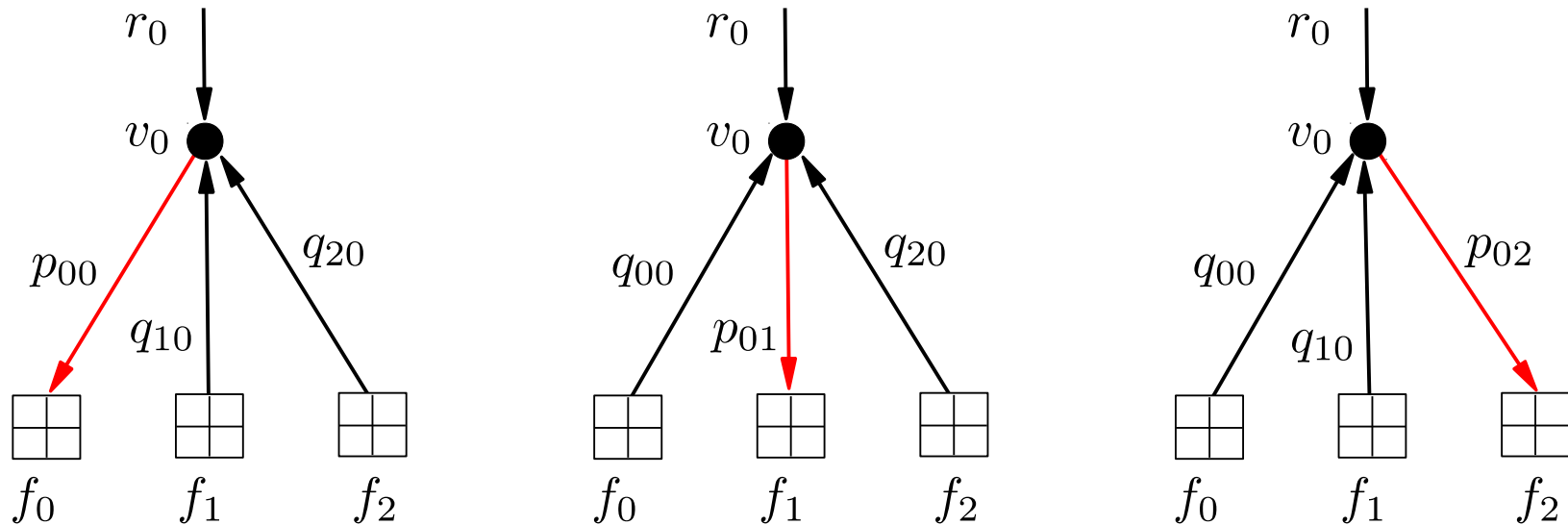


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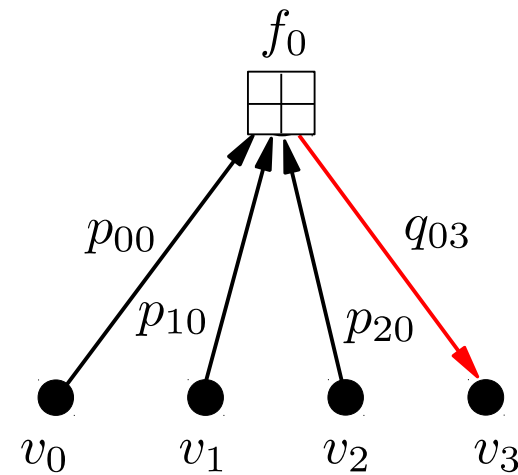
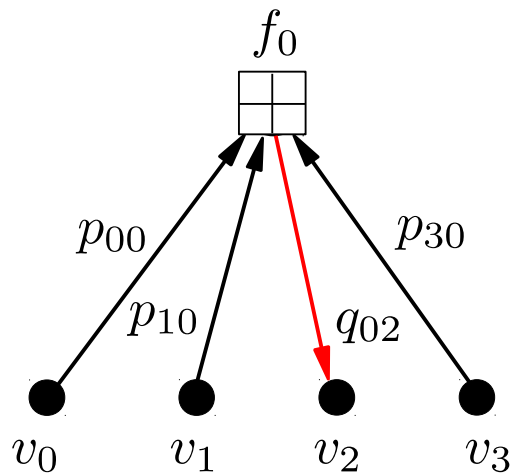
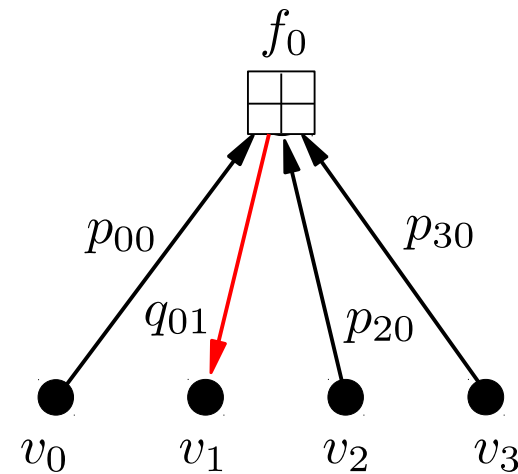
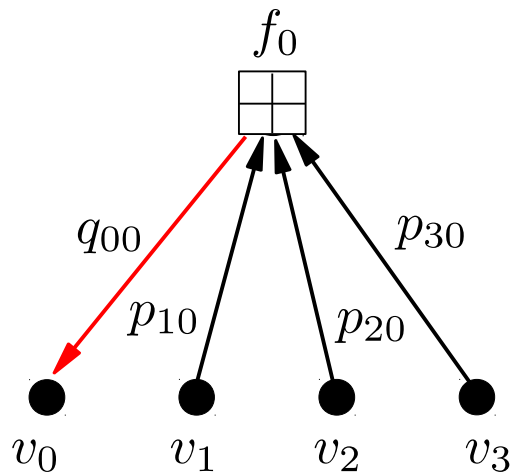
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# Message dependencies

- The message dependencies of the information flow into and out of a variable node look like this:



- The message dependencies of the information flow into and out of a check node look like this:



# Is this globally optimal?

**Problem:** The extrinsic information from a parity-check is **independent of the a priori probability** in the first iteration only **until it is returned back via a cycle** in the graph.

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➡ Cycles cause a correlation in extrinsic information.

BP decoding is a MAP algorithm if no cycles exist in the graph; otherwise it is sub-optimal!

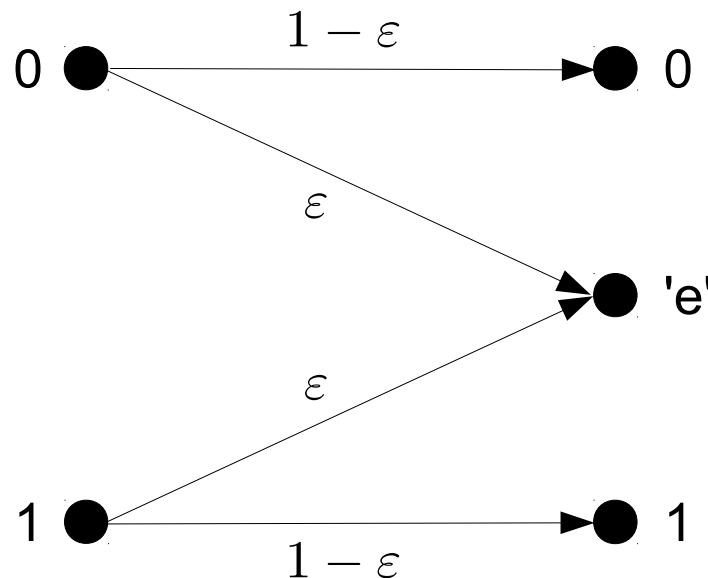
# Message passing on the BEC

**Recall:** On the binary erasure channel (BEC), a bit is either received correctly or erased with probability  $\varepsilon$

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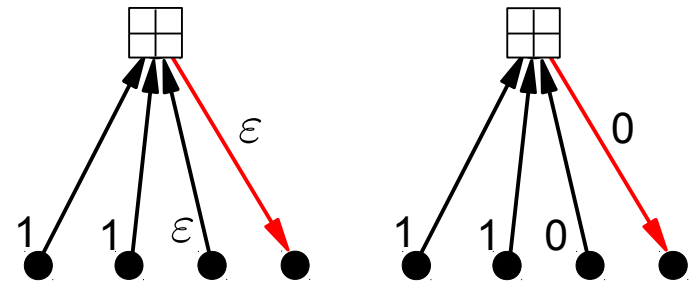
➔ The BEC is **much simpler** to analyze than the BSC or AWGN channels.



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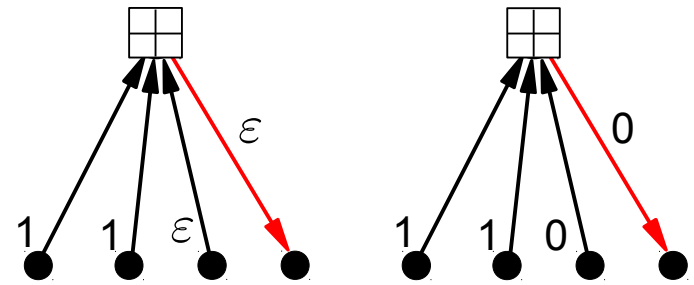
➔ At a **check node**, the outgoing message is an erasure if **any** of the incoming messages are erasures; otherwise the message is the mod-2 sum of the incoming messages.



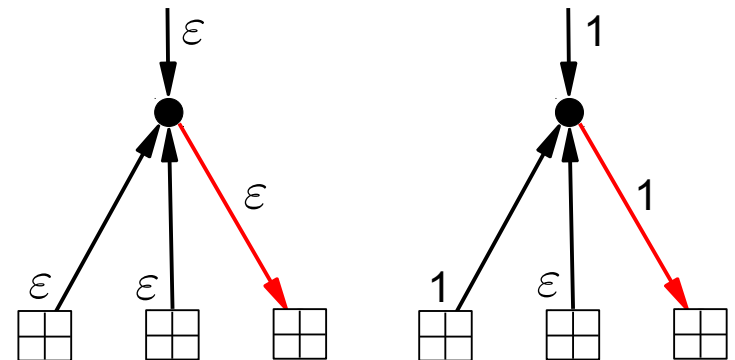
# Belief Propagation - BEC

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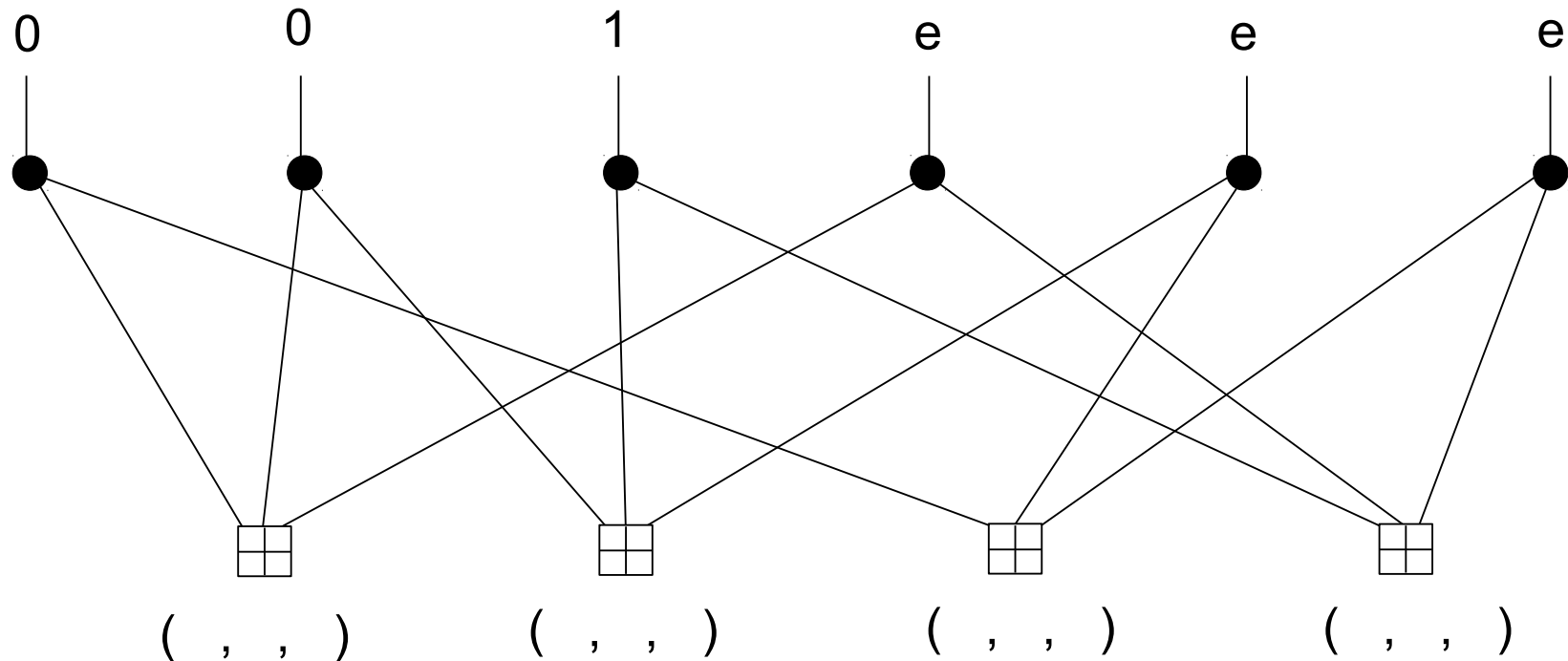
➔ At a **variable node**, the outgoing message is an erasure if **all** of the incoming messages are erasures; otherwise the message is the value of all non-erasures, which must agree.



# Iterative Decoding Example (BEC)

Ex:  $\mathbf{v} = [0\ 0\ 1\ 0\ 1\ 1] \rightarrow \mathbf{r} = [0\ 0\ 1\ e\ e\ e]$

received (corrupted) codeword

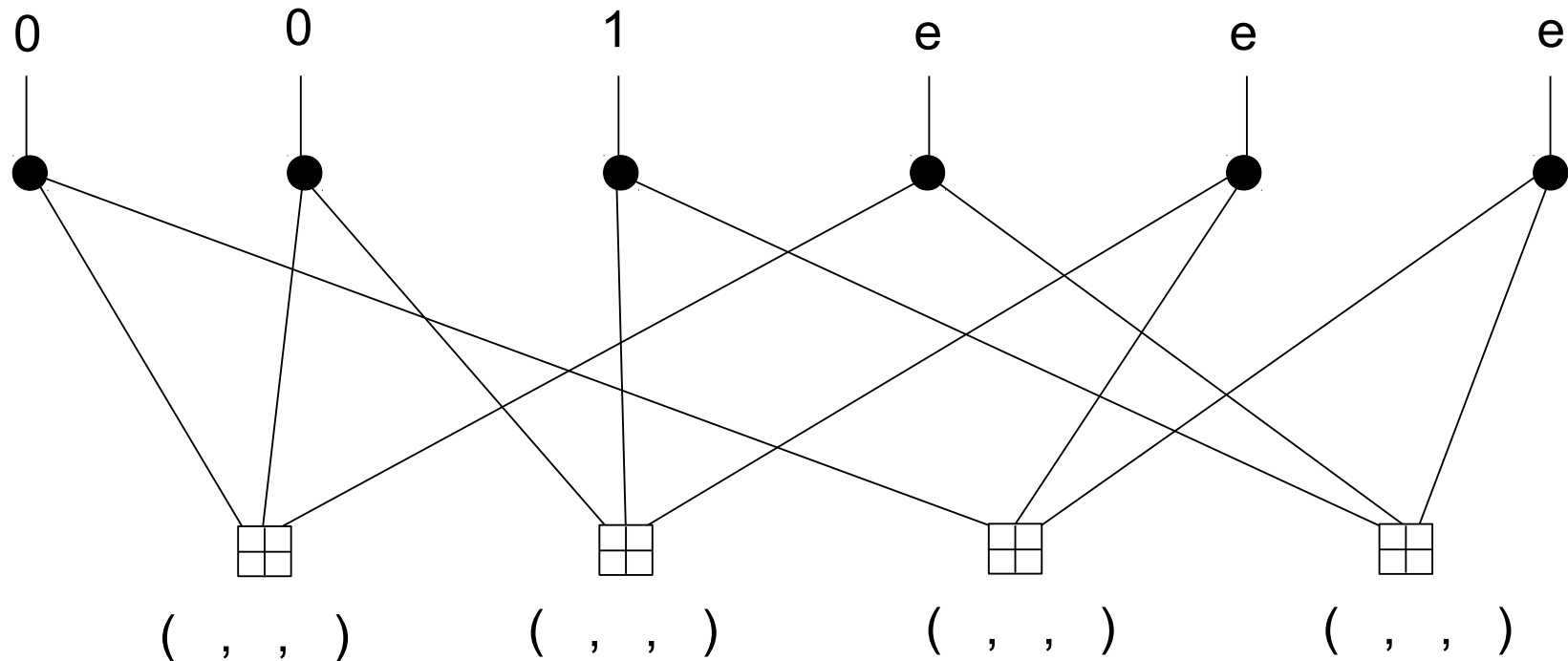


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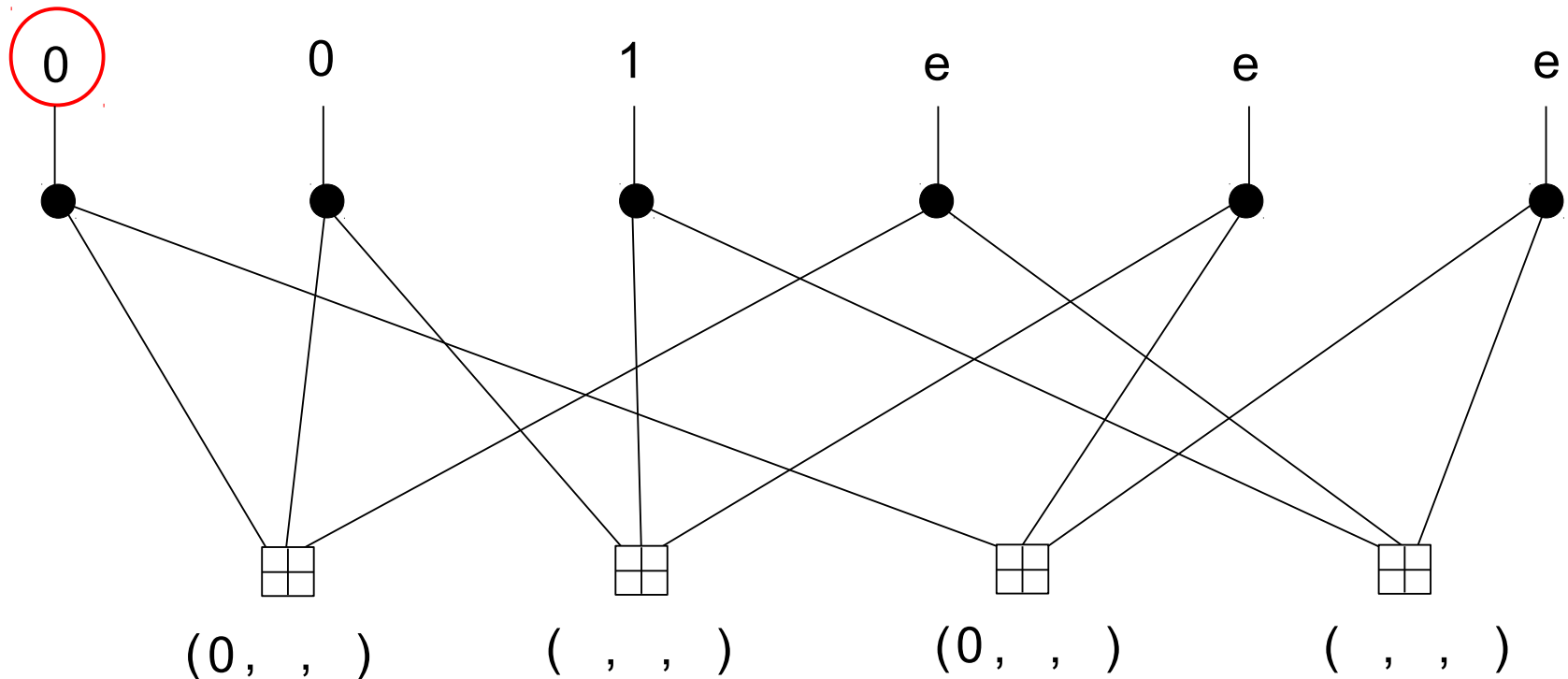


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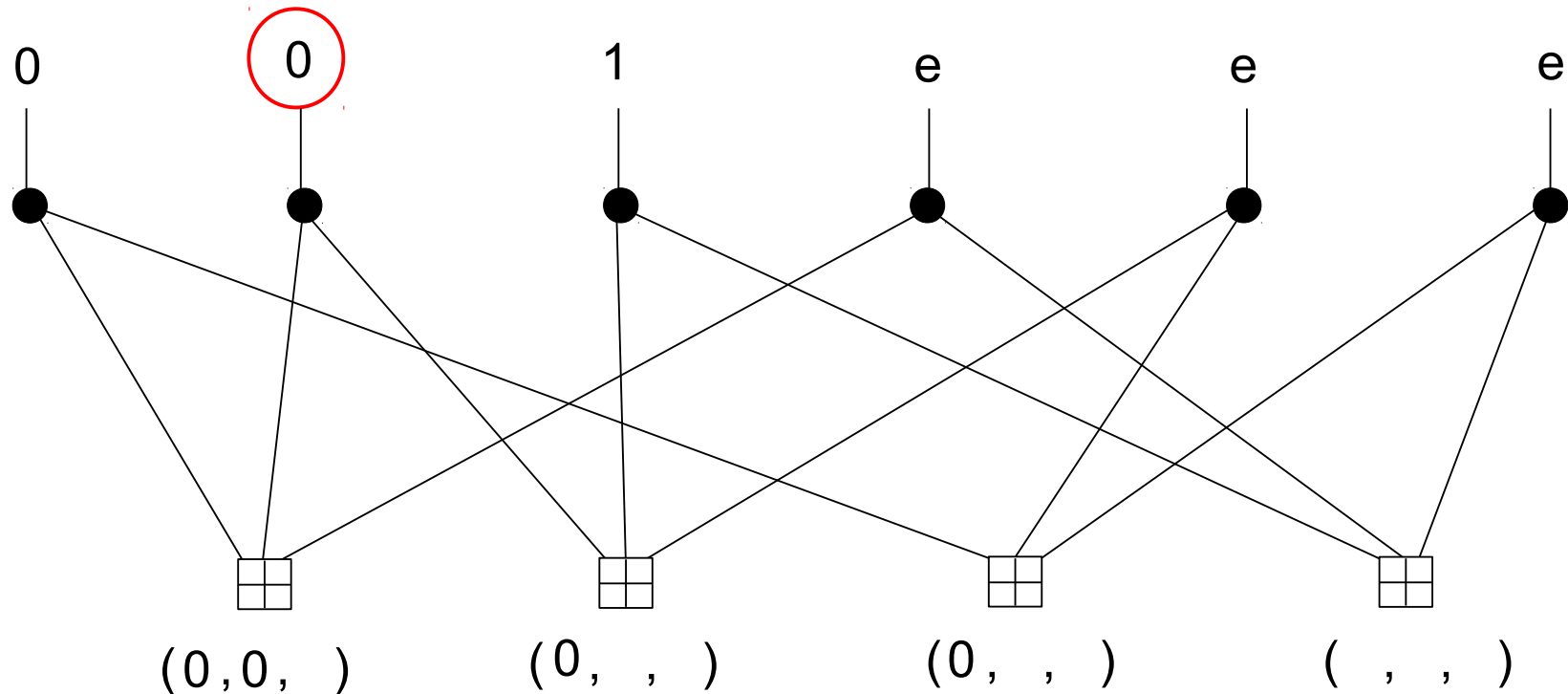


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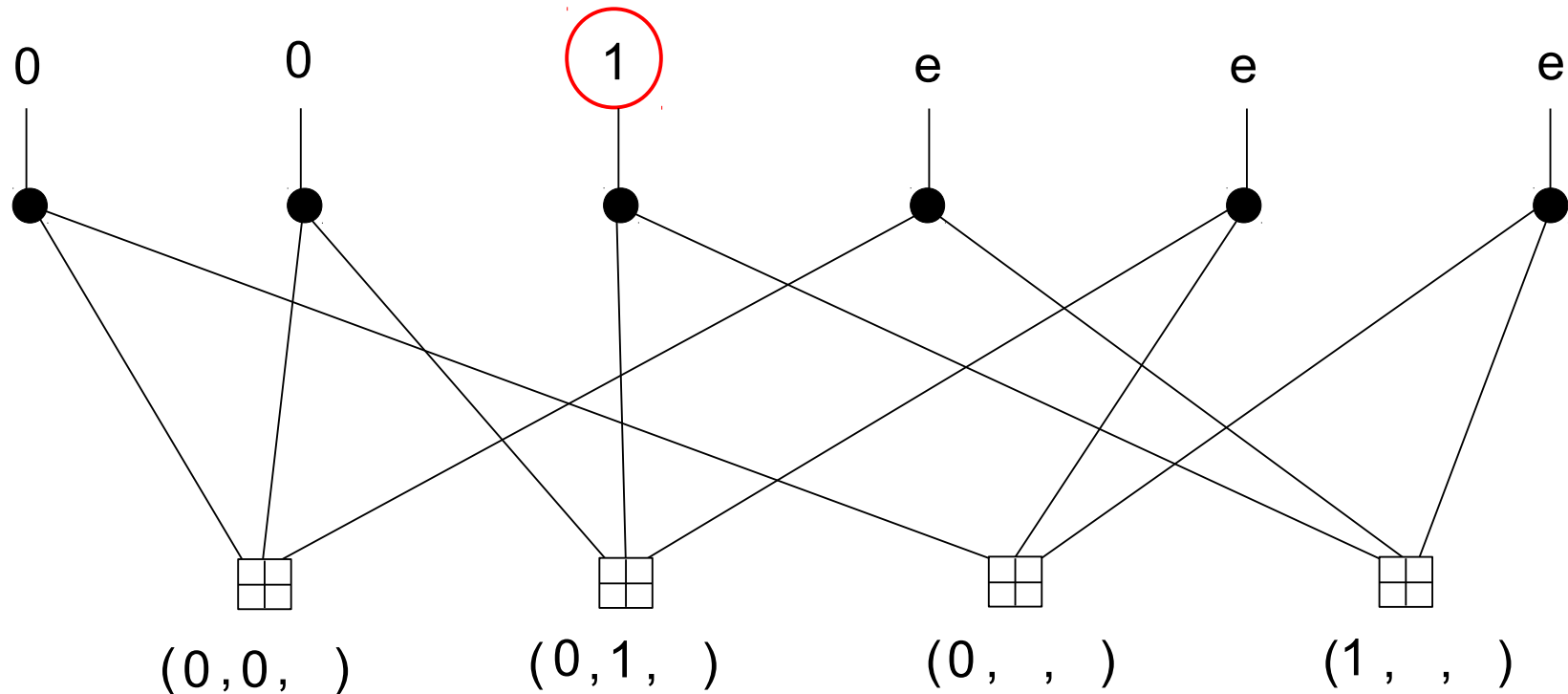


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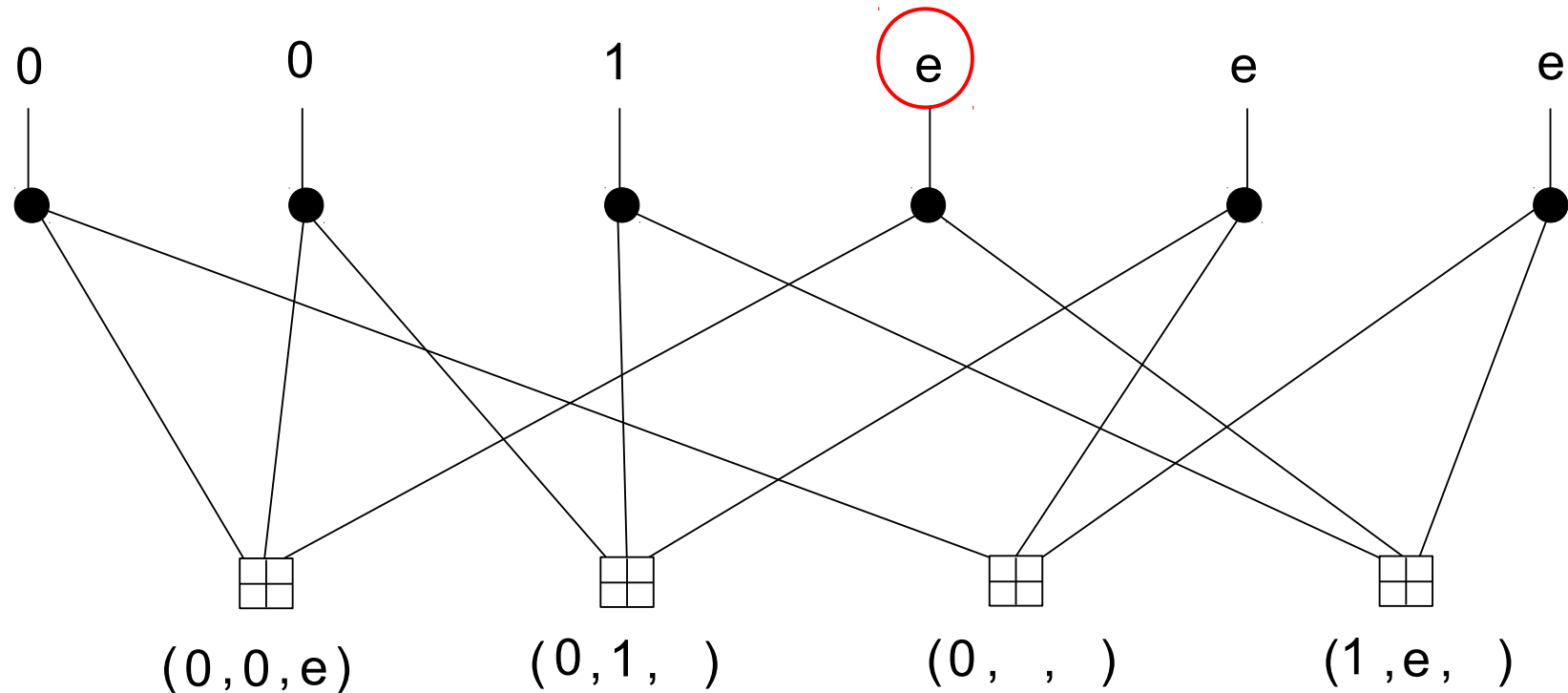


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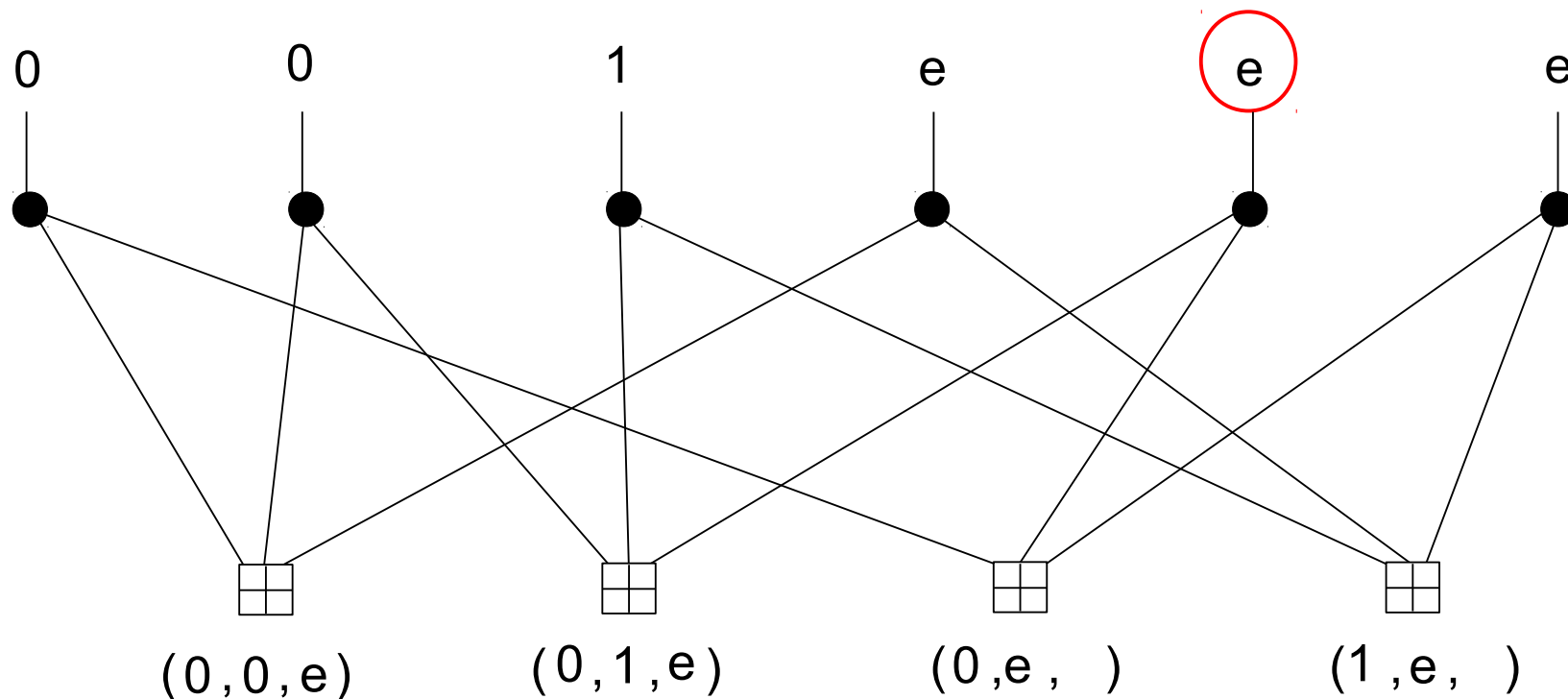


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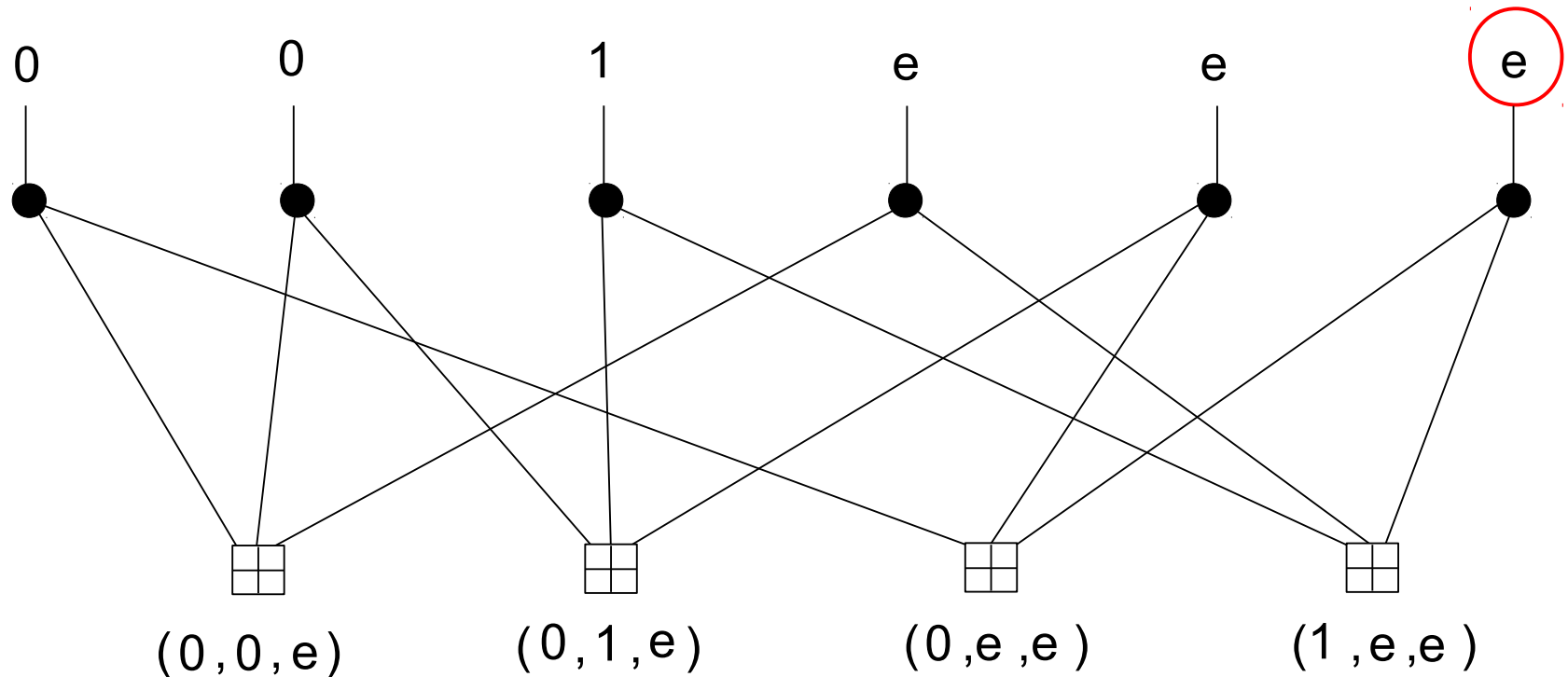


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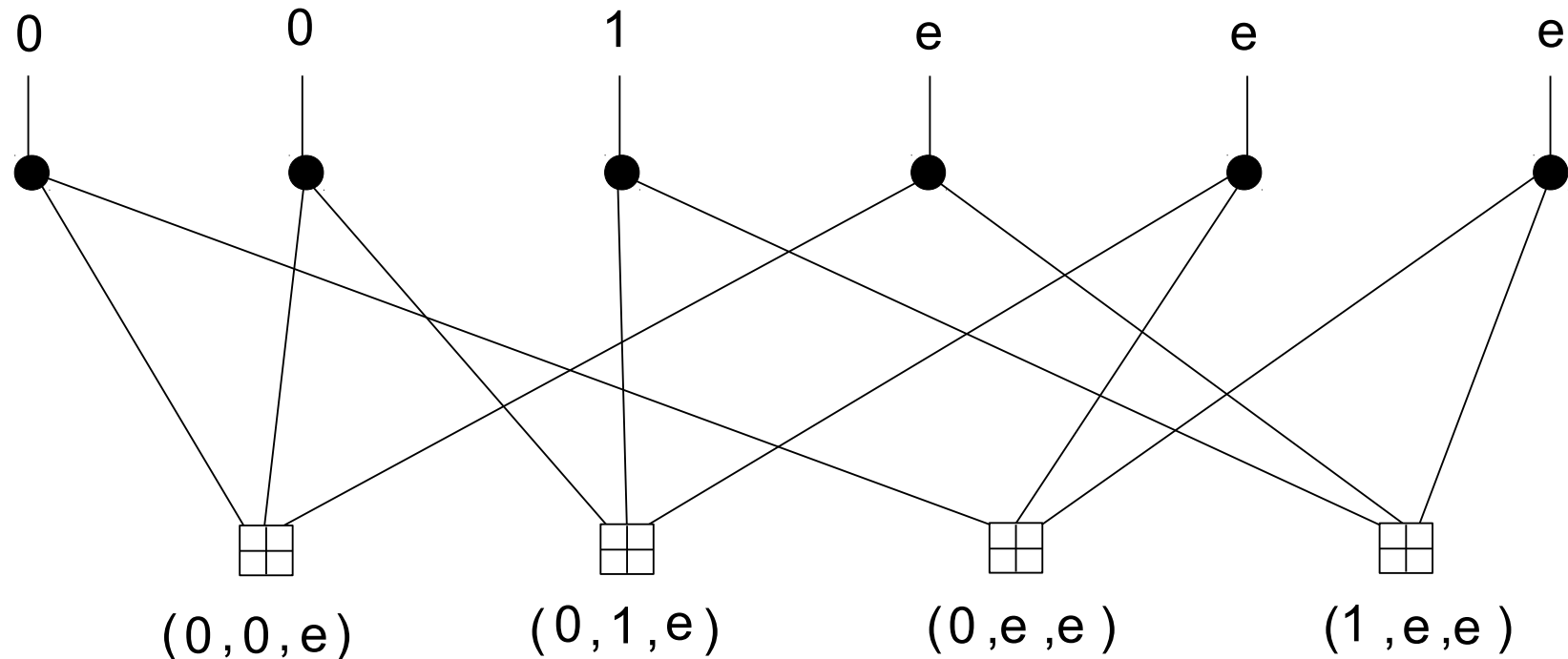


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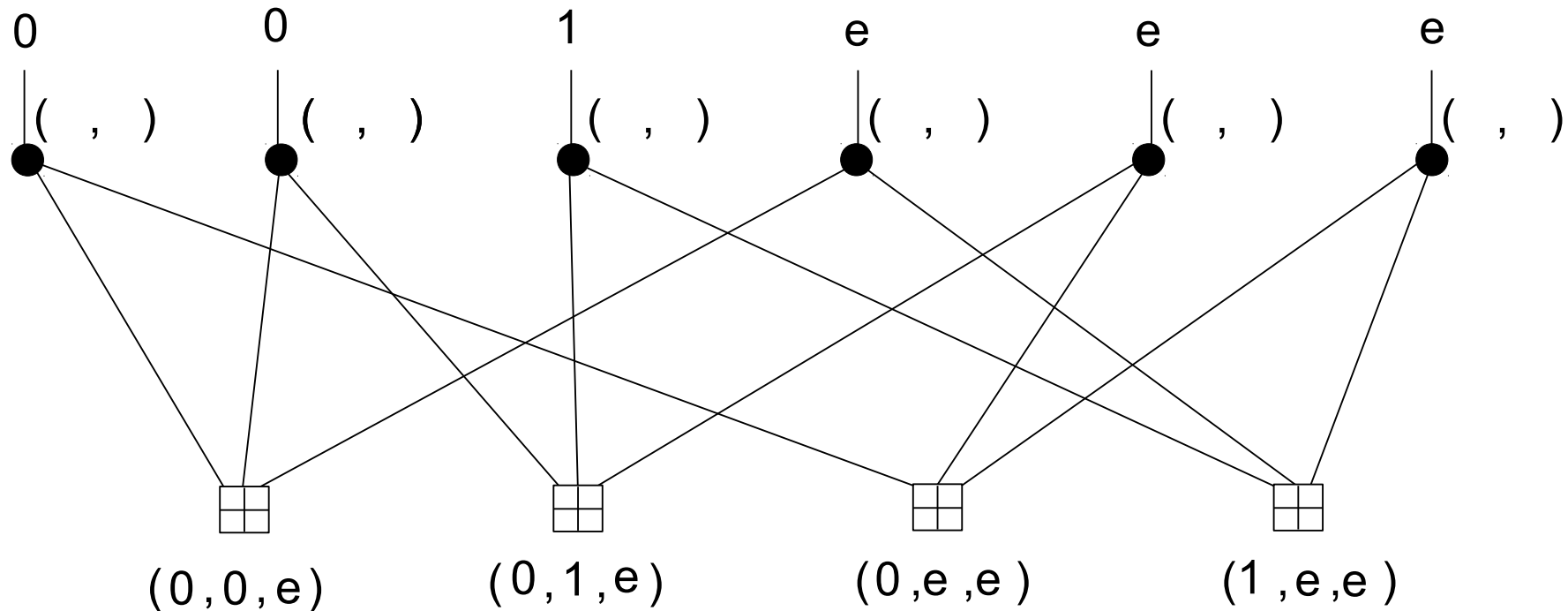
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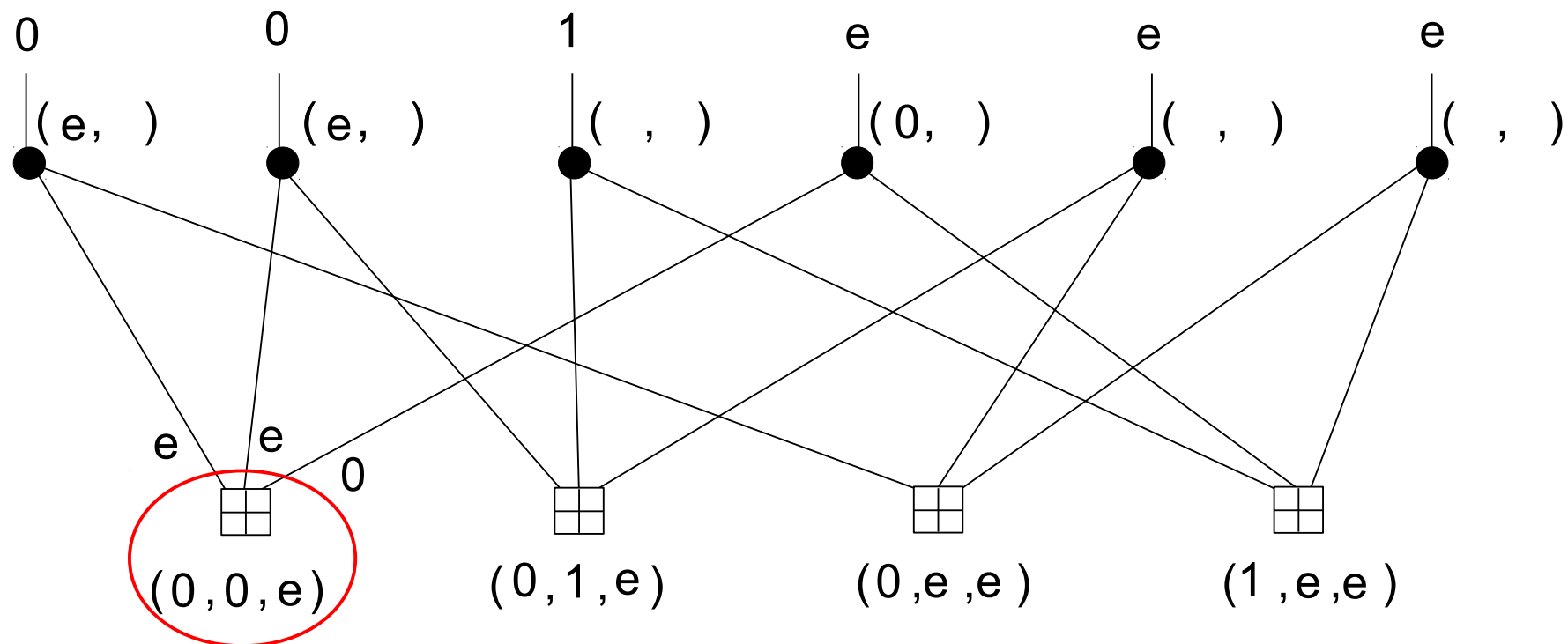




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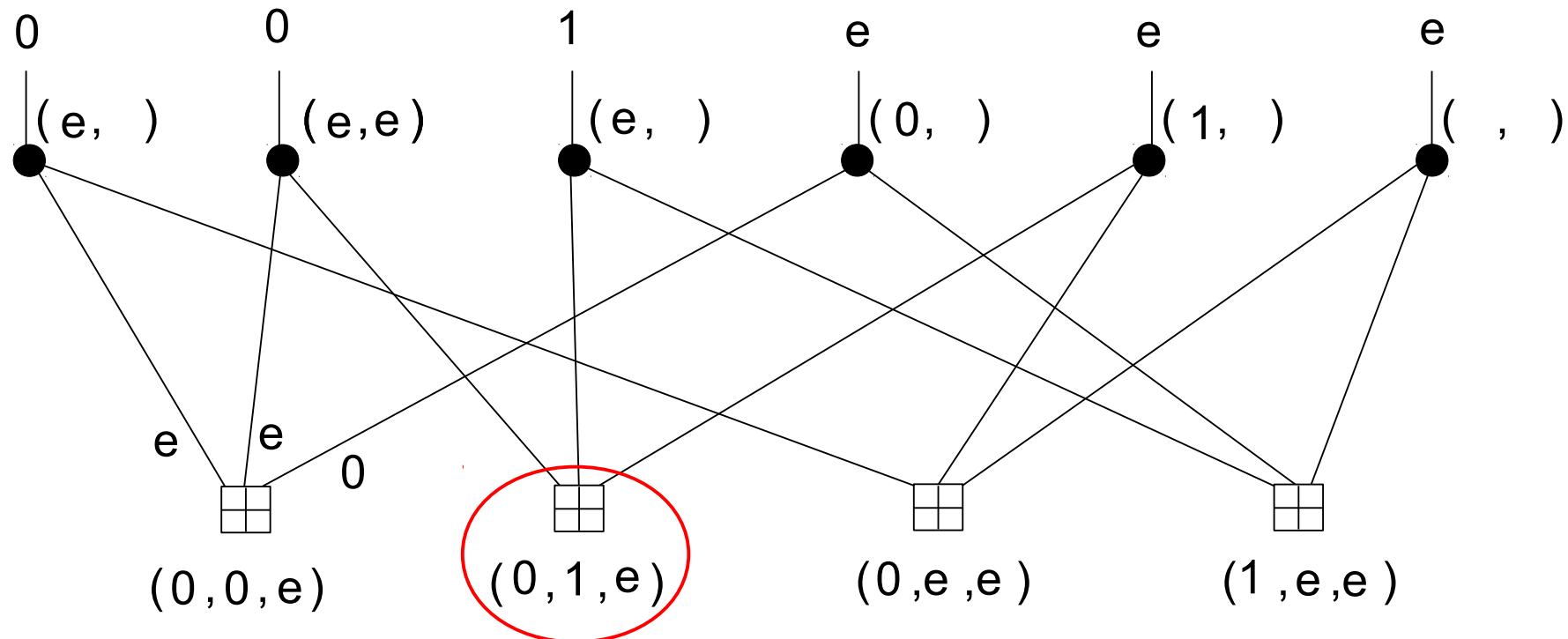
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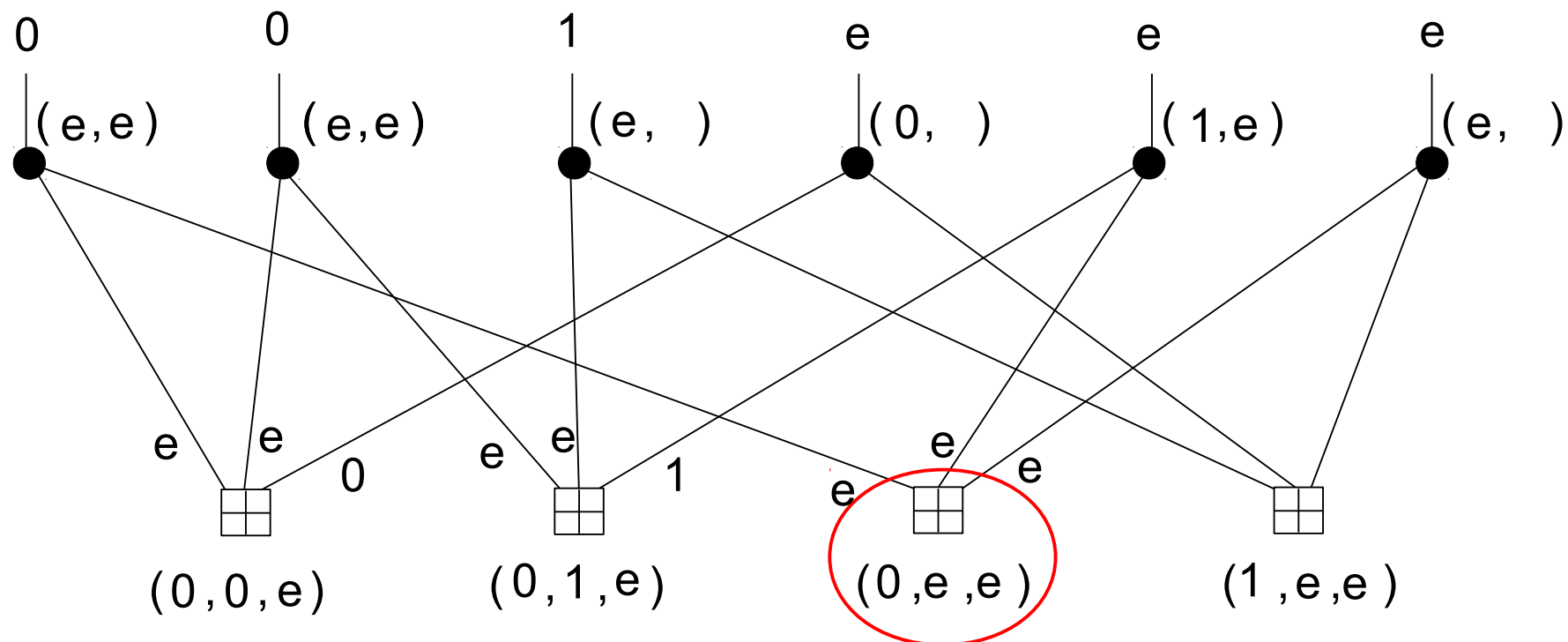
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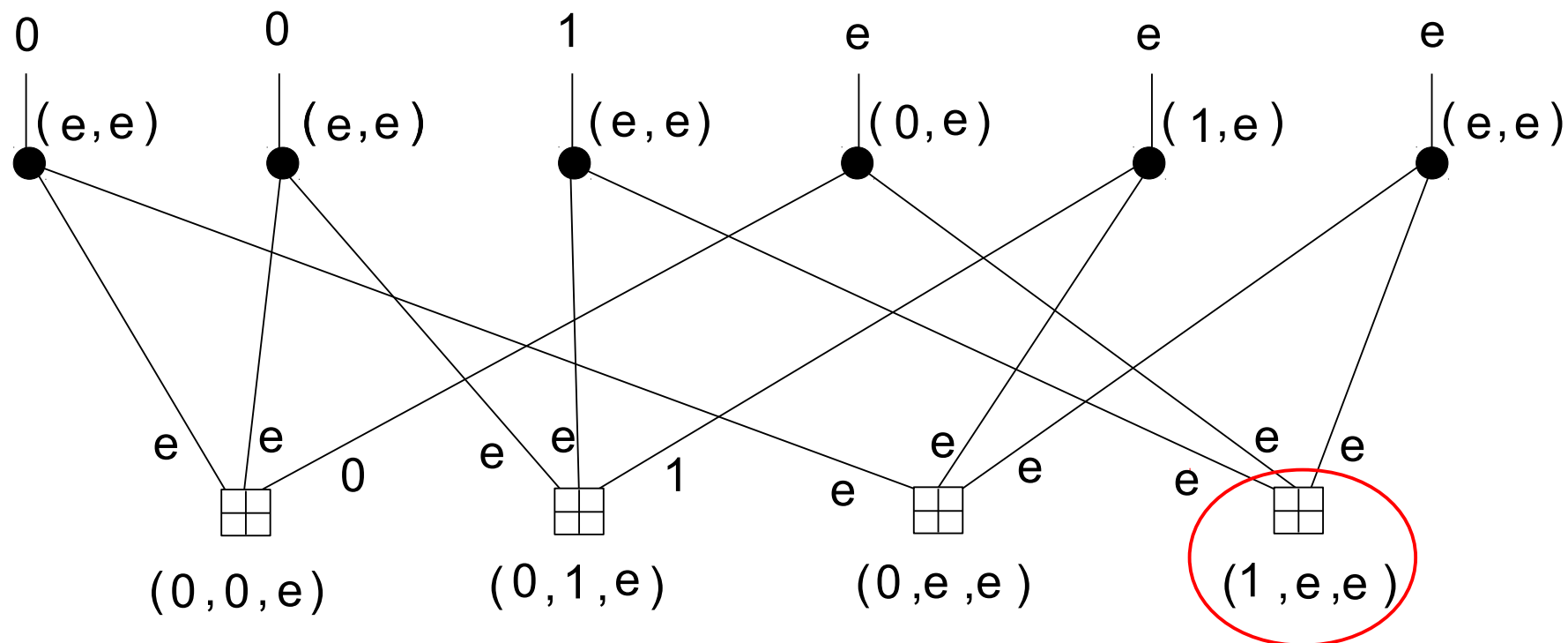
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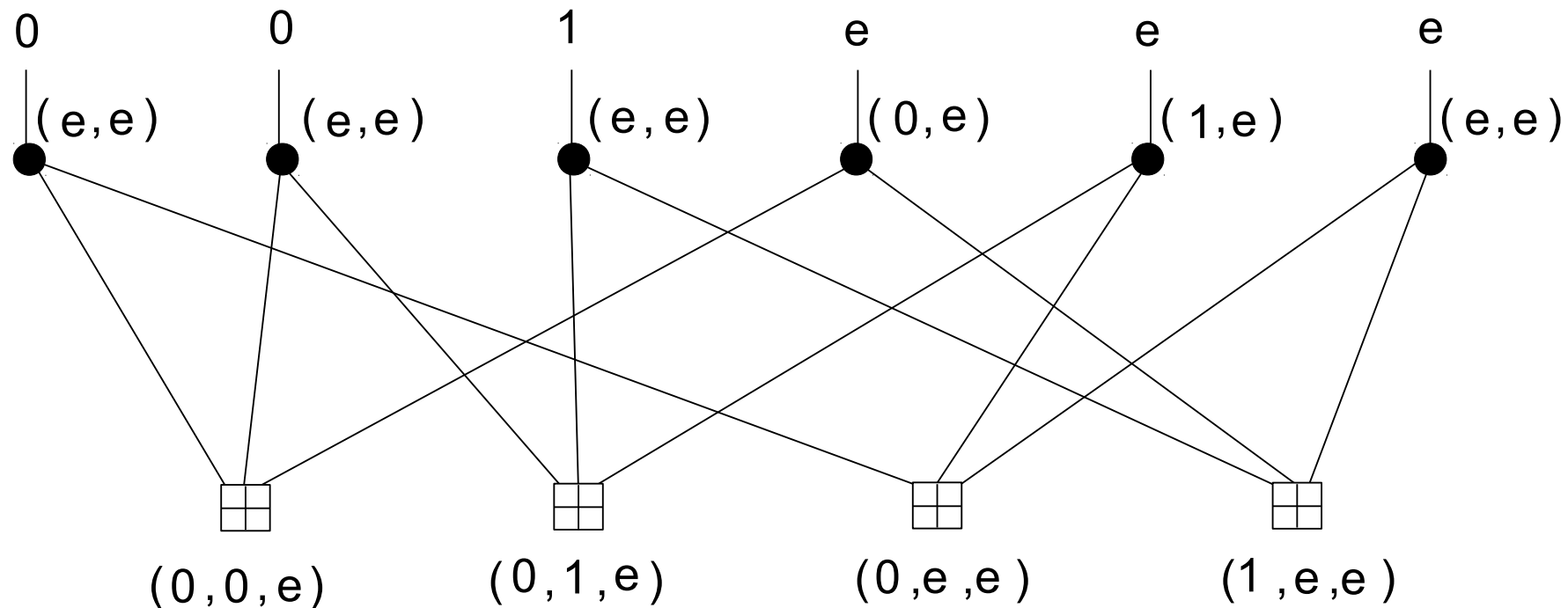
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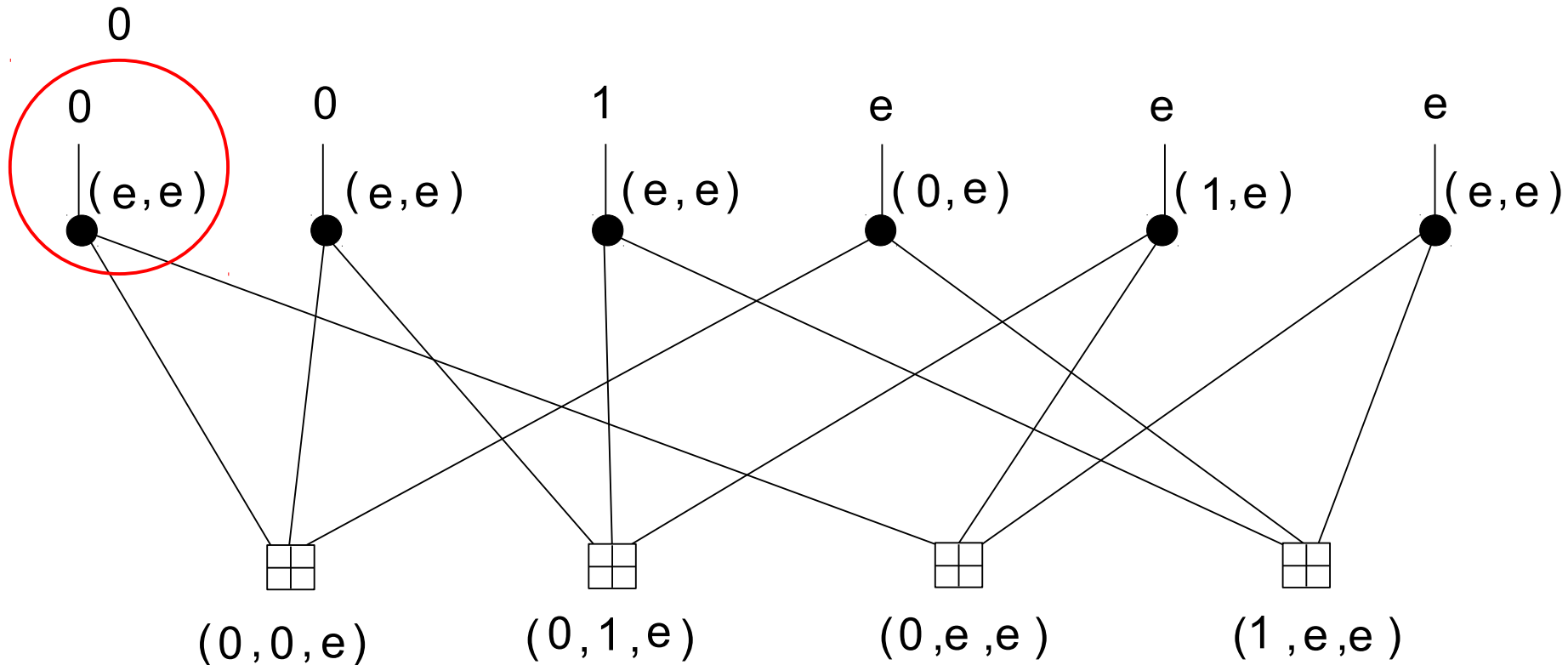
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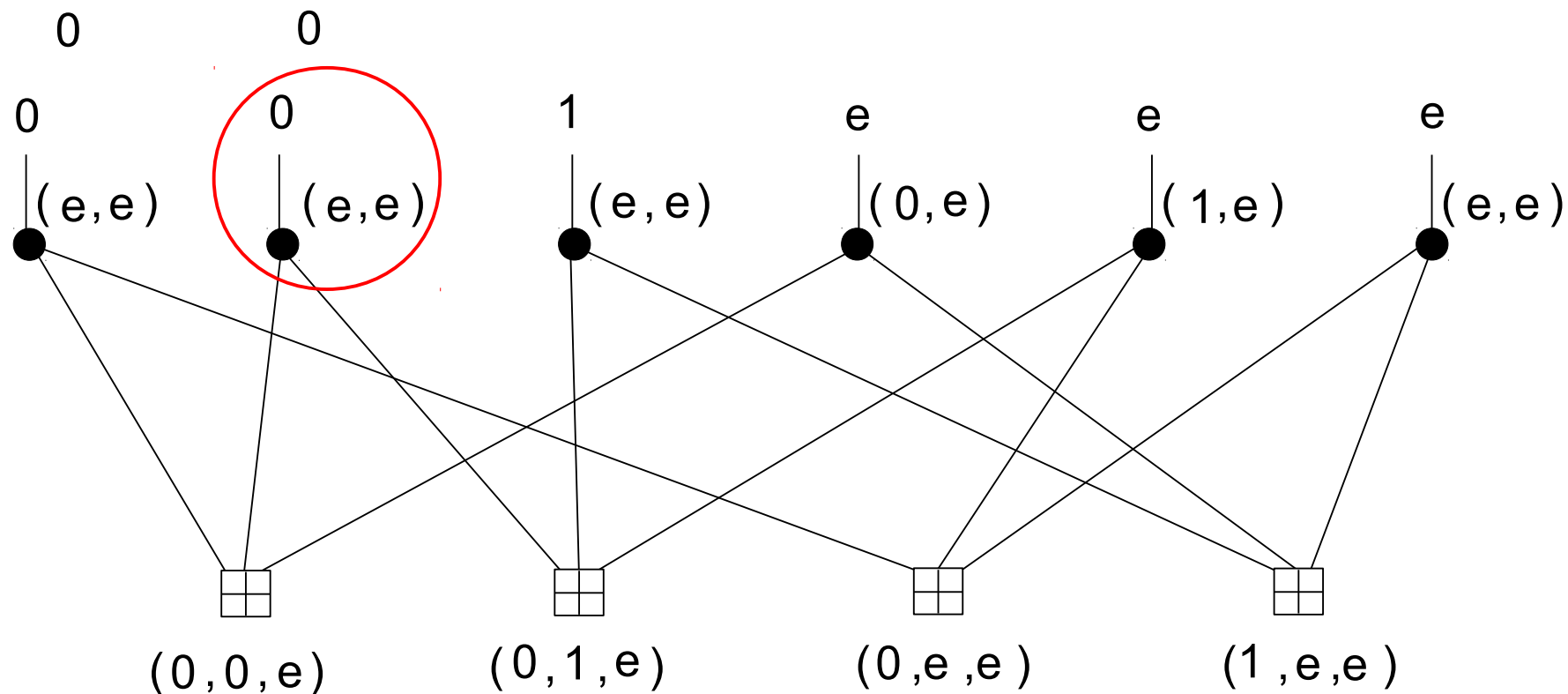
## Iteration 1: bit processing



# Iterative Decoding Example (BEC)

Ex:  $\mathbf{v} = [0\ 0\ 1\ 0\ 1\ 1] \rightarrow \mathbf{r} = [0\ 0\ 1\ e\ e\ e]$

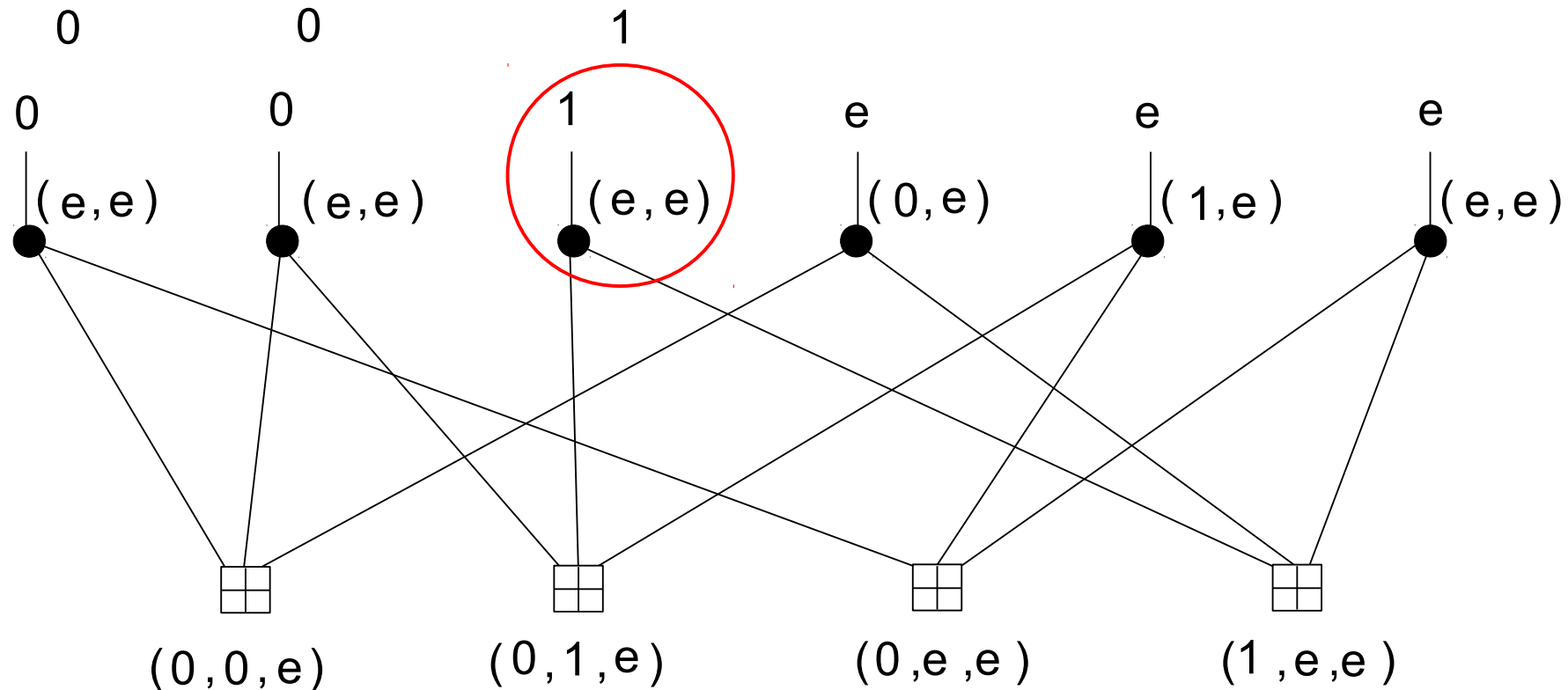
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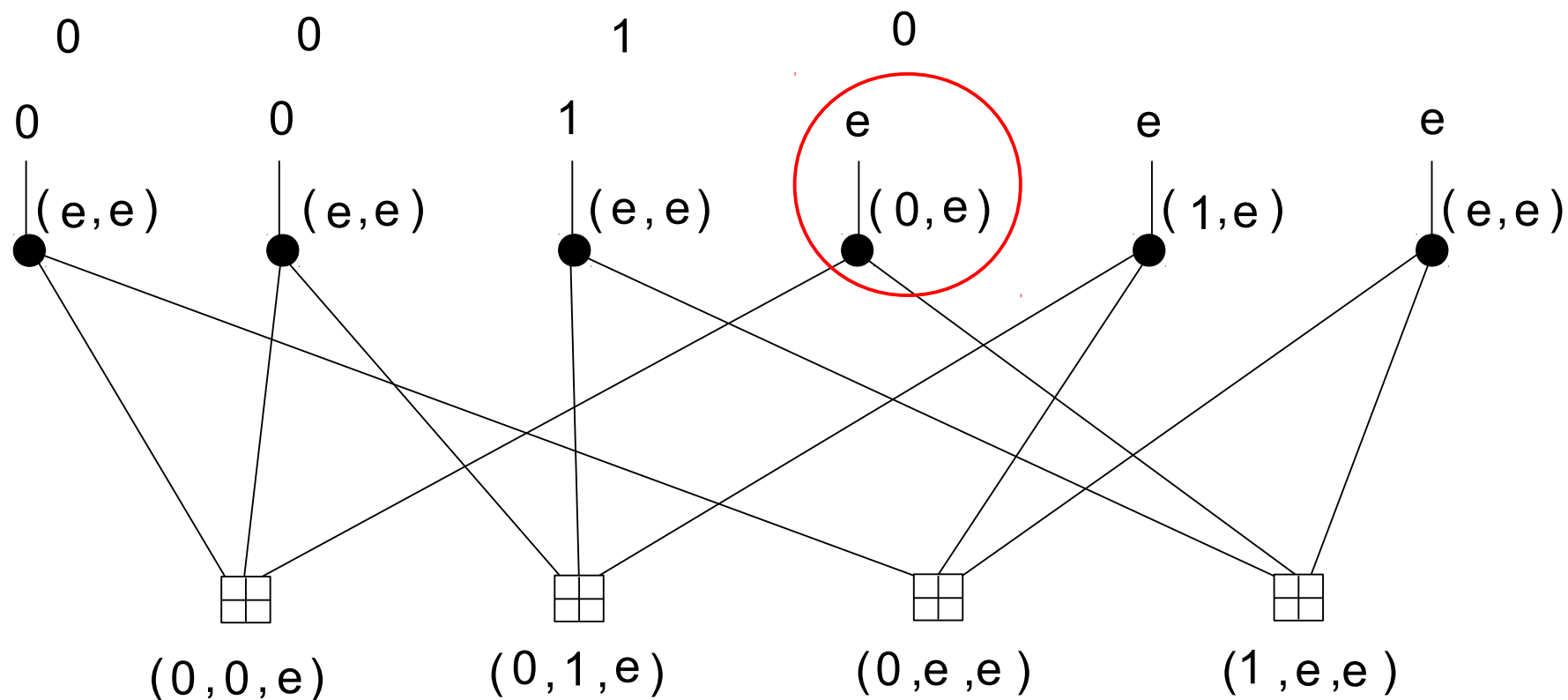




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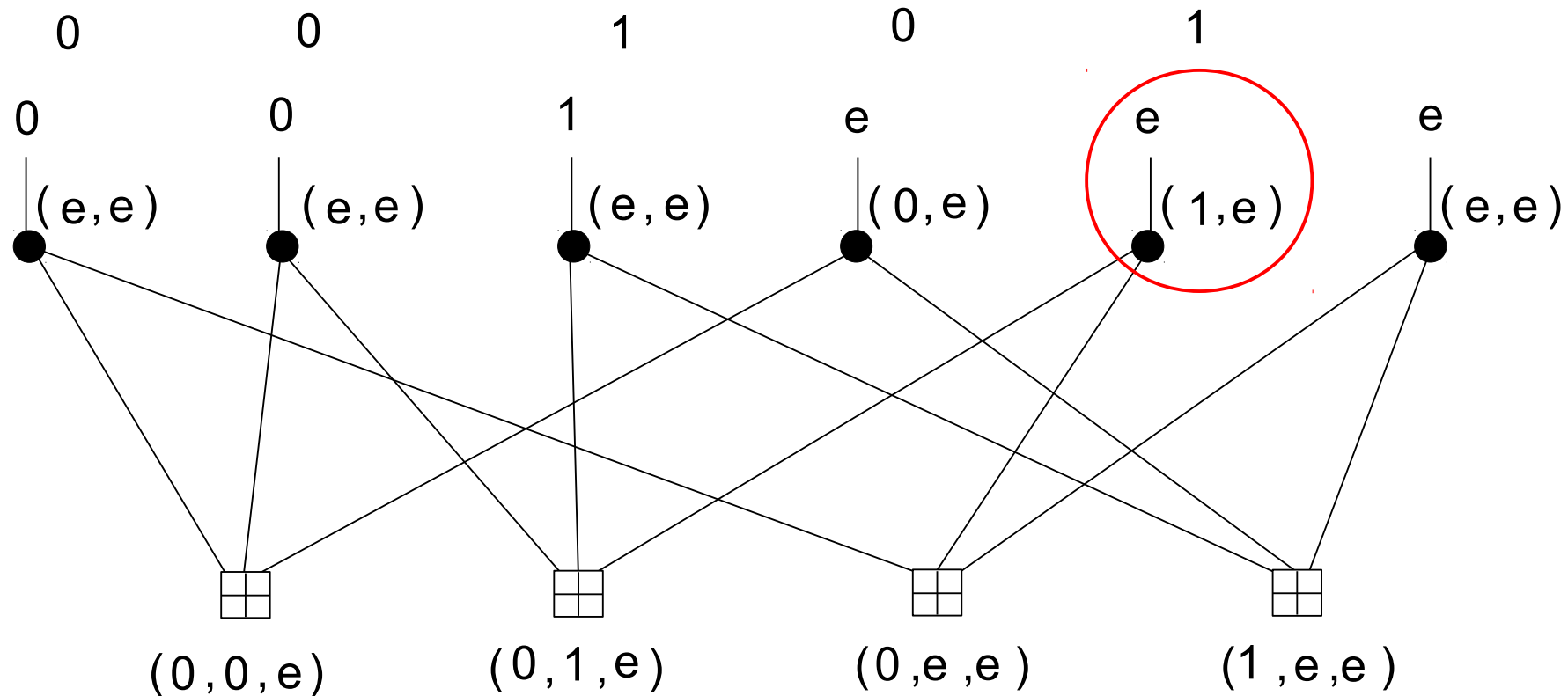
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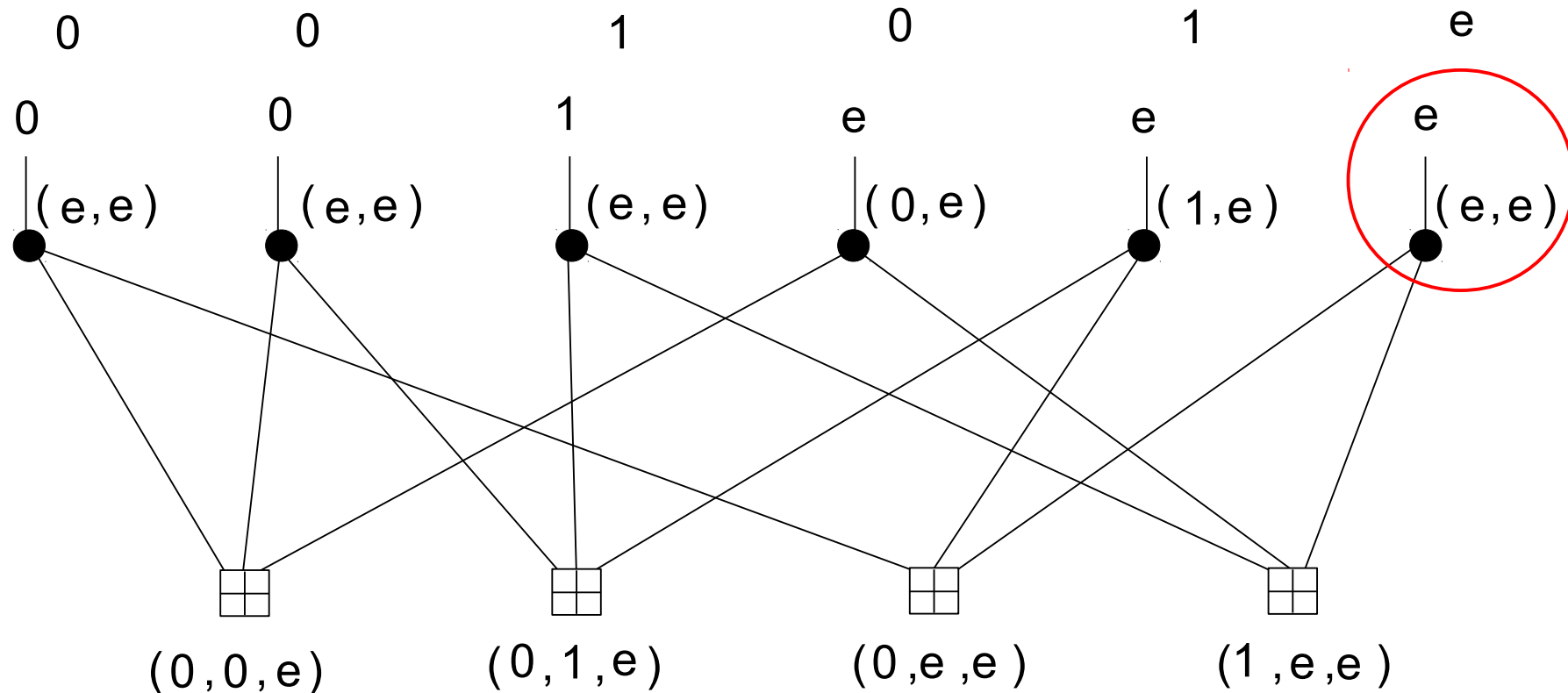
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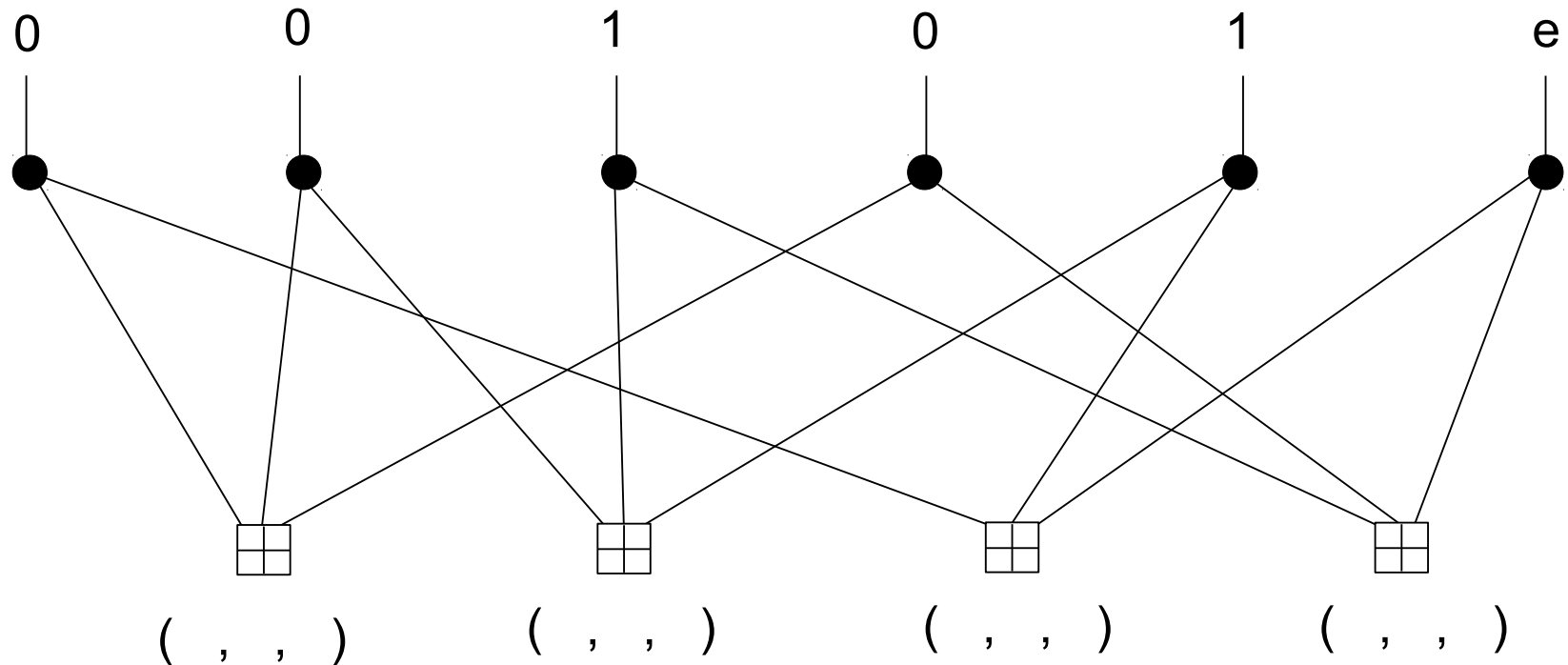
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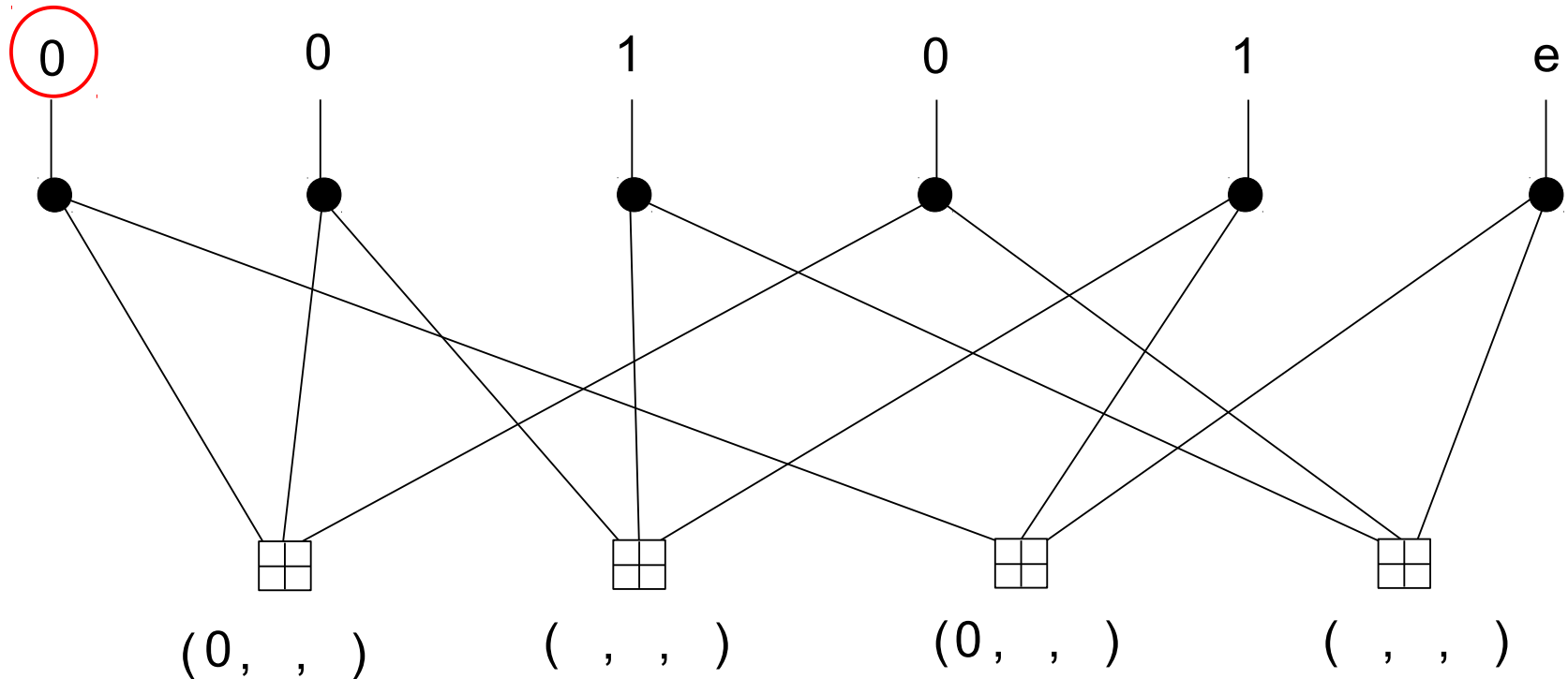
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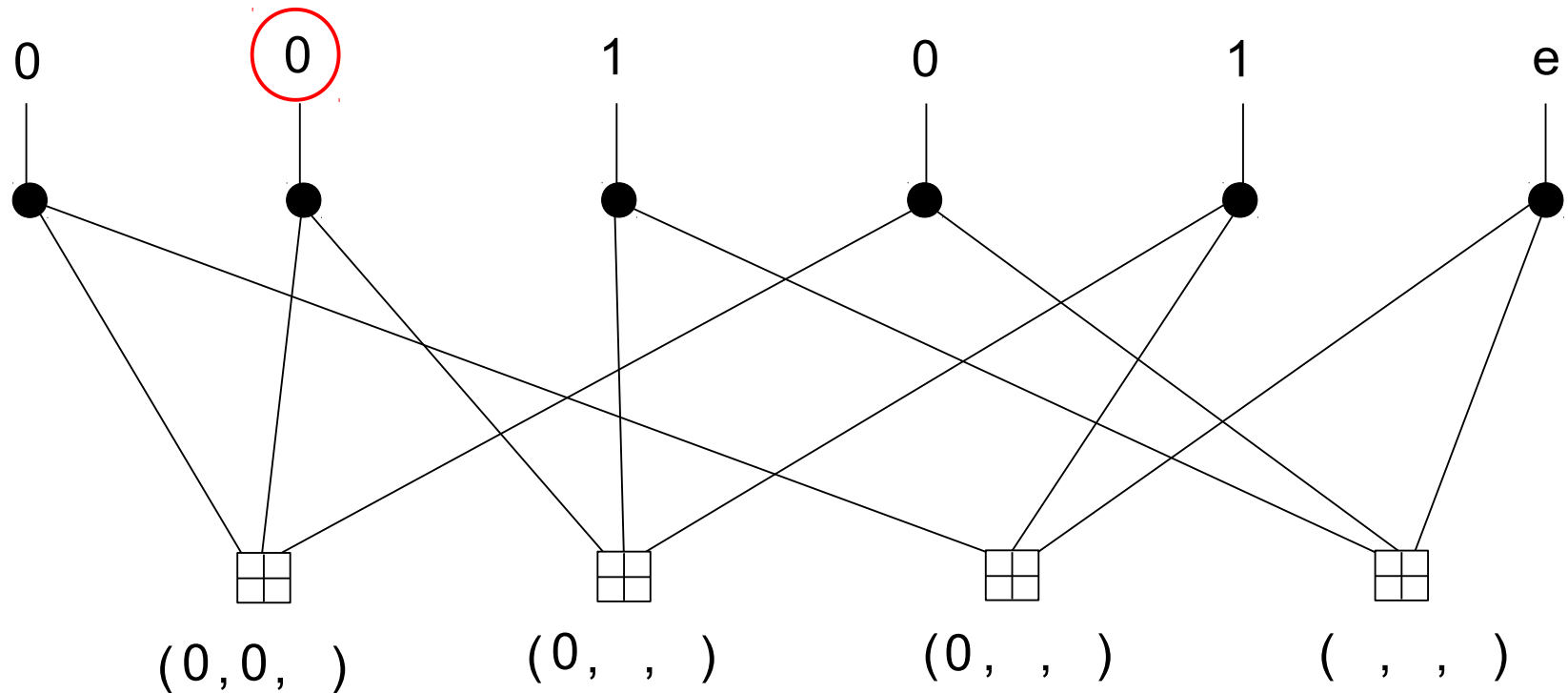
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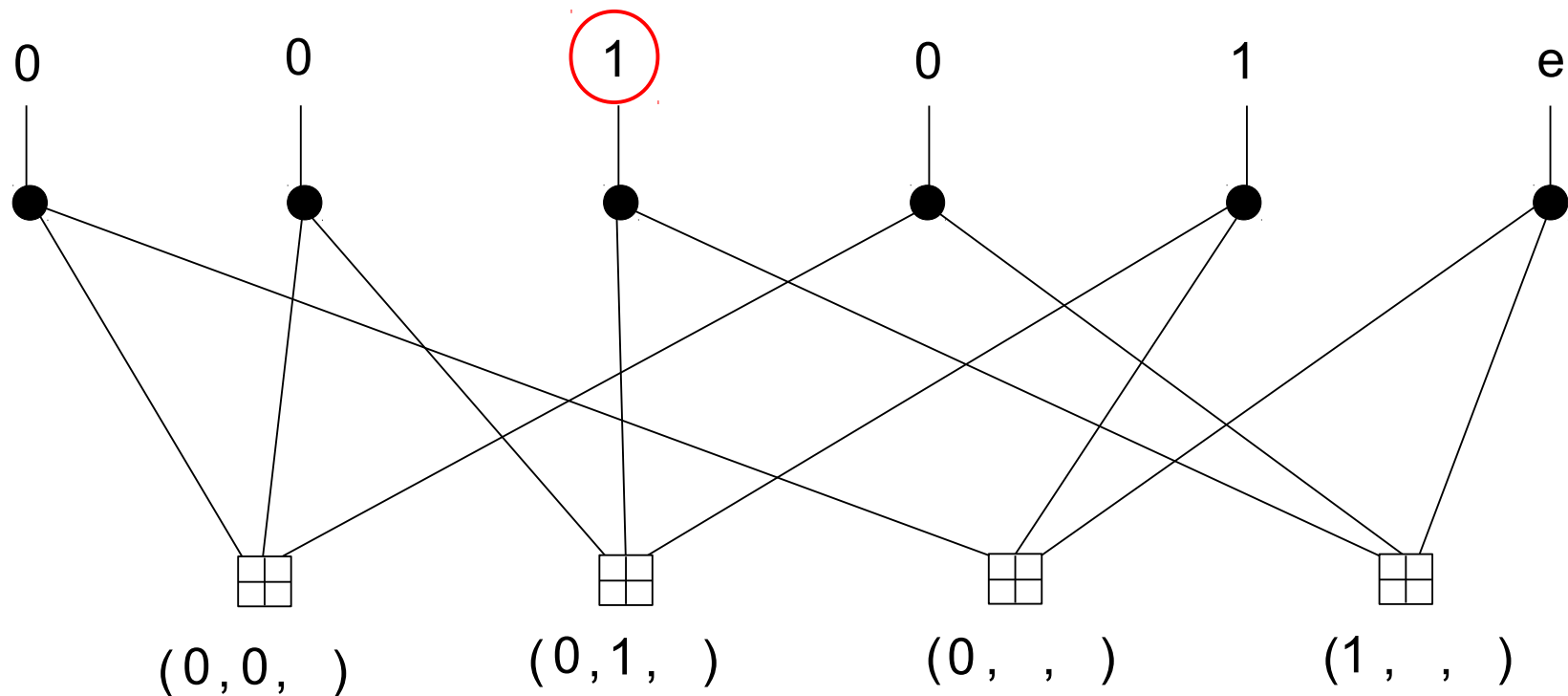
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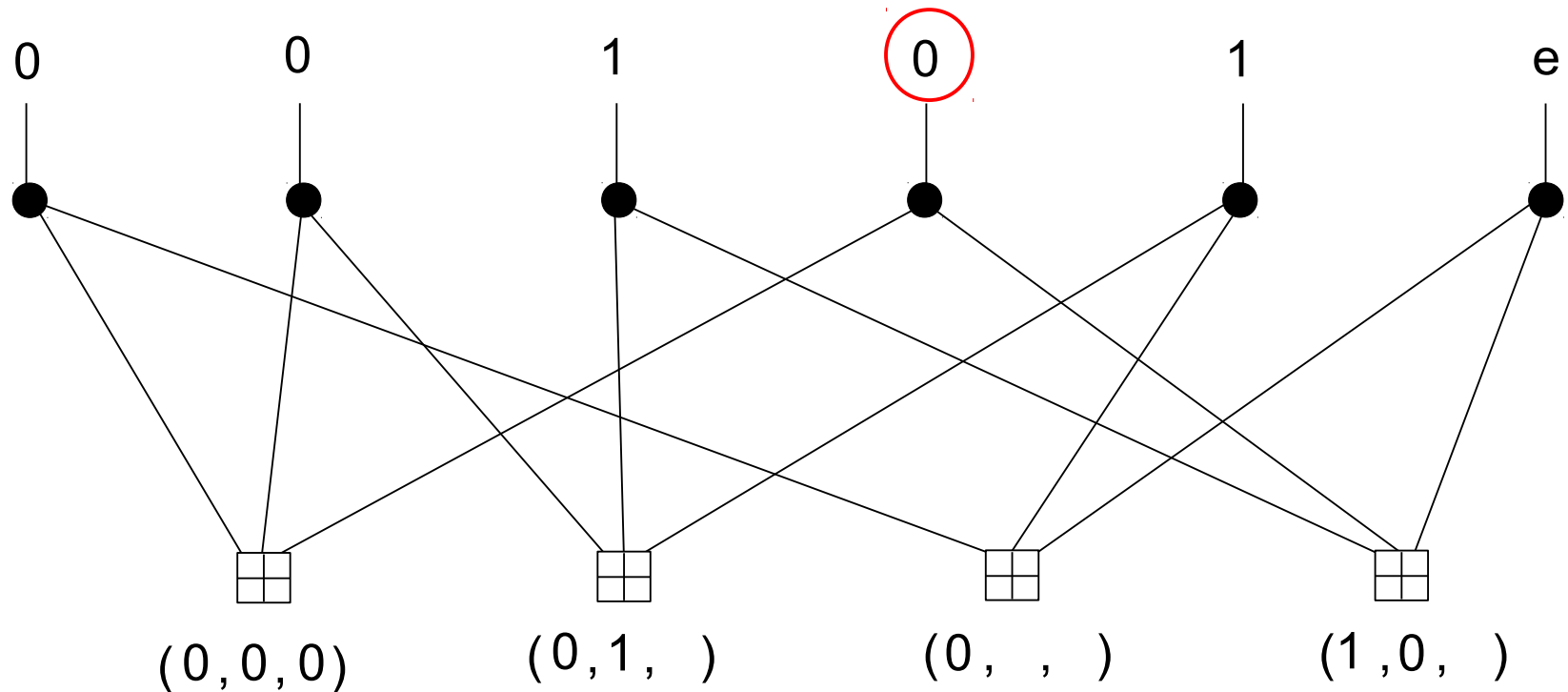
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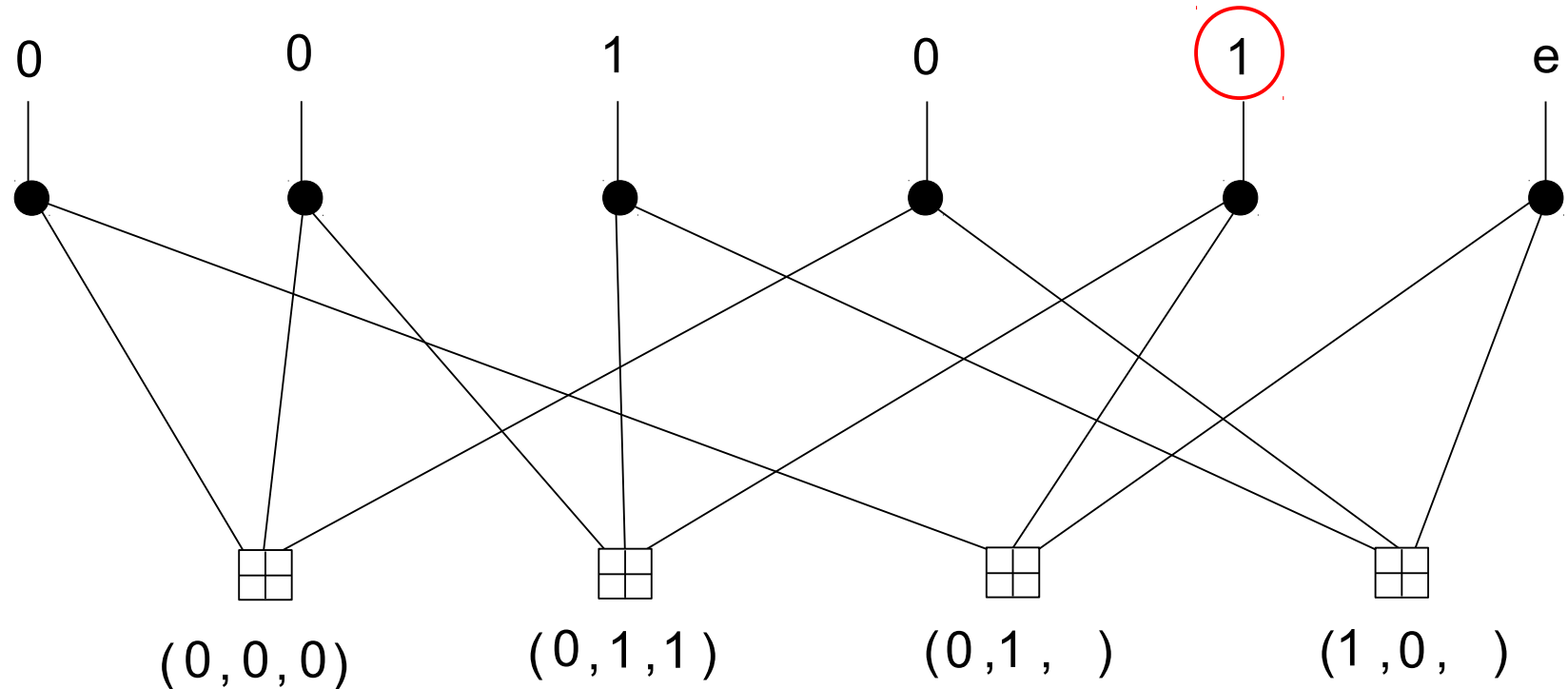




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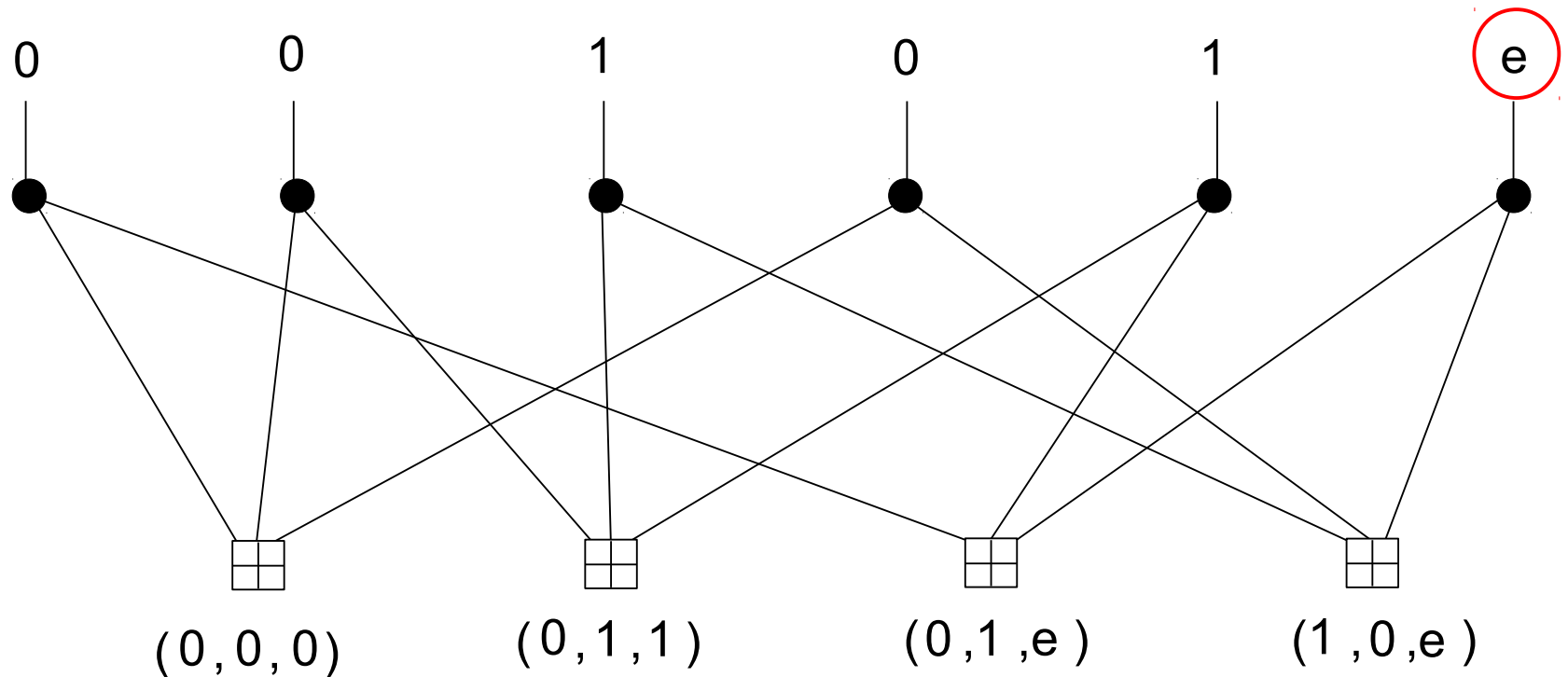
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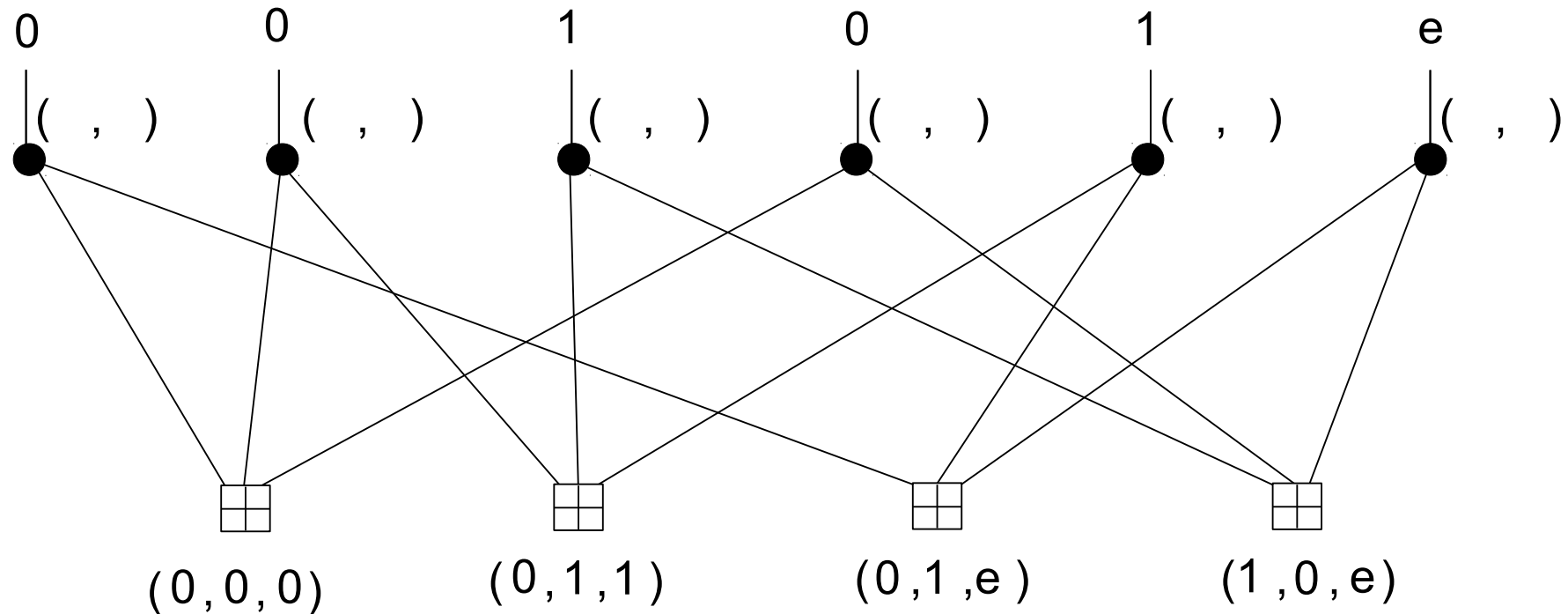
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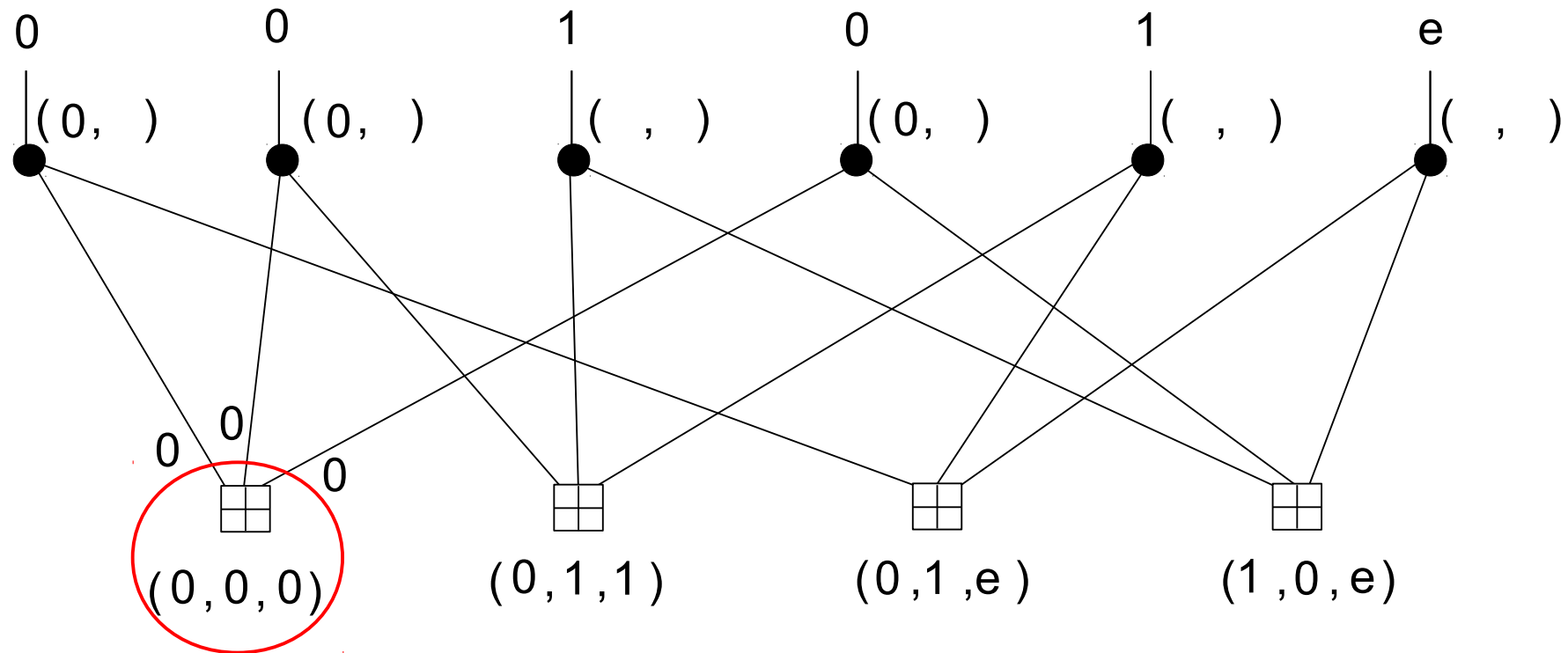
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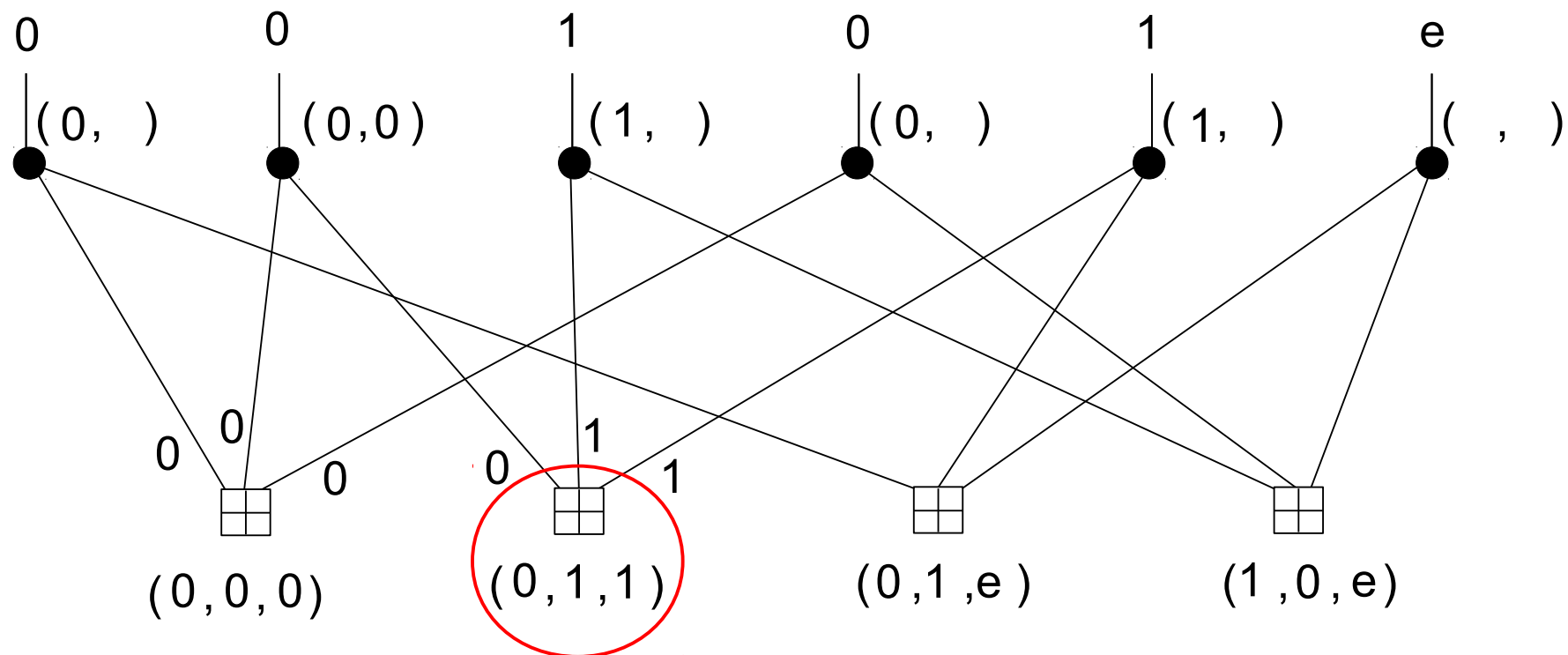
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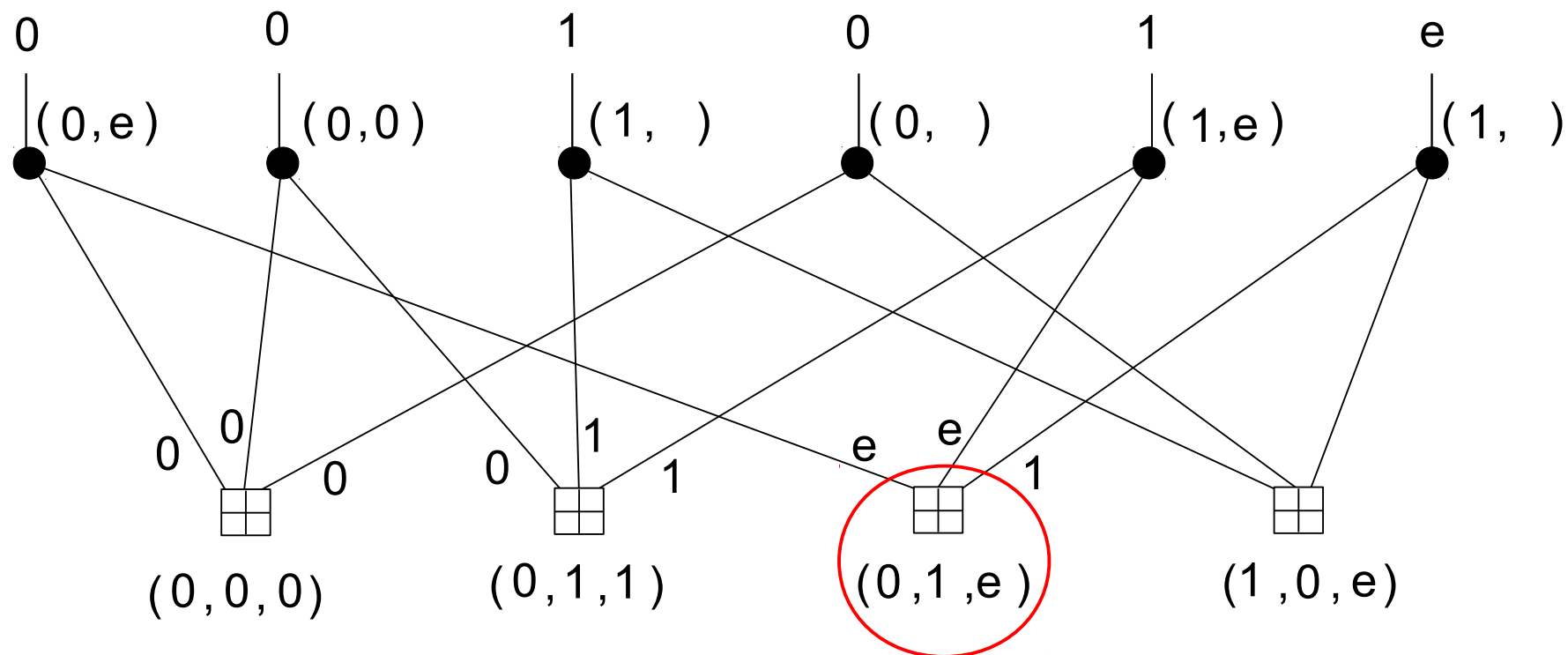
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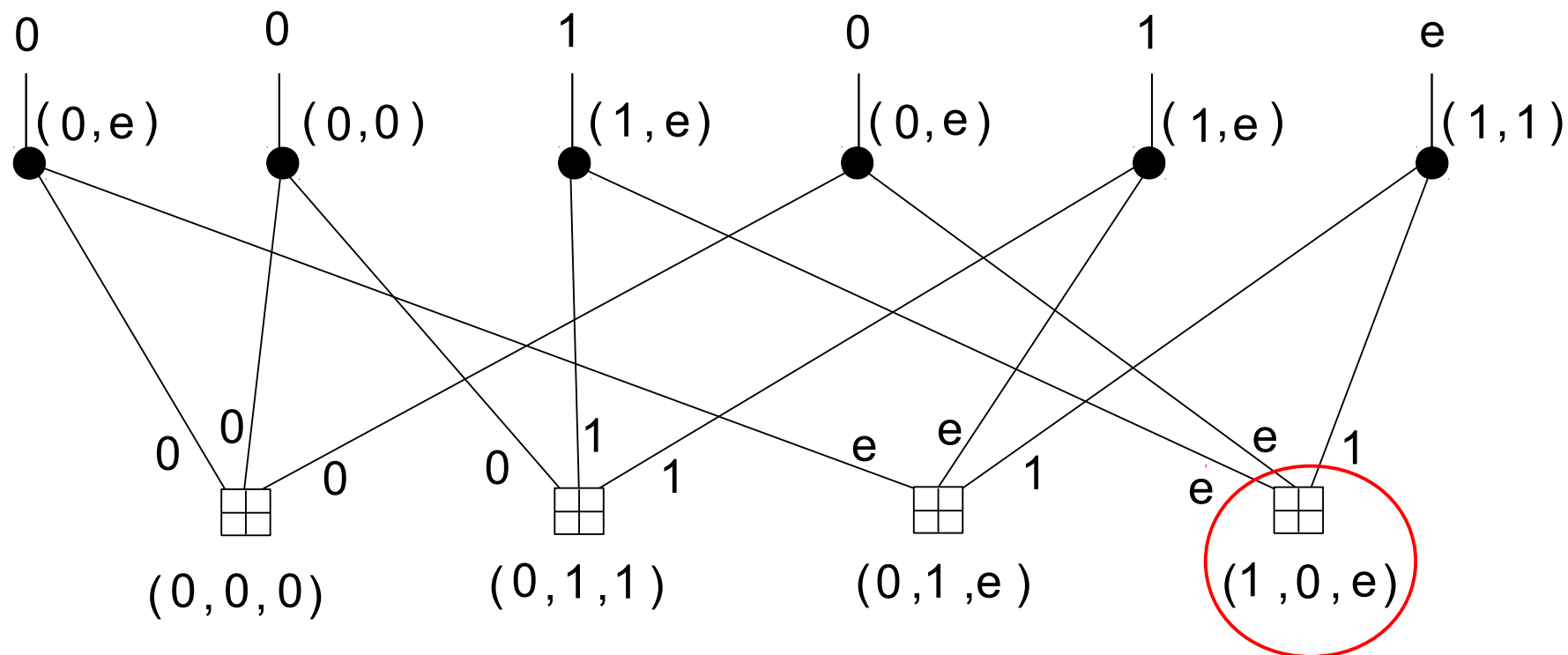
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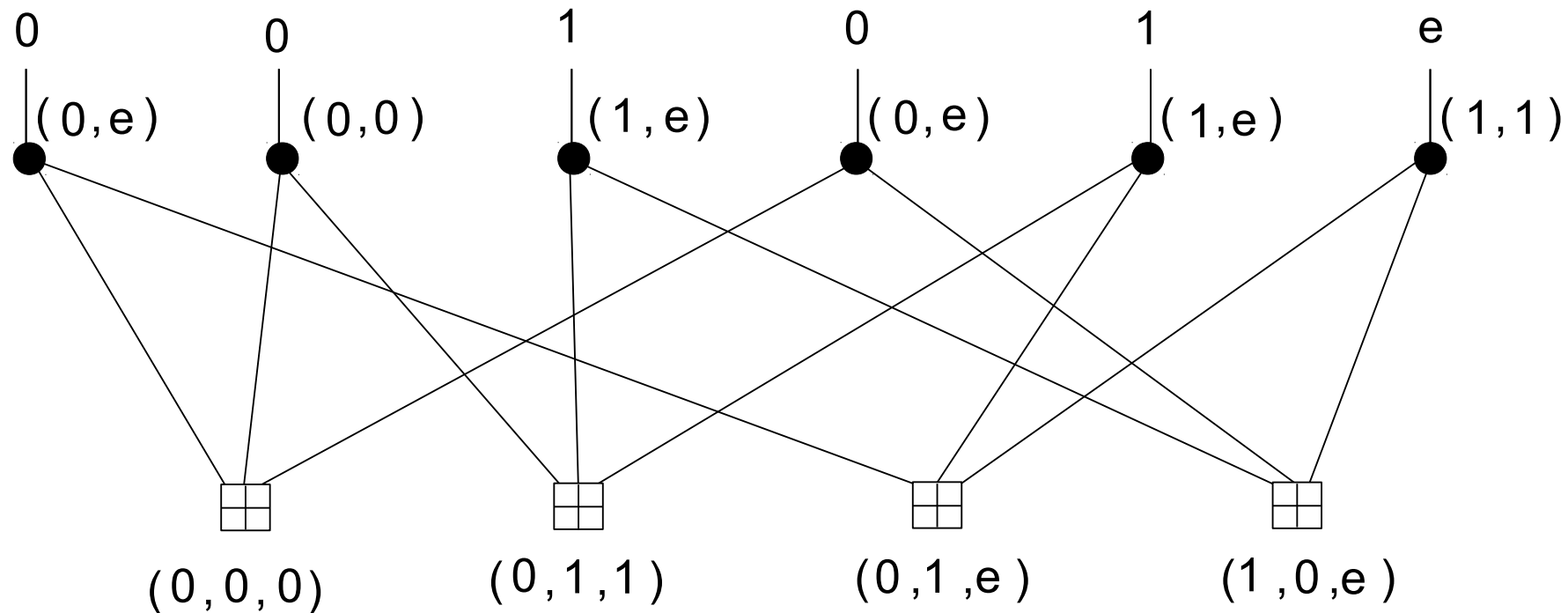
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## Iteration 2: bit processing

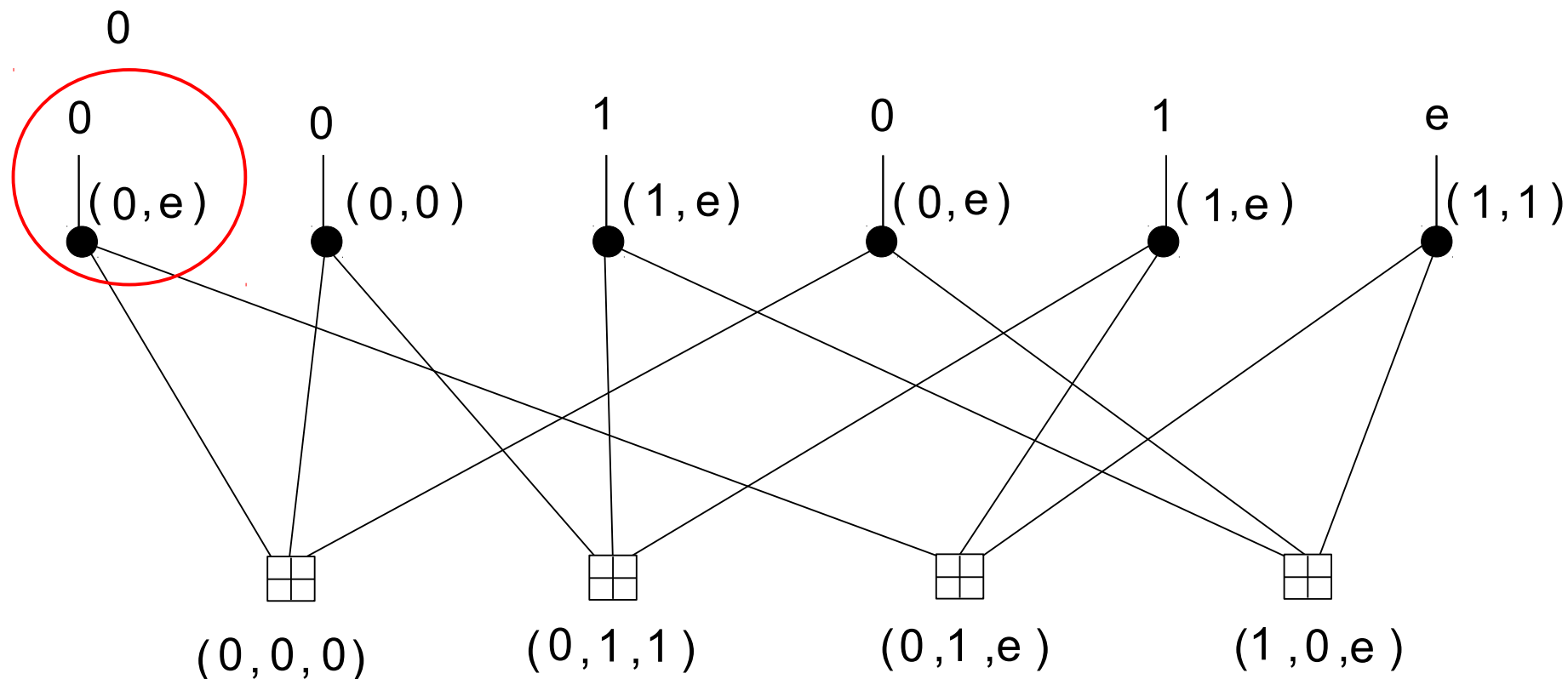




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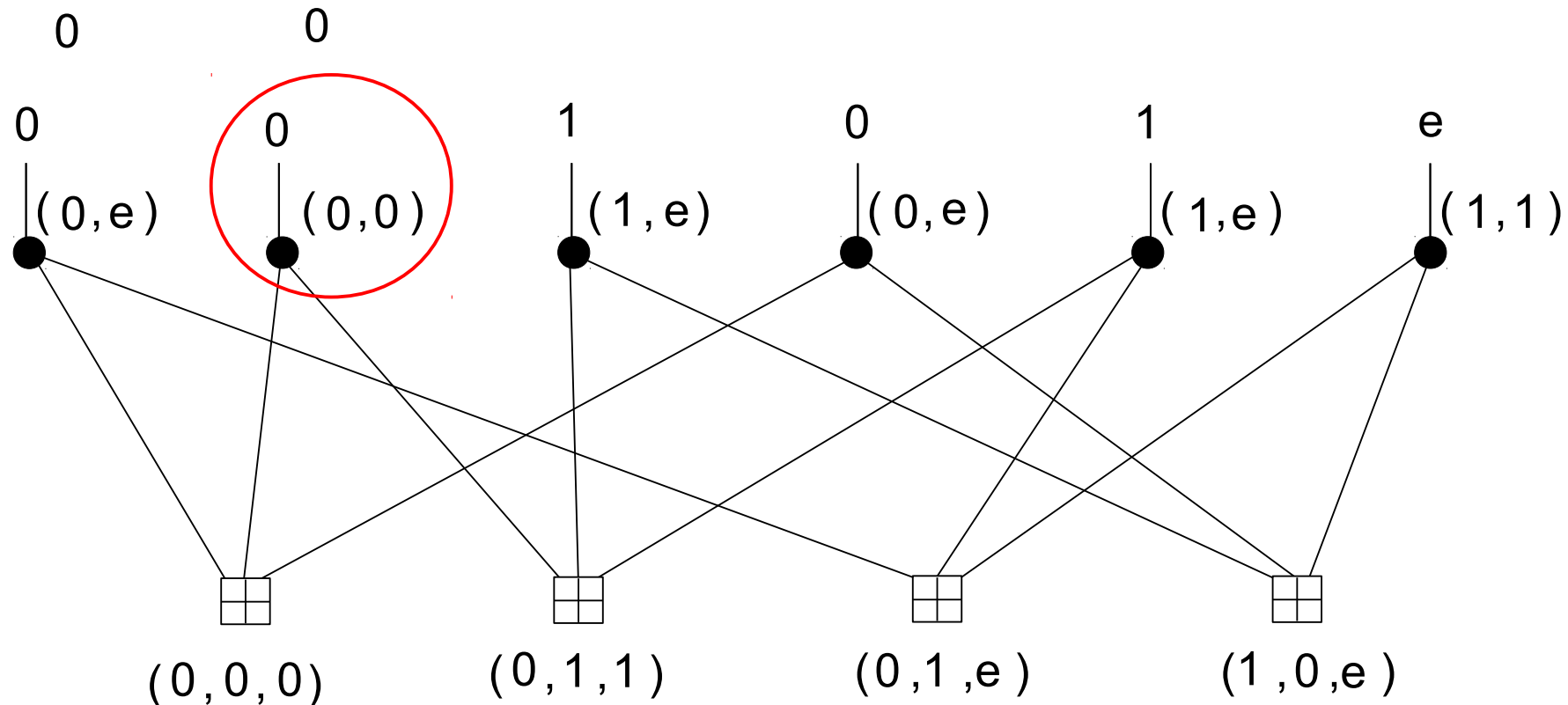
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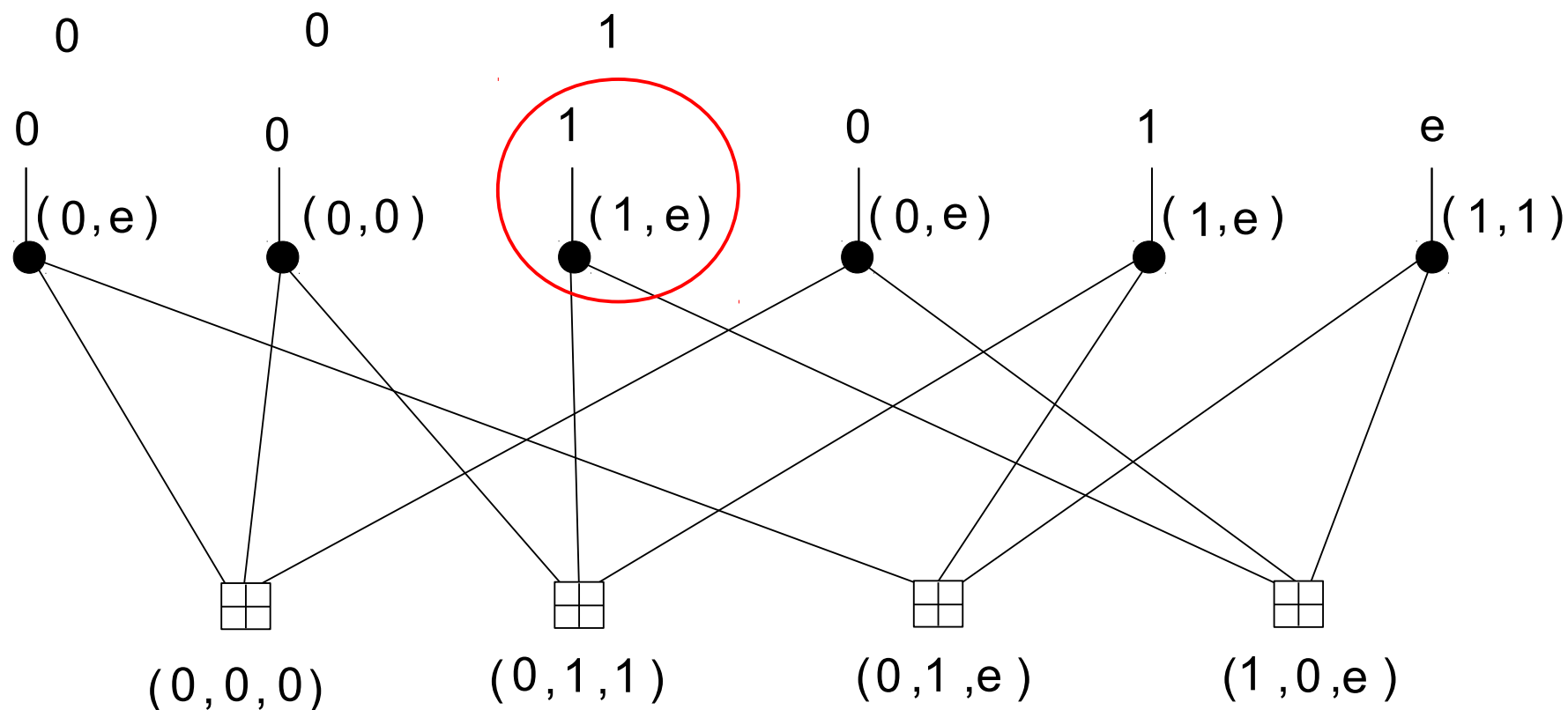
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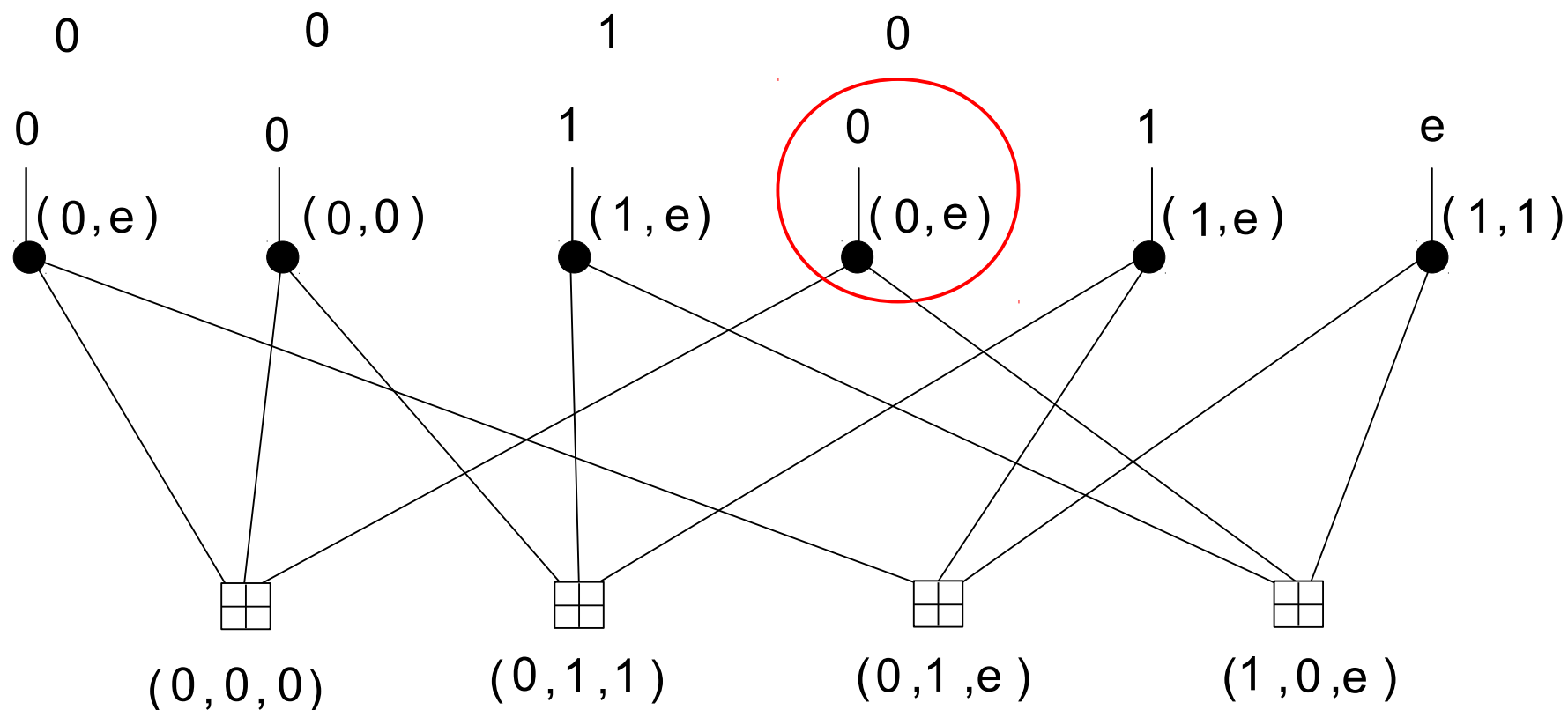
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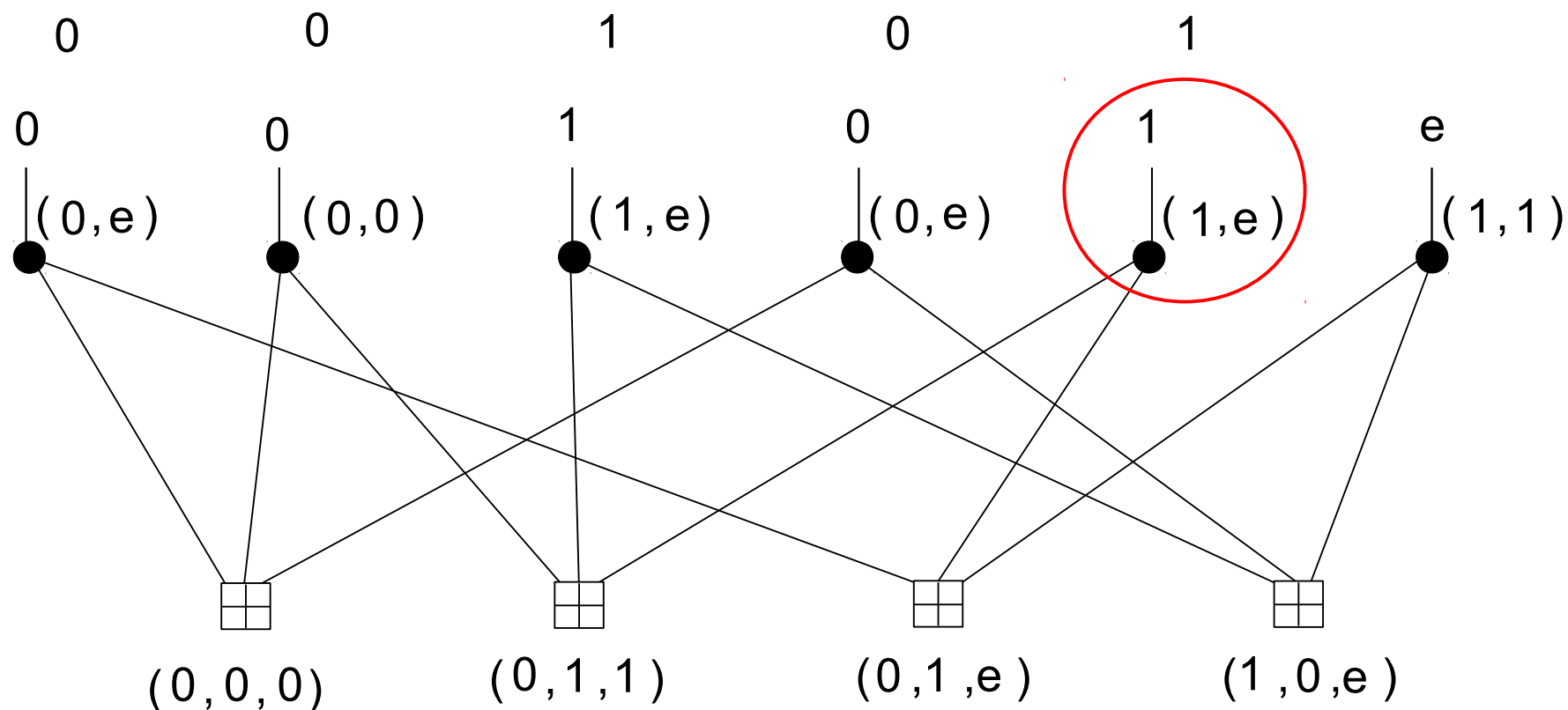
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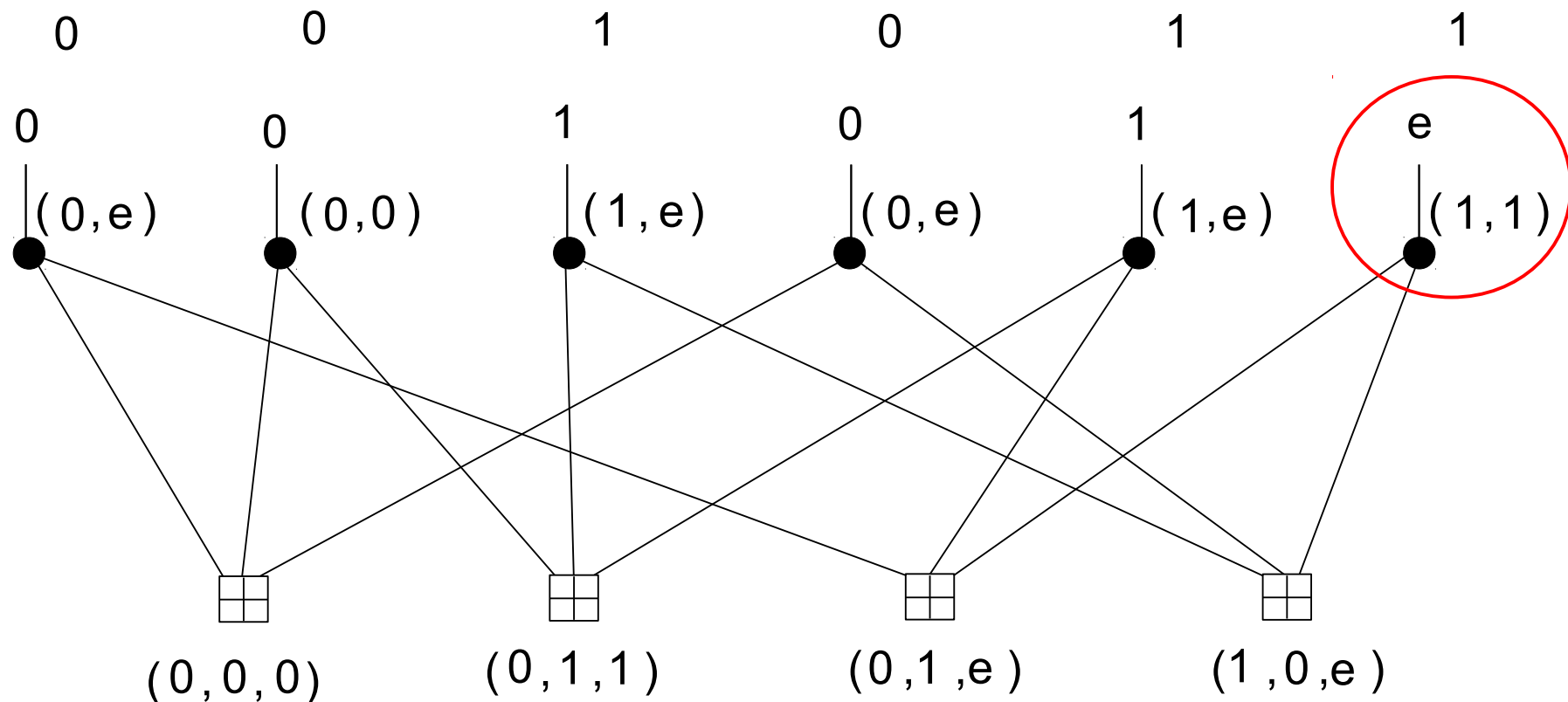
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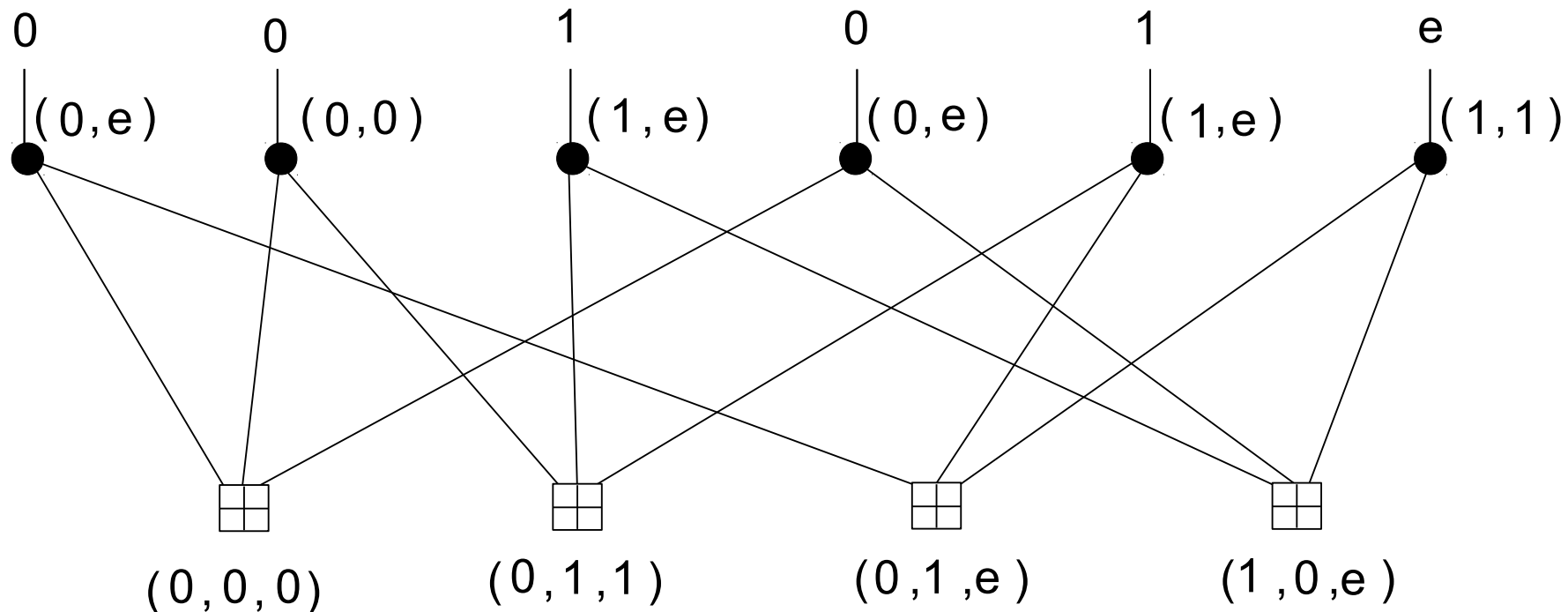
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## Iteration 2: bit processing

decoded codeword

0	0	1	0	1	1
---	---	---	---	---	---



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- The corresponding  $(J, K)$ -regular LDPC code ensemble is the set of all such parity-check matrices for a fixed  $J, K$ , and  $n$ .

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1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1
1	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	1	0	0	0	1	0	0	0
0	0	0	1	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0	0	1	0	0
0	1	0	0	0	0	1	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	1	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	1	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	1

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0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1
1	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	1	0	0	0	1	0	0	0
0	0	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0	0	1	0	0
0	1	0	0	0	0	1	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	1	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	1	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	1

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# Code Ensembles

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0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1
1	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0	0	1	0	0
0	1	0	0	0	0	1	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	1	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	1	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	1

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0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1
1	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0	0	1	0	0
0	1	0	0	0	0	1	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	1	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	1	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	1

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0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1
1	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0	1	0	0	0
0	1	0	0	0	0	1	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	1	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	1	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	1

$$n = 20, J = 3, K = 4$$

- $n=20$  cols.,  $J=3$  submatrices
- First submatrix contains descending blocks of four 1's
- Other  $J-1=2$  submatrices are random column permutations of the first submatrix
- The code ensemble contains all such parity-check matrices

- Sets of codes that share certain characteristics or properties are called **code ensembles**.
- Coding theorists often analyze **ensemble average** performance. Typically, one aims to show that **'good' codes occur with high probability in the ensemble**, i.e., almost all codes are good.

**Example:** Gallager's  $(J, K)$ -regular LDPC code ensemble

1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1
0	1	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0
1	0	0	0	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0
0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	0	0	1	0	0	1	0	0	0	0	0	1	0	0	0	0	0	0	1
0	1	0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	1	0
1	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	1	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	1	0

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0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	
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1	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	1	0	0	0	
0	0	0	1	0	0	0	0	0	0	1	0	1	0	1	0	0	0	0	0	
0	0	0	0	0	0	0	1	1	0	0	1	0	0	0	1	0	0	0	0	
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0	1	0	0	0	1	0	0	0	0	0	1	0	0	0	1	0	0	0	0	
0	0	1	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	1	0	
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- Gallager proved that for  $J > 2$  and large block length  $n$ , the minimum distance typical of most members of a  $(J,K)$ -regular LDPC code ensemble is bounded below as  $\delta_{min}n$ , where  $\delta_{min} > 0$  is a constant that depends on  $J$  and  $K$  (not  $n$ ), i.e.,  $d_{min} \geq \delta_{min}n$ .

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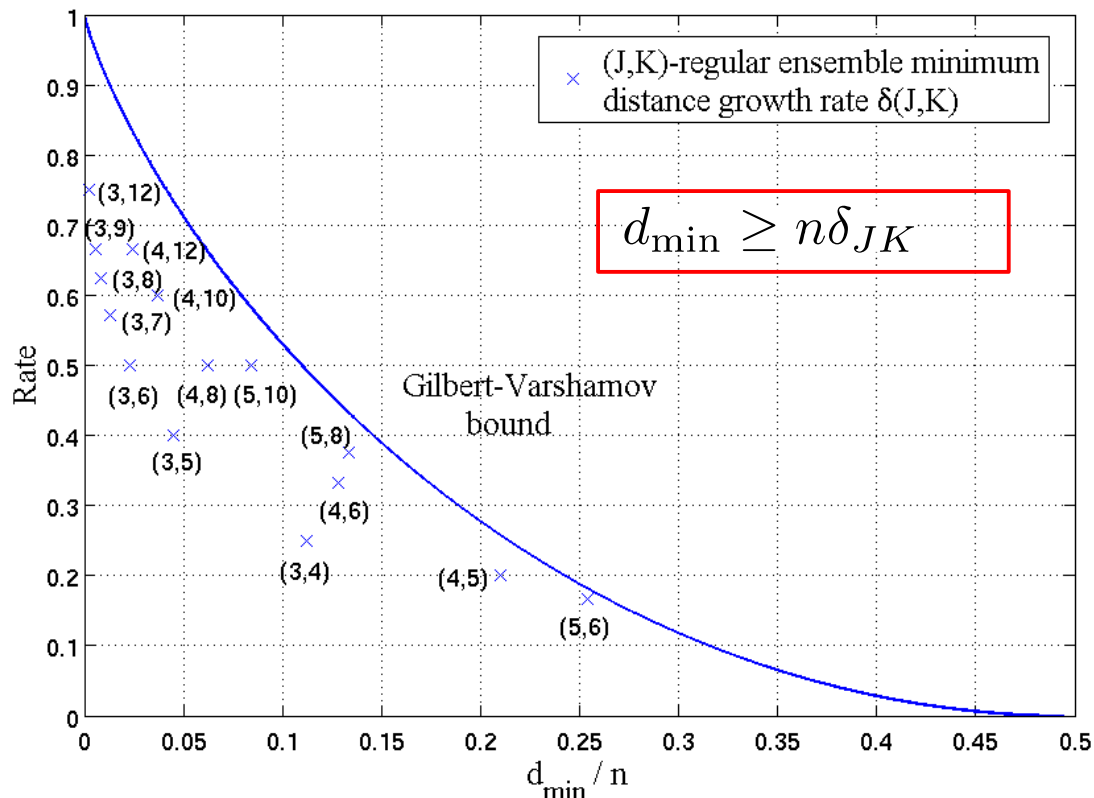
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- Such code ensembles are called **asymptotically good**
  - ➔ Under ML decoding, we will achieve excellent performance

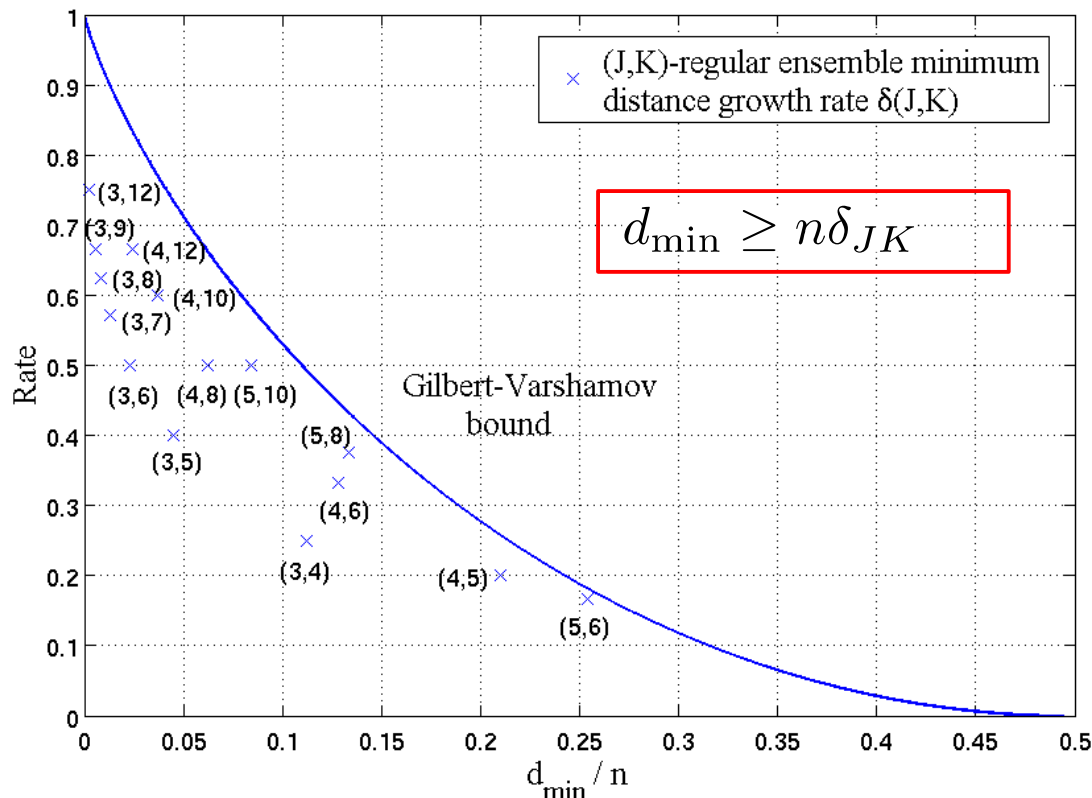
# Code Ensembles – Minimum Distance Growth Rates

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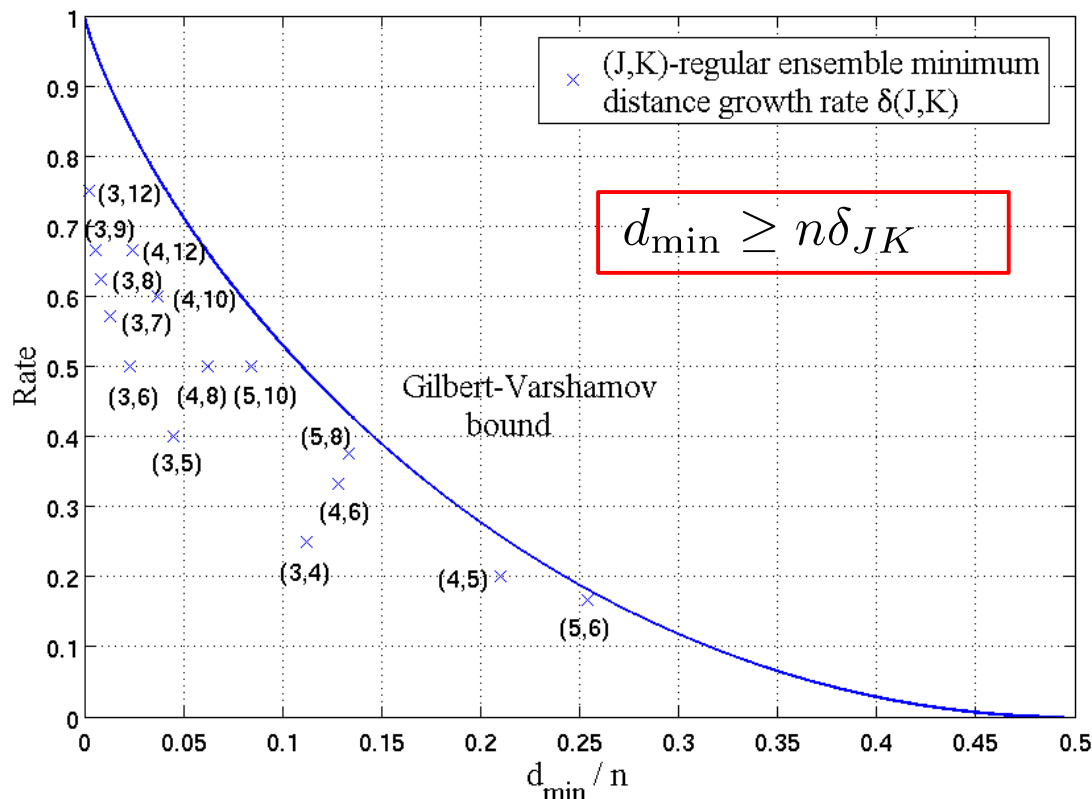
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- An ensemble with minimum distance growth rate strictly greater than zero is called **asymptotically good**, in the sense that, as the block length tends to infinity, the minimum distance grows linearly with block length.
- As the **density** ( $J$  and  $K$ ) of  $(J,K)$ -regular ensembles **increases**, the minimum distance growth rate **approaches the Gilbert-Varshamov bound** for the ensemble of all block codes.

- We use **density evolution** (DE) to determine for which channel noise levels the message passing decoder can correct errors and for which levels it cannot.

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We assume:

- (1) An ensemble of Tanner graphs
  - (2) The channel is memoryless
  - (3) The graphs are cycle free
- For a given code ensemble we determine an **iterative decoding threshold** – the maximum level of noise that can possibly be corrected by a **particular code ensemble**

## DE for (J,K)-regular ensembles:

- Messages are '1', '0', or 'e'
- Let  $q^{(l)}$  be the probability that a check-to-bit message is an 'e' at iteration  $l$
- Let  $p^{(l)}$  be the probability that a bit-to-check message is an 'e' at iteration  $l$

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- This implies that the probability that **at least one** incoming message is and 'e' is

$$q^{(l)} = 1 - (1 - p^{(l)})^{K-1}$$

- At iteration  $l$ , a message from bit-to-check is an 'e' if
  - (1) The original message from the channel is an 'e', **and**
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- We can use this method to determine for what values of  $\varepsilon$  the message passing decoder is likely to correct erasures and how many iterations are necessary!

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- Then:

$$p^{(0)} = 0.3000 \quad p^{(5)} = 0.0098$$

$$p^{(1)} = 0.2076 \quad p^{(6)} = 0.0007$$

$$p^{(2)} = 0.1419 \quad p^{(7)} = 0.0000$$

$$p^{(3)} = 0.0858$$

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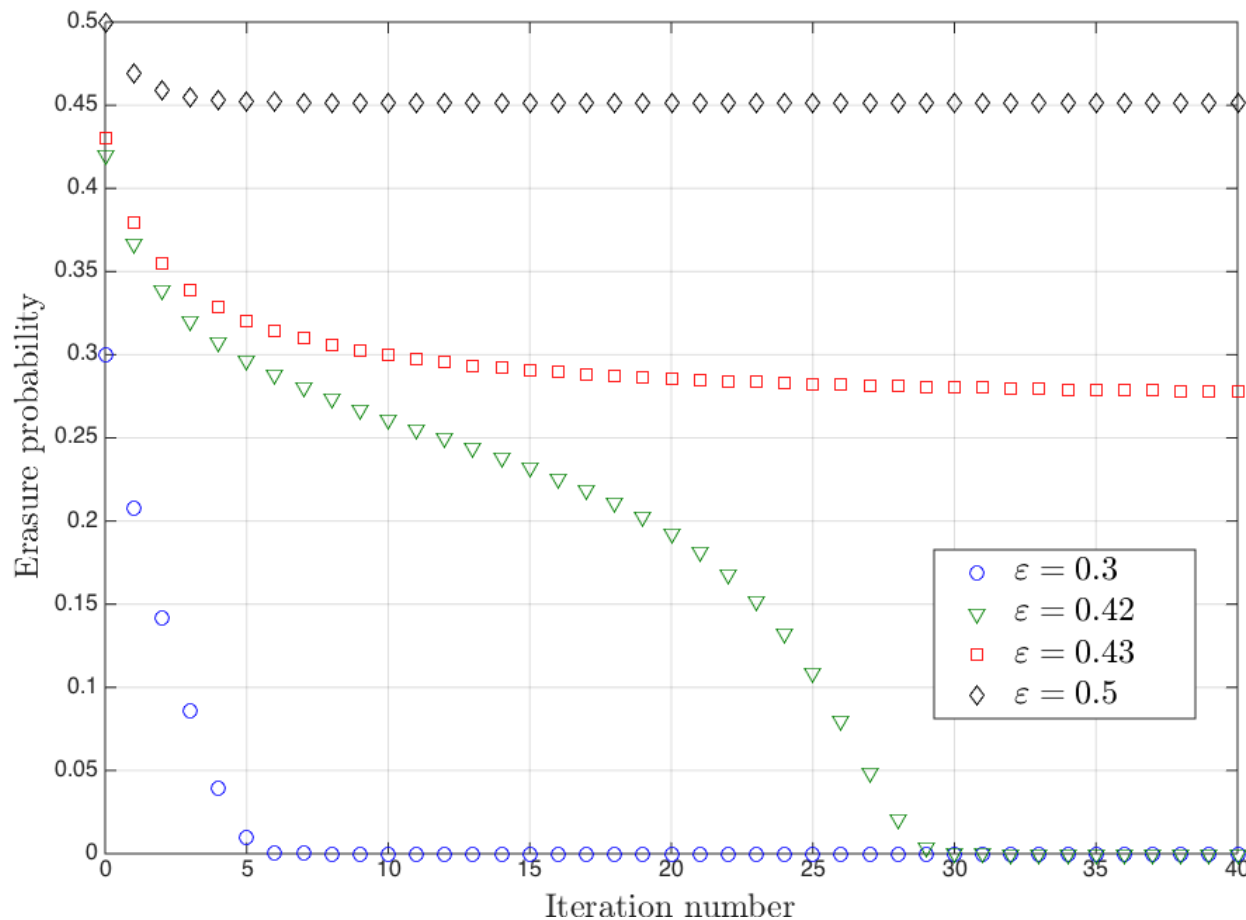
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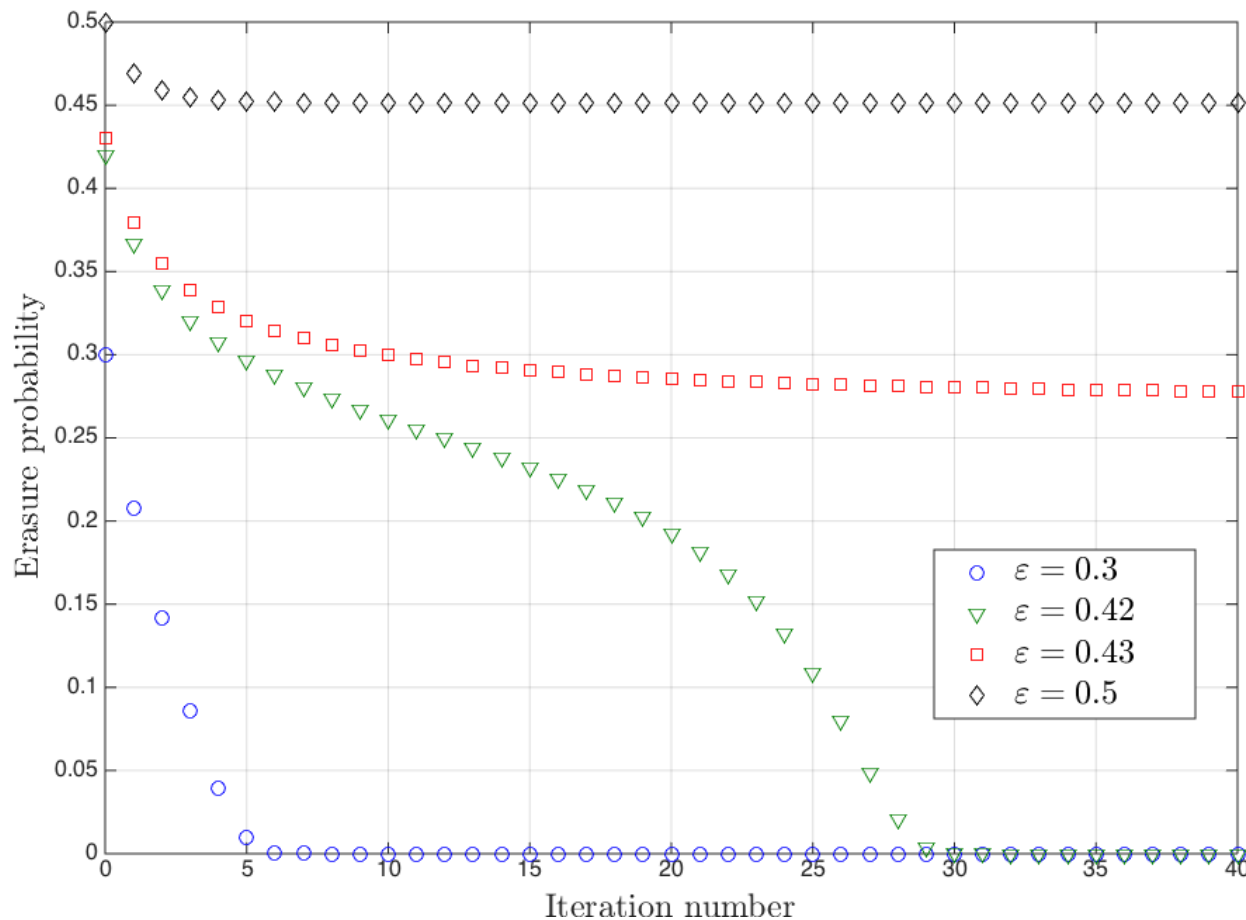
➔ After **7 iterations**, the erasure probability in a codeword will go to 0 for a (3,6)-regular LDPC code (with no 14-cycles or less) transmitted over a BEC with  $\varepsilon = 0.3$

# Example (cont.)



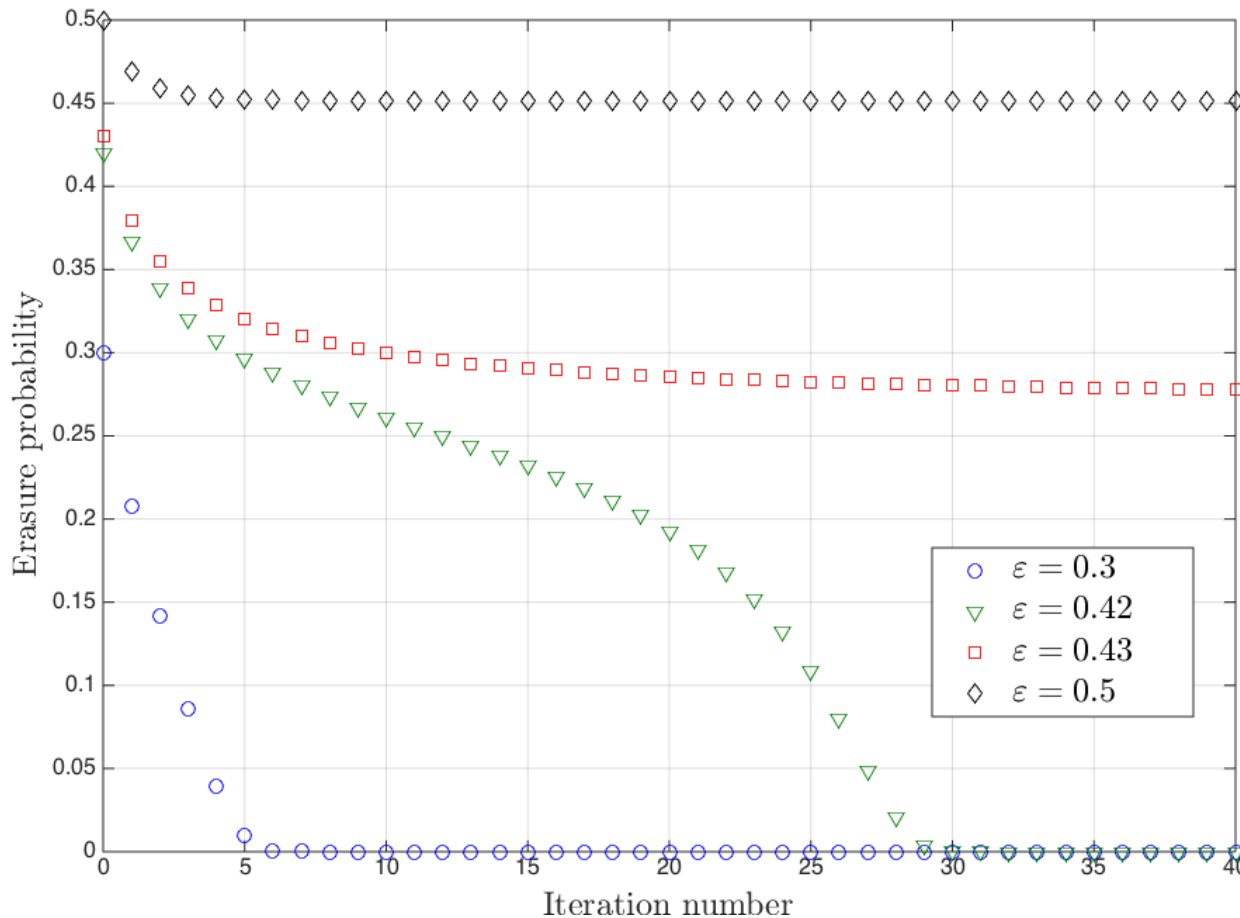
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- For (3,6)-regular LDPC codes, this value is  $\epsilon^* = 0.429$ .  $\epsilon^*$  is called the **iterative decoding threshold**.



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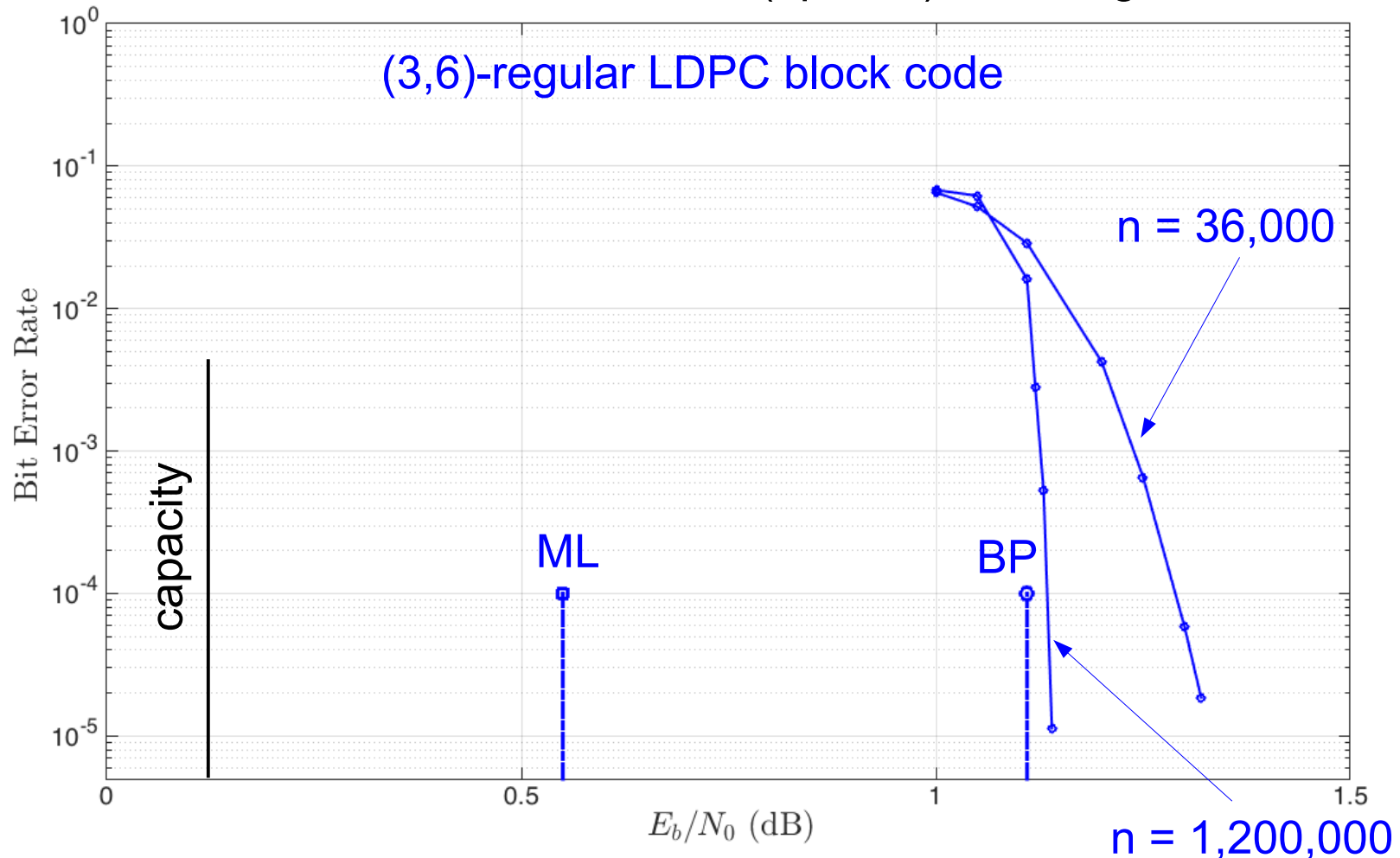
so the probability that a message is a particular LLR value is described by a probability density function (PDF).

- ➔ We track the PDFs over iterations and determine if the probability of error goes to zero as the number of iterations goes to infinity for a particular channel SNR.

# Example: AWGNC

BP = iterative (suboptimal) decoding threshold

ML = maximum likelihood (optimal) decoding threshold



# Thresholds of $(J,K)$ -regular LDPC Code Ensembles

- The iterative decoding thresholds can be calculated for a variety of  $(J,K)$ -regular LDPC code ensembles using DE.

## BEC thresholds

$J$	$K$	Rate	$\varepsilon^*$	$\varepsilon_{\text{Sh}}$
3	6	0.5	0.429	0.5
4	8	0.5	0.383	0.5
5	10	0.5	0.341	0.5
3	5	0.4	0.517	0.6
4	6	0.333	0.506	0.667
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- There exists a relatively **large gap to capacity**.
- Iterative decoding thresholds get **further from capacity** as the graph **density increases**. (Minimum distance growth rates improve.)

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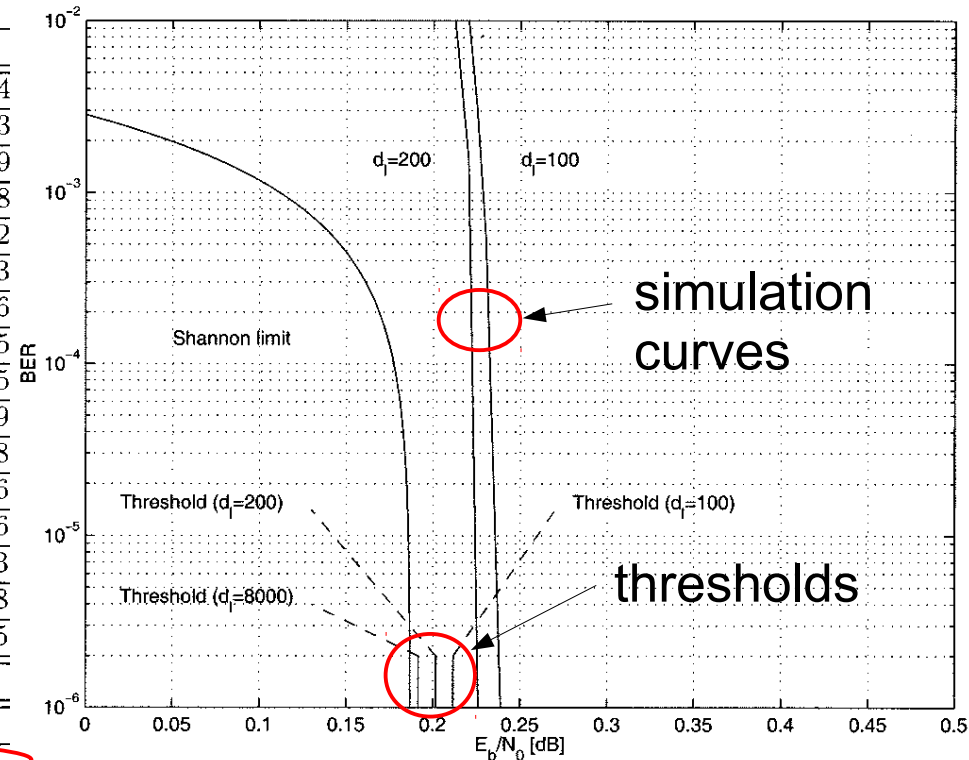
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- Check node irregularity is not essential to improve thresholds. Typically good code designs have one or two different check node degrees.
- Trying all possible degree distributions is not practical. Optimization techniques such as iterative linear programming and differential evolution have been applied to find good degree distributions.

# Random Irregular LDPC Codes

- Good rate  $R = 1/2$  irregular LDPC codes: [CFRU01]

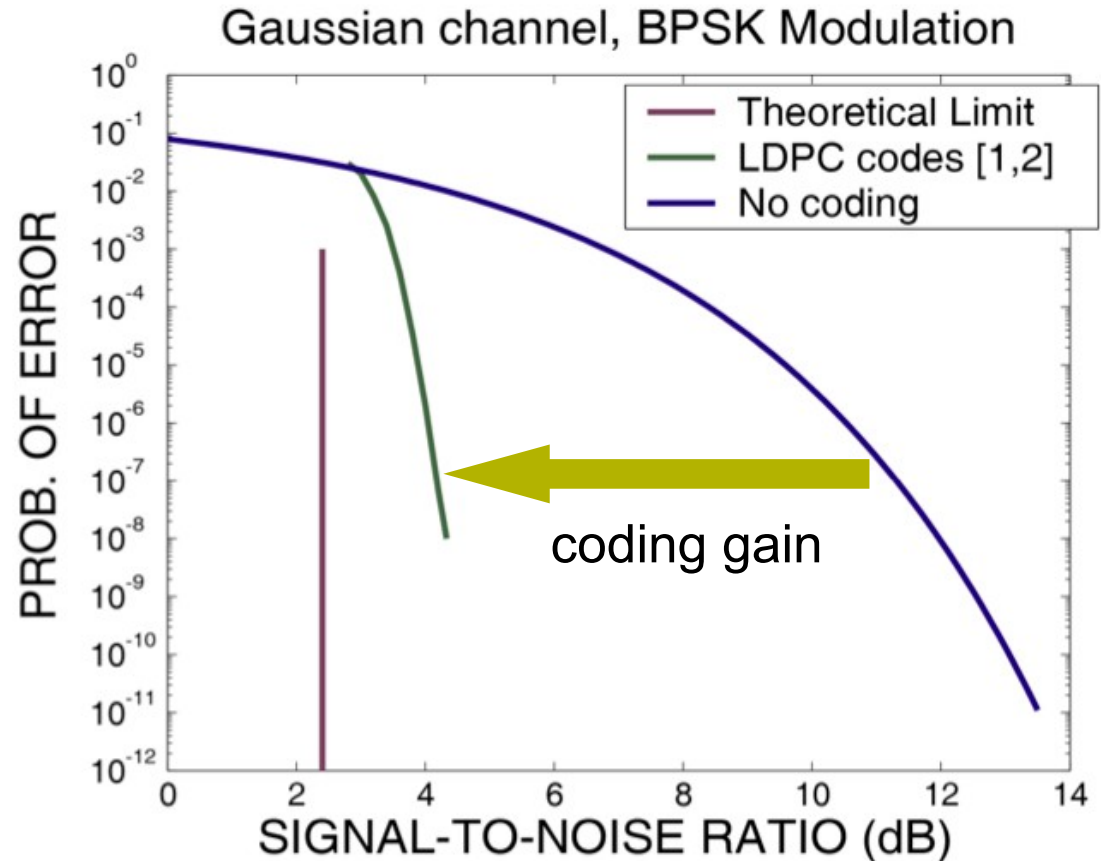
$d_l$	100		200		8000	
	$x$	$\lambda_x$	$x$	$\lambda_x$	$x$	$\lambda_x$
	2	0.170031	2	0.153425	2	0.096294
	3	0.160460	3	0.147526	3	0.095393
	6	0.112837	6	0.041539	6	0.033599
	7	0.047489	7	0.147551	7	0.091918
	10	0.011481	18	0.047938	15	0.031642
	11	0.091537	19	0.119555	20	0.086563
	26	0.152978	55	0.036379	50	0.093896
	27	0.036131	56	0.126714	70	0.006035
	100	0.217056	200	0.179373	100	0.018375
					150	0.086919
					400	0.089018
					900	0.057176
					2000	0.085816
					3000	0.006163
					6000	0.003028
					8000	0.118165
$\rho_{av}$	10.9375		12.0000		18.5000	
$\sigma$	0.97592		0.97704		0.9781869	
$SNR_{norm}$	0.0247		0.0147		0.00450	



[CFRU01] S.-Y. Chung, G. D. Forney, Jr., T. J. Richardson, and R. Urbanke, "On the Design of Low-Density Parity-Check Codes within 0.0045 dB of the Shannon Limit", *IEEE Comm. Letters*, vol. 5 no. 2, Feb. 2001.

# Error Floors of LDPC Codes

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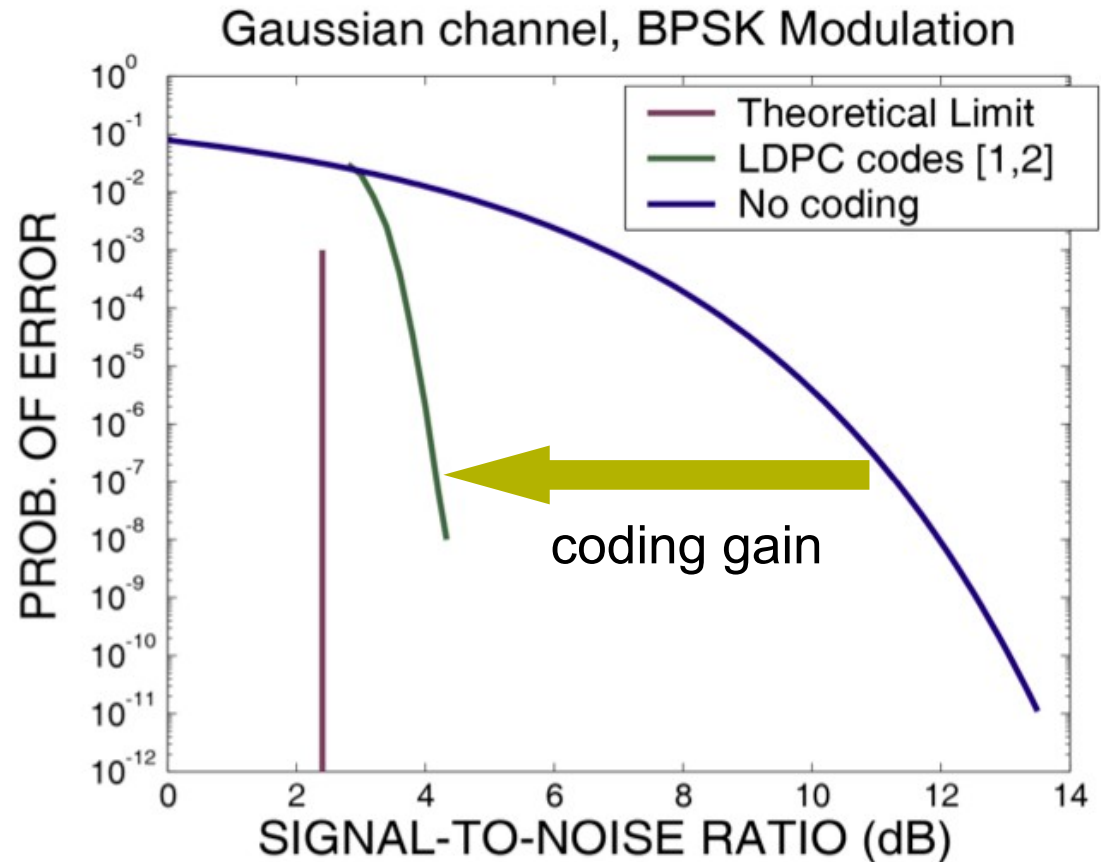


- [1] Djurdevic, et al., 2003
- [2] IEEE 802.3an

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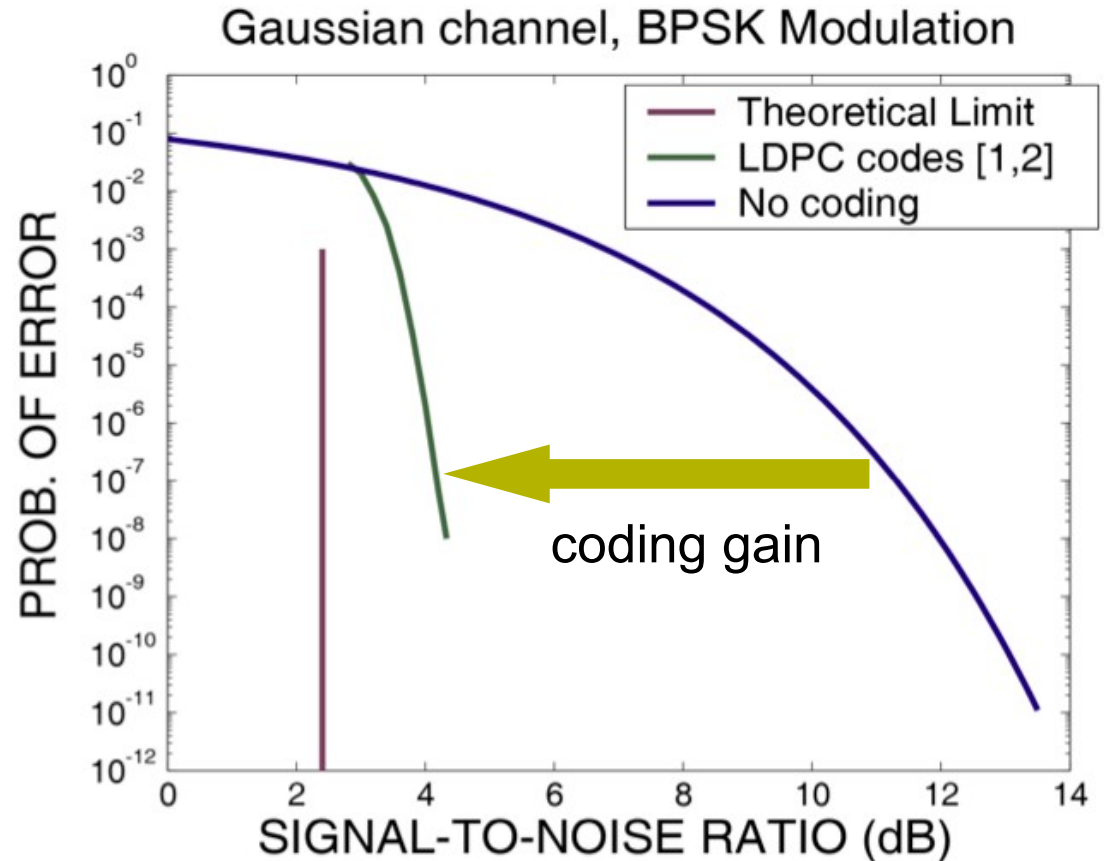
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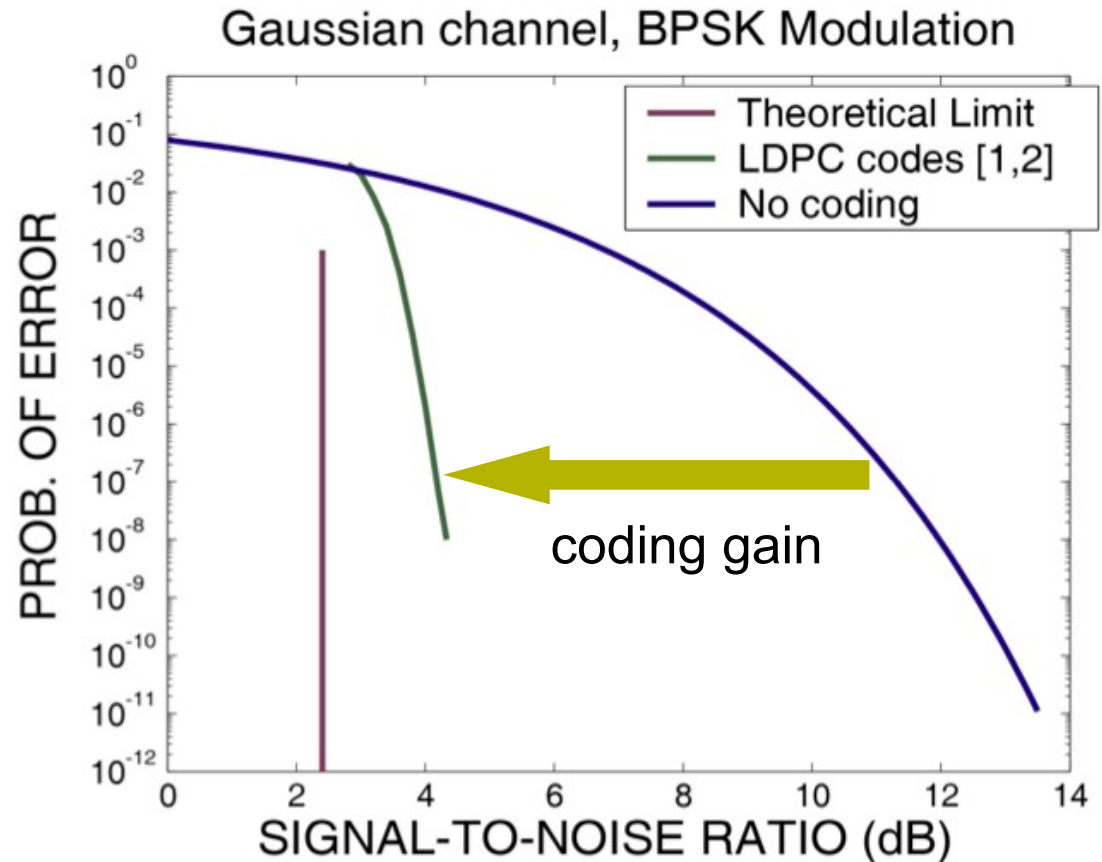


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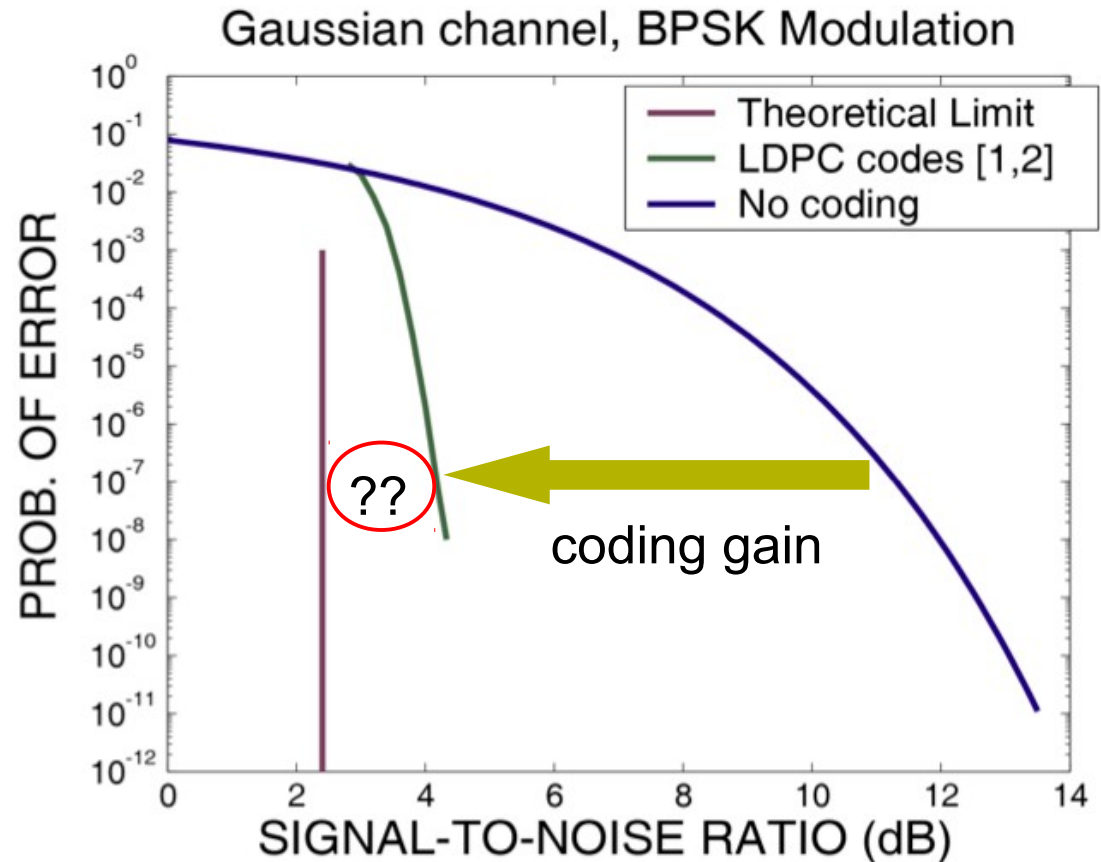
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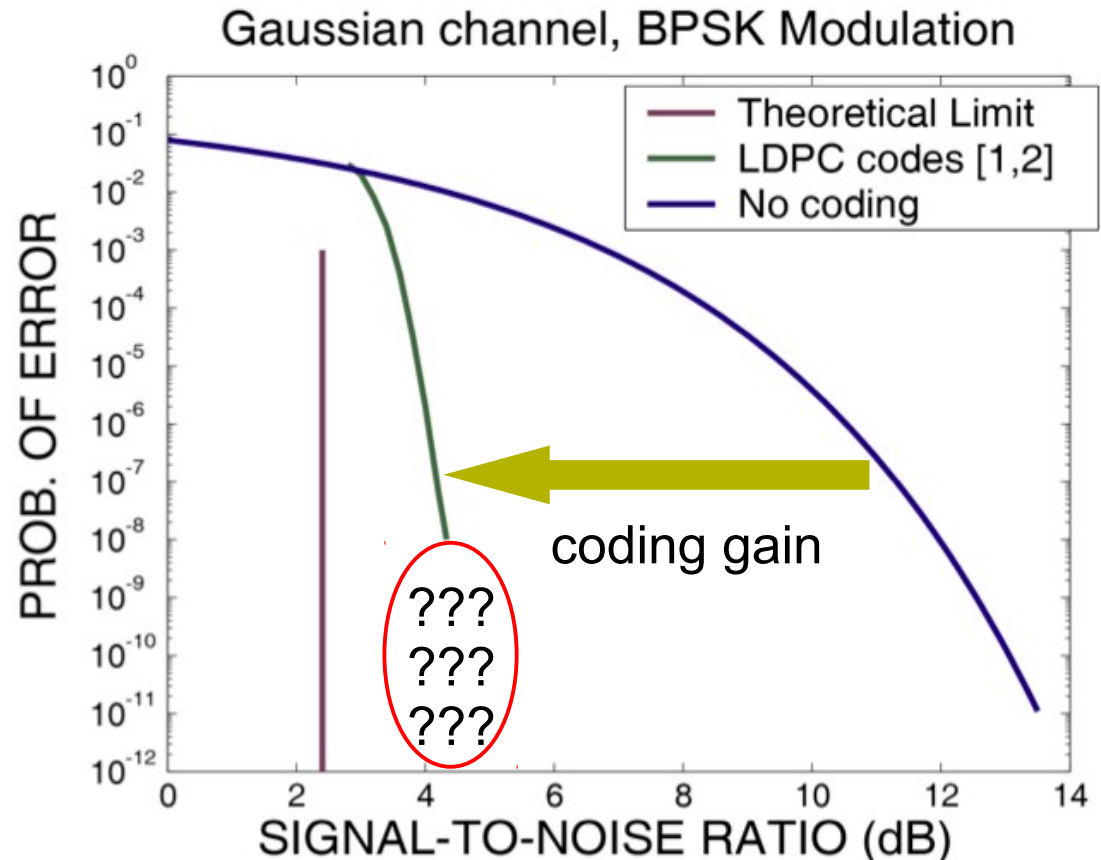
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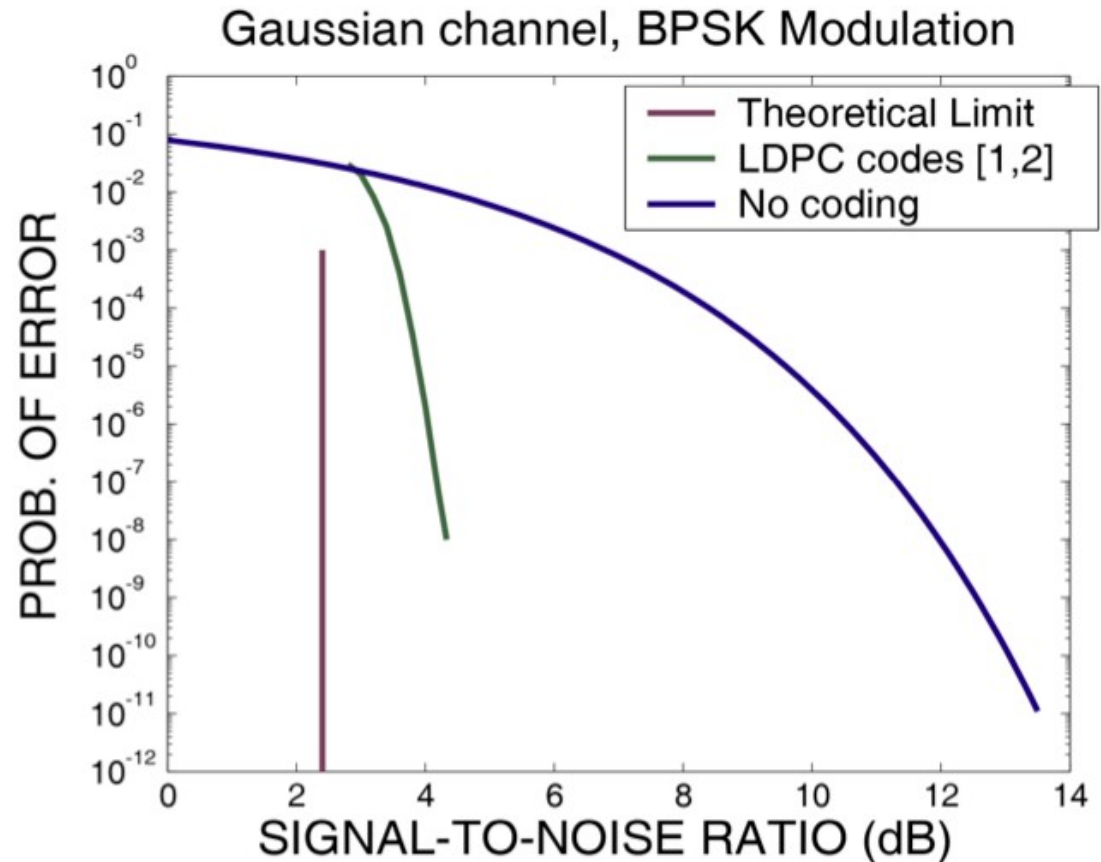
- ✗ how to approach capacity with acceptable implementation complexity and latency
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# What is hard about LDPC codes?

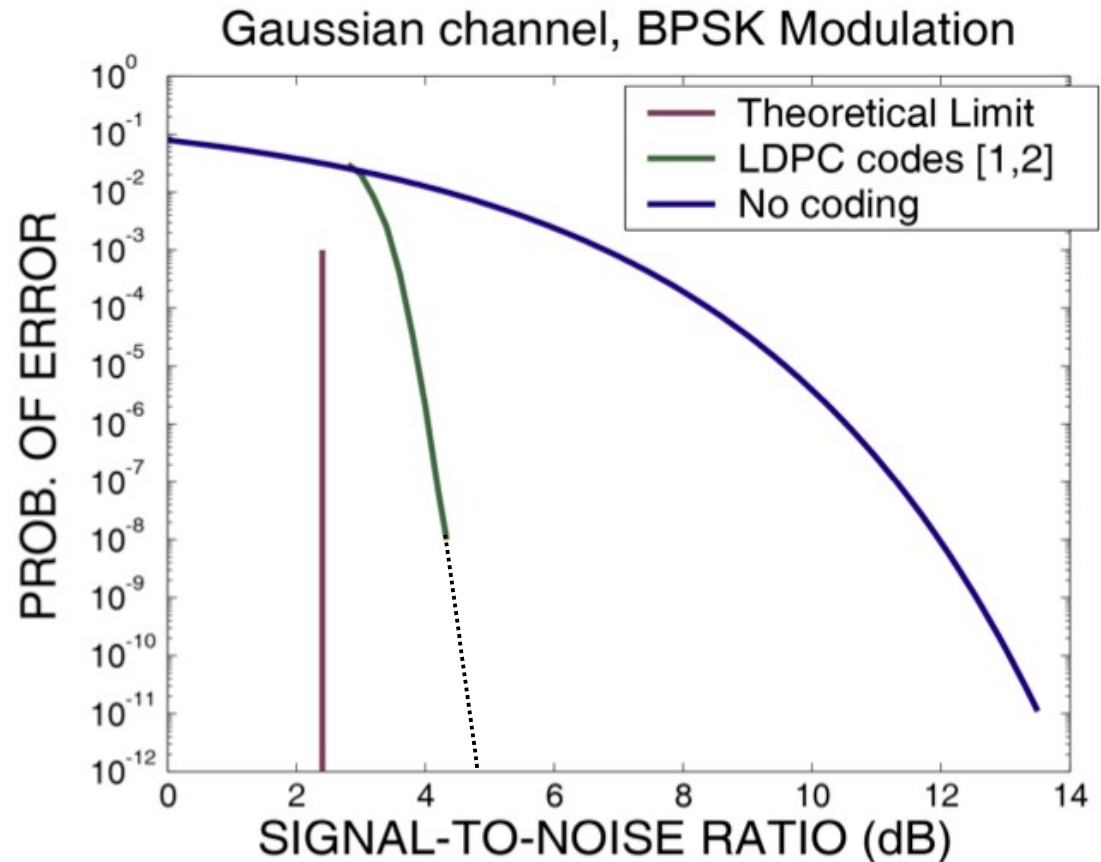
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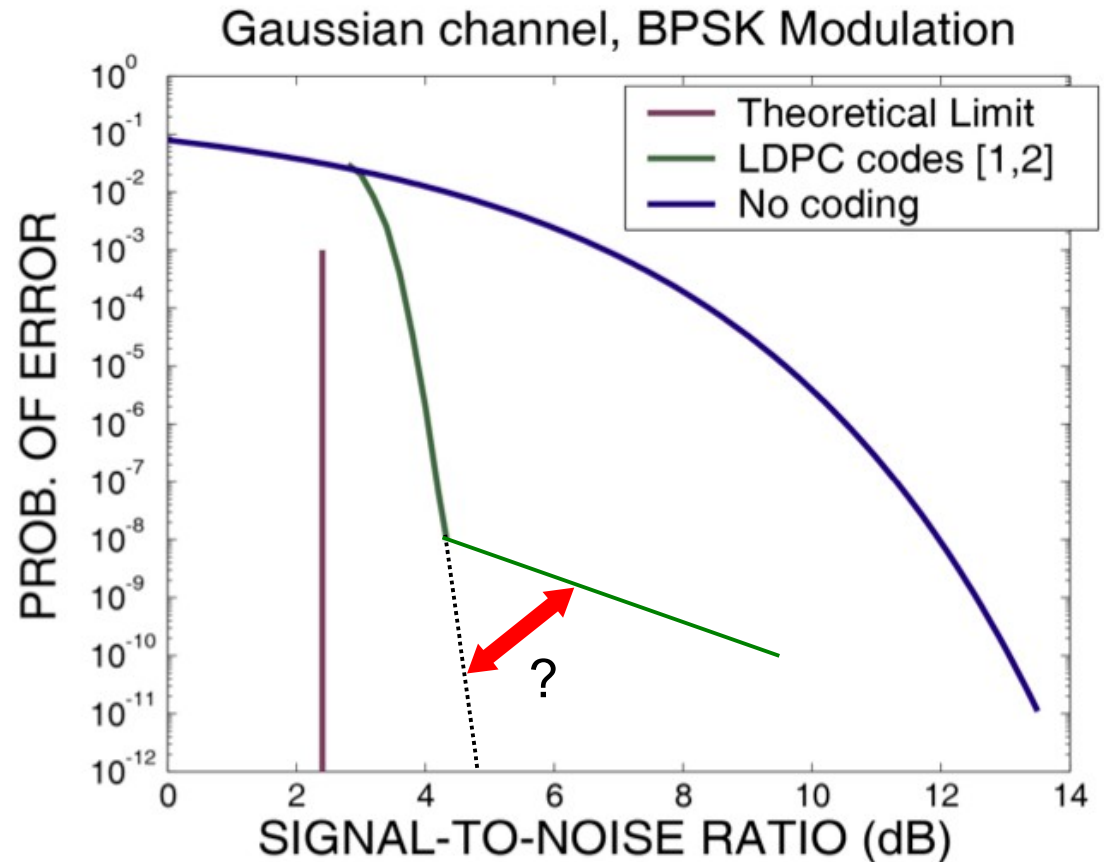
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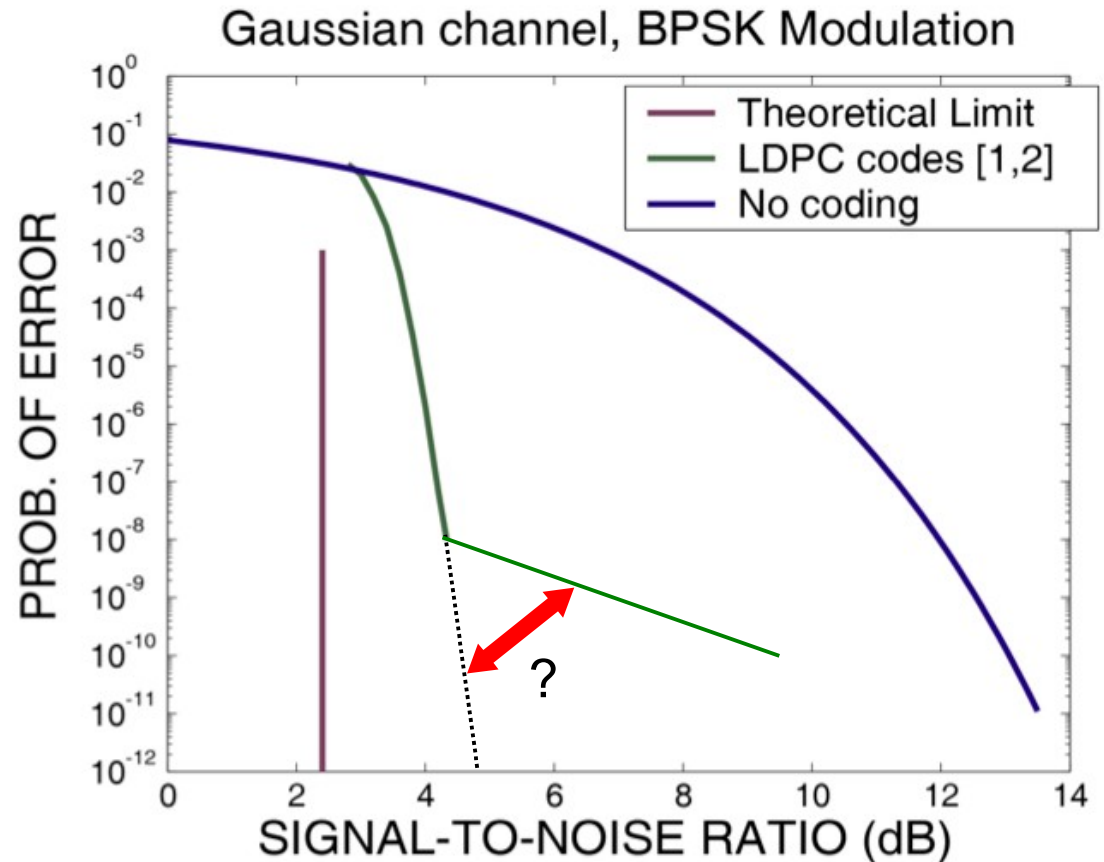
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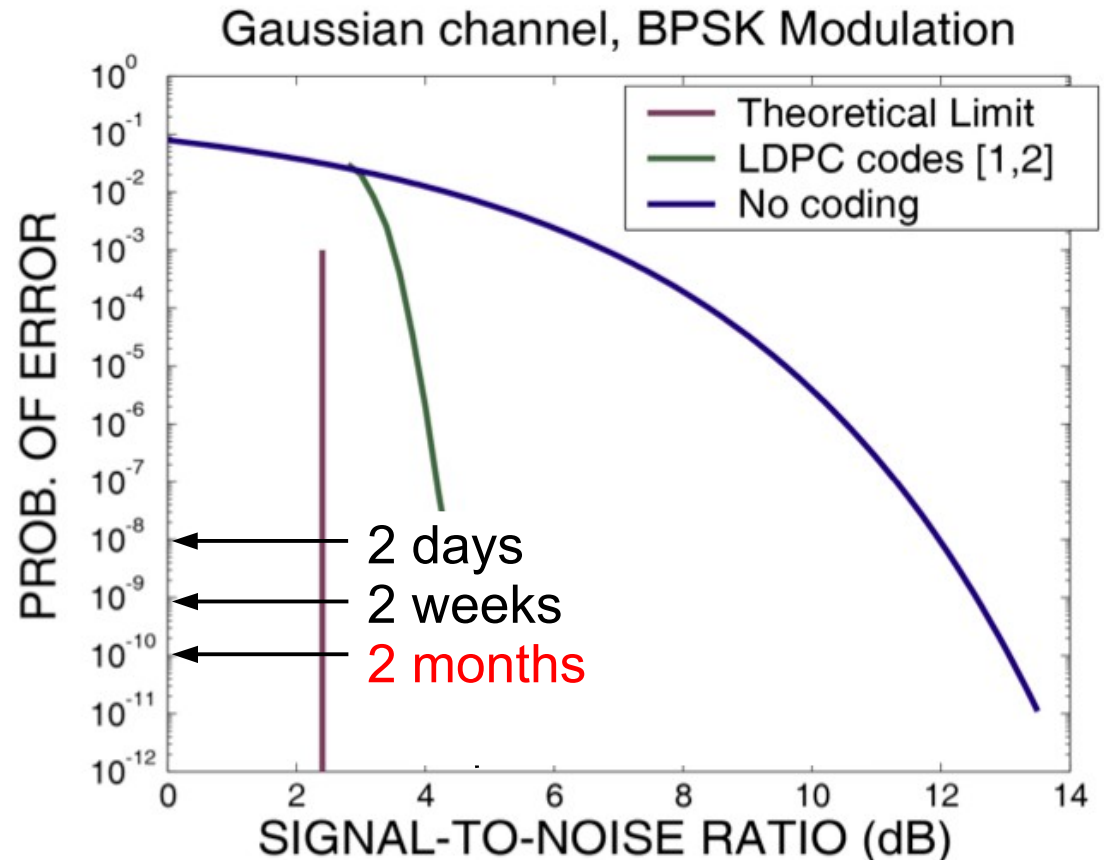
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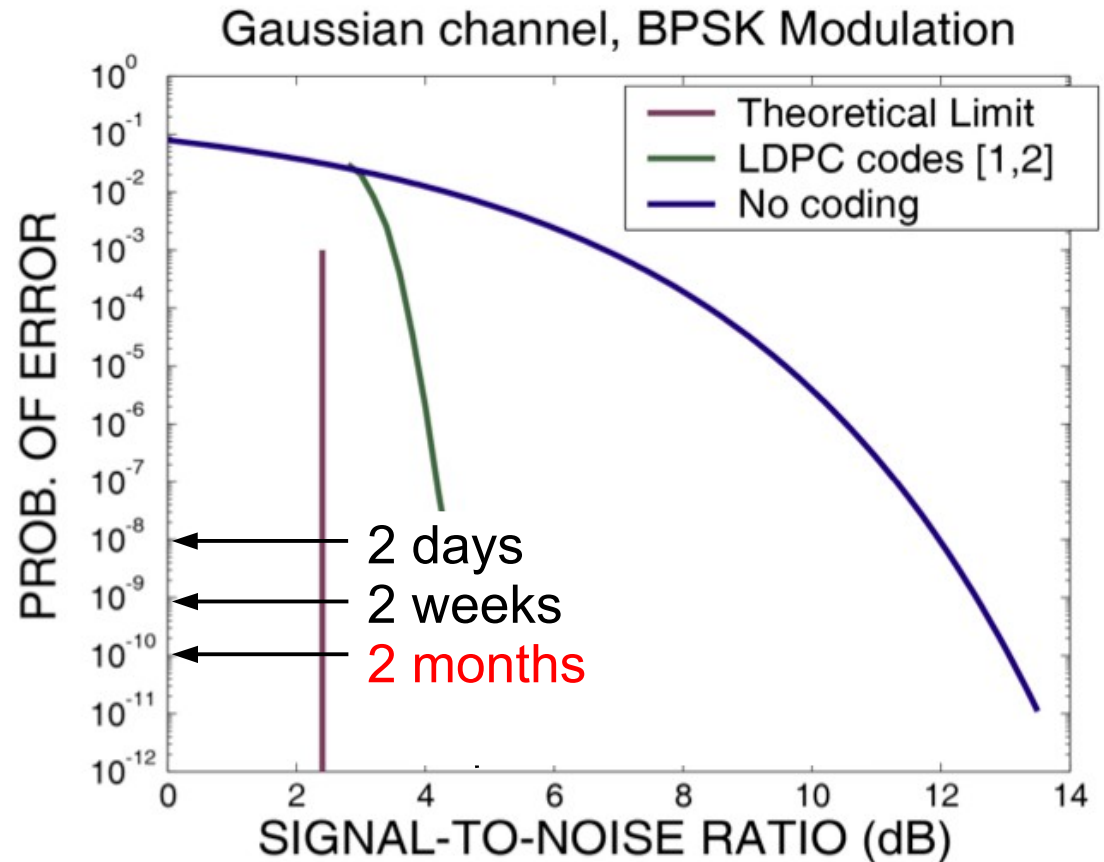


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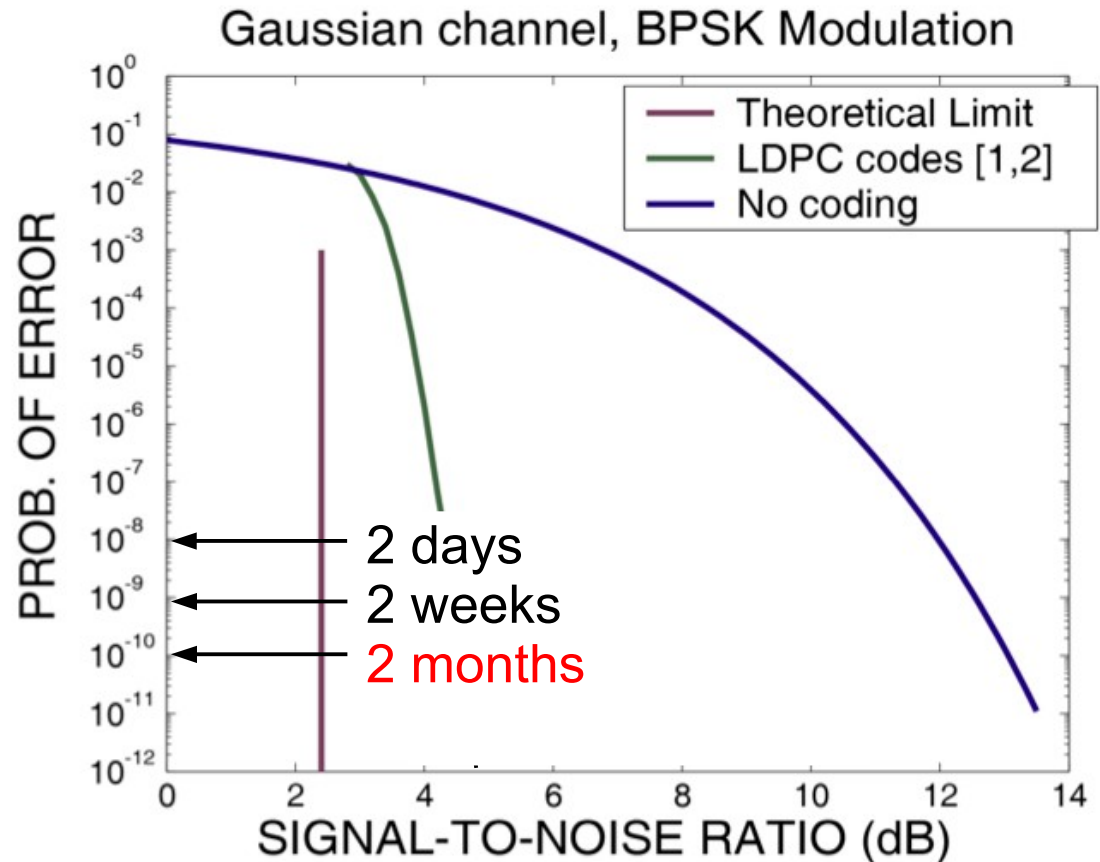
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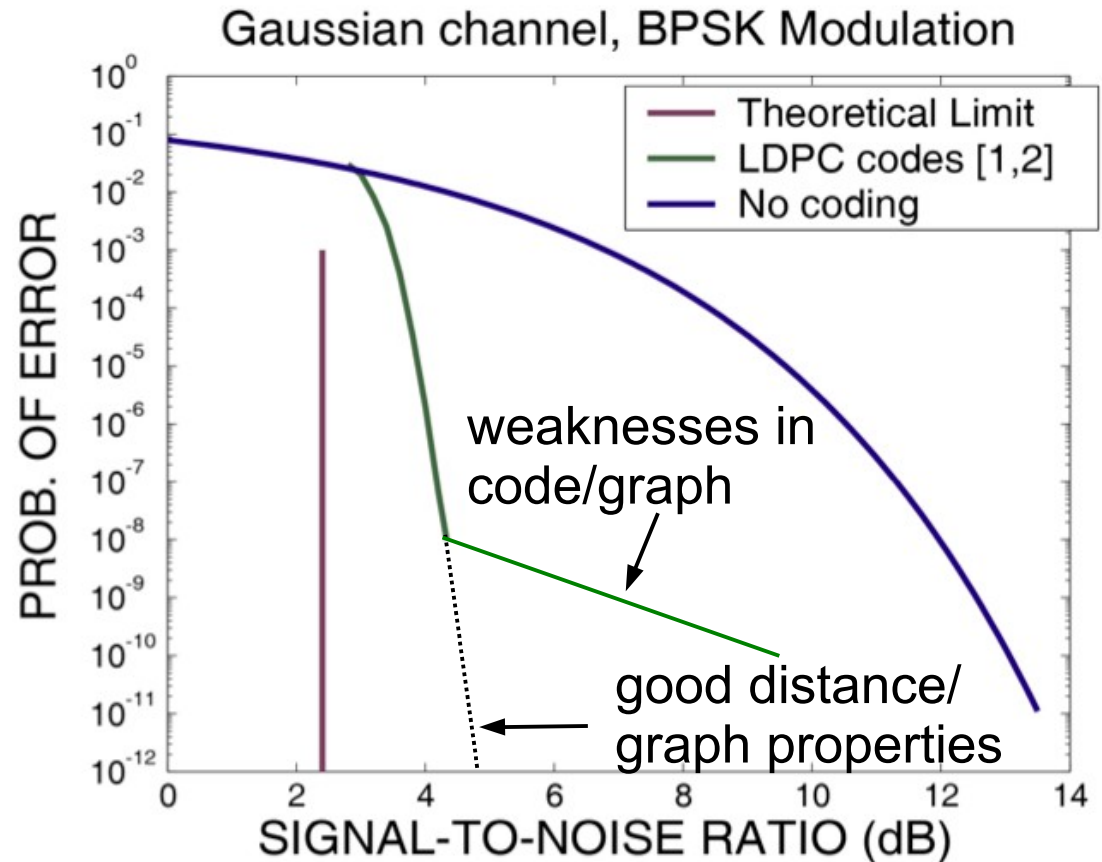
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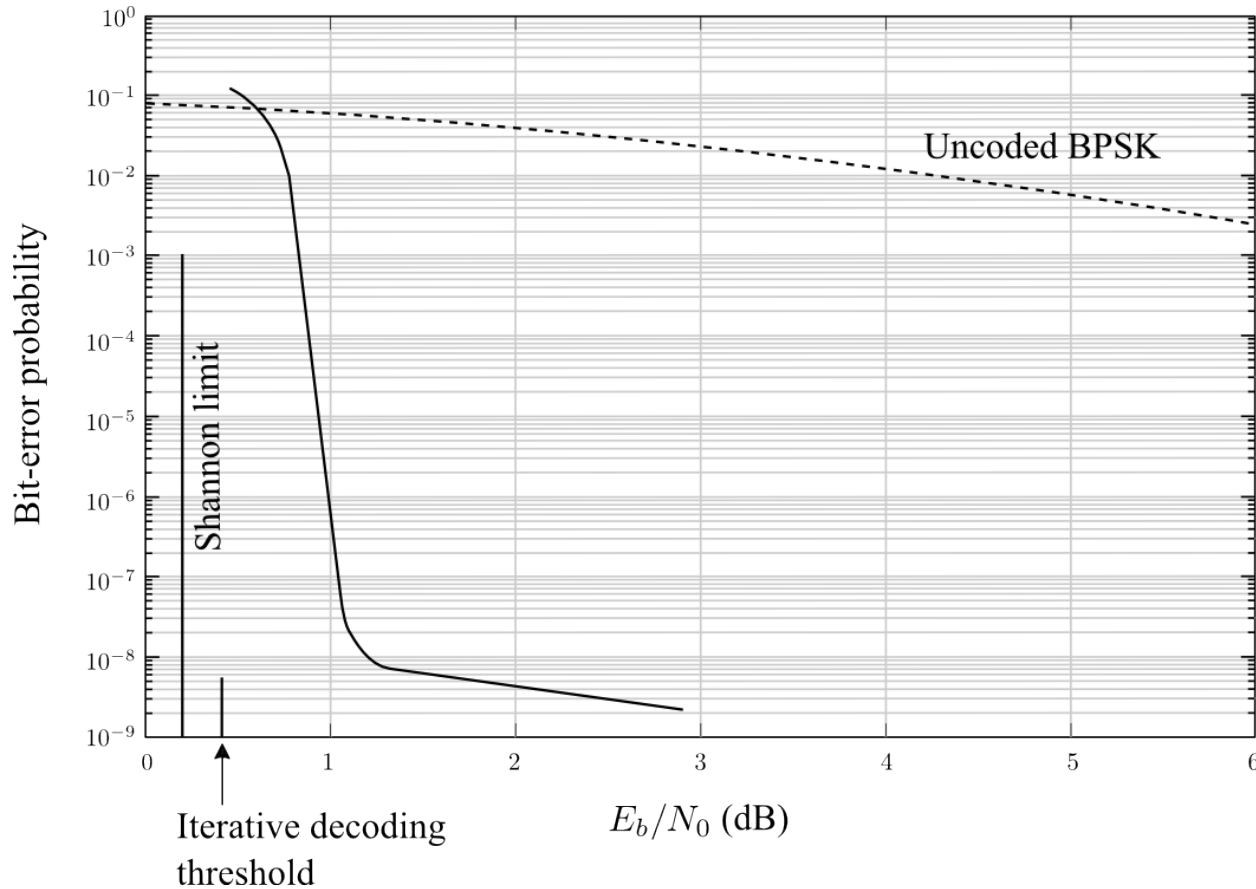
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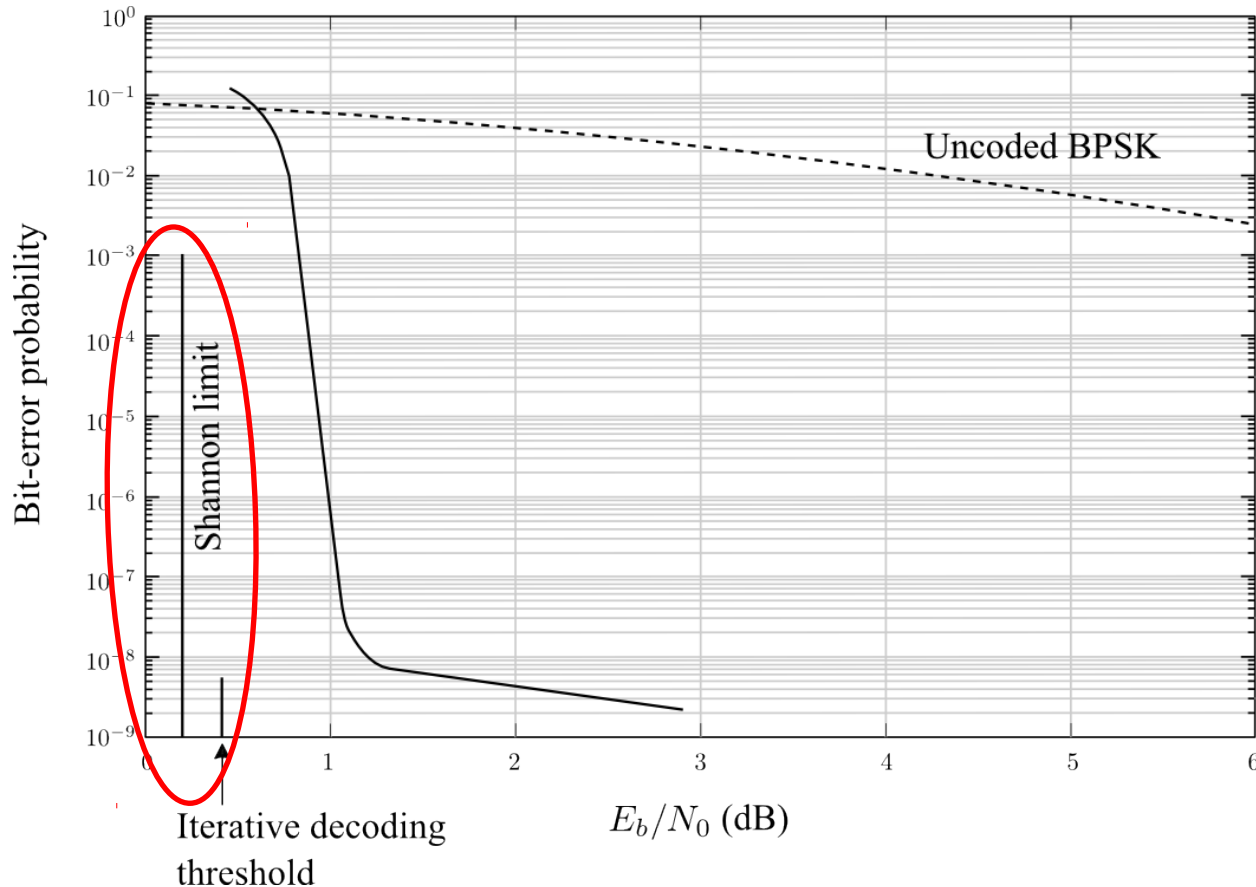
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# Typical LDPC Code Behavior



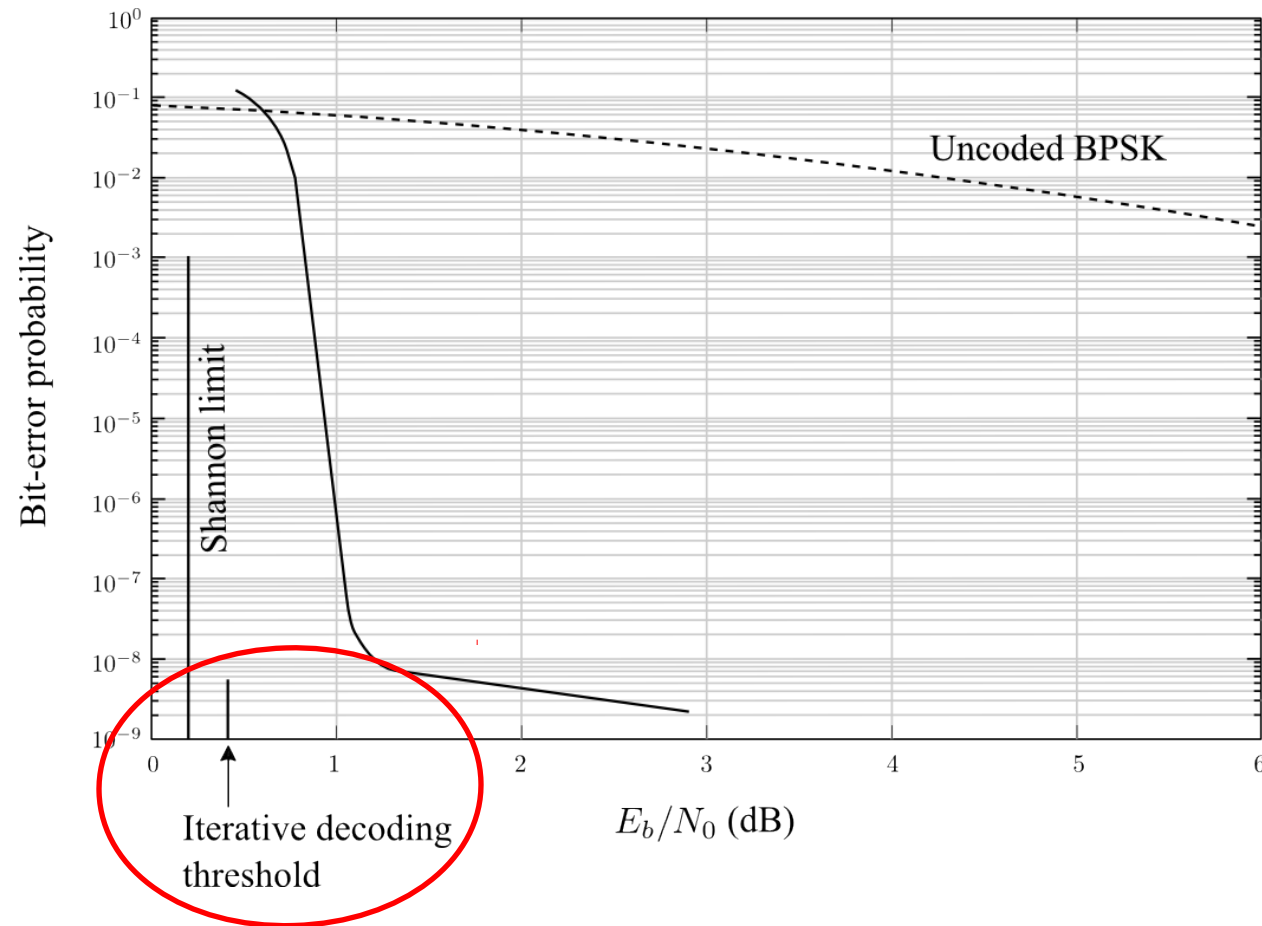
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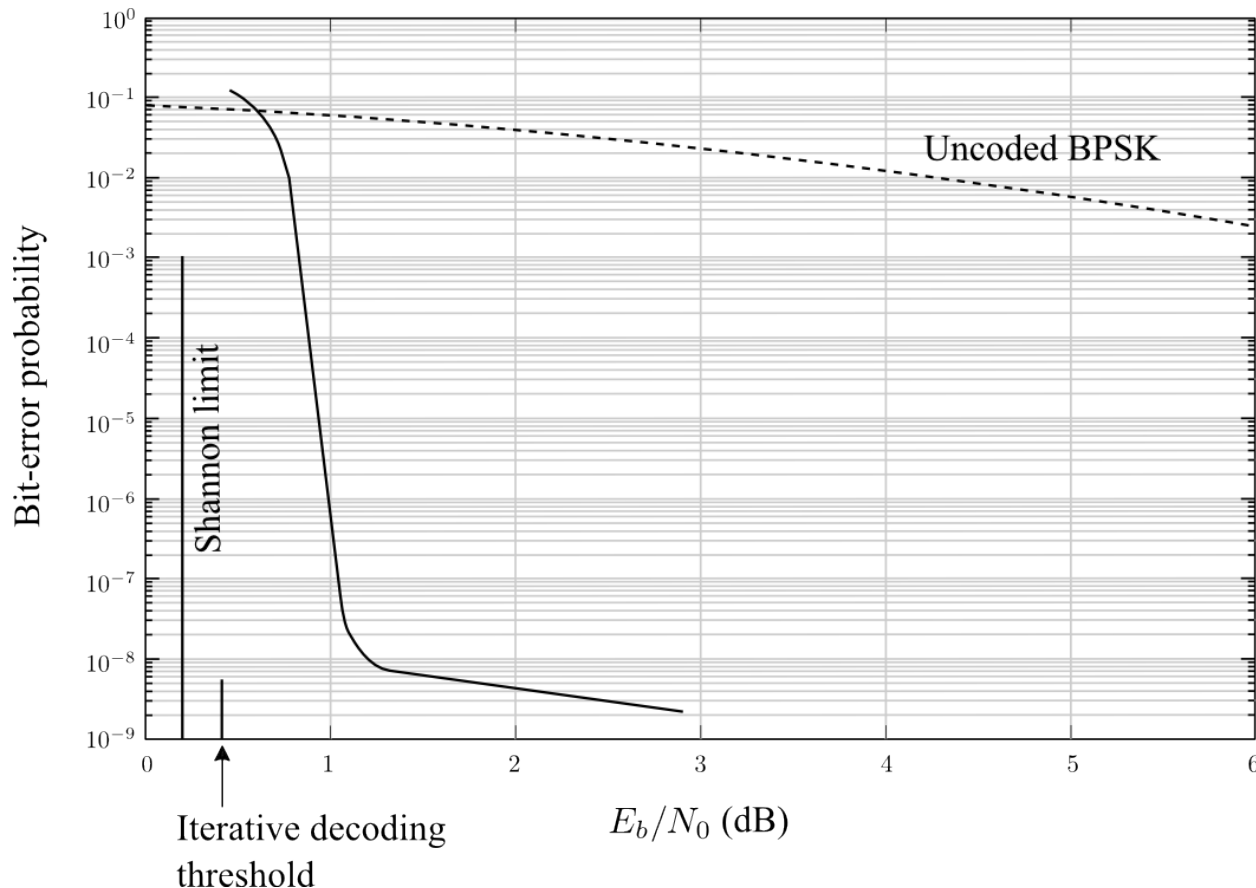
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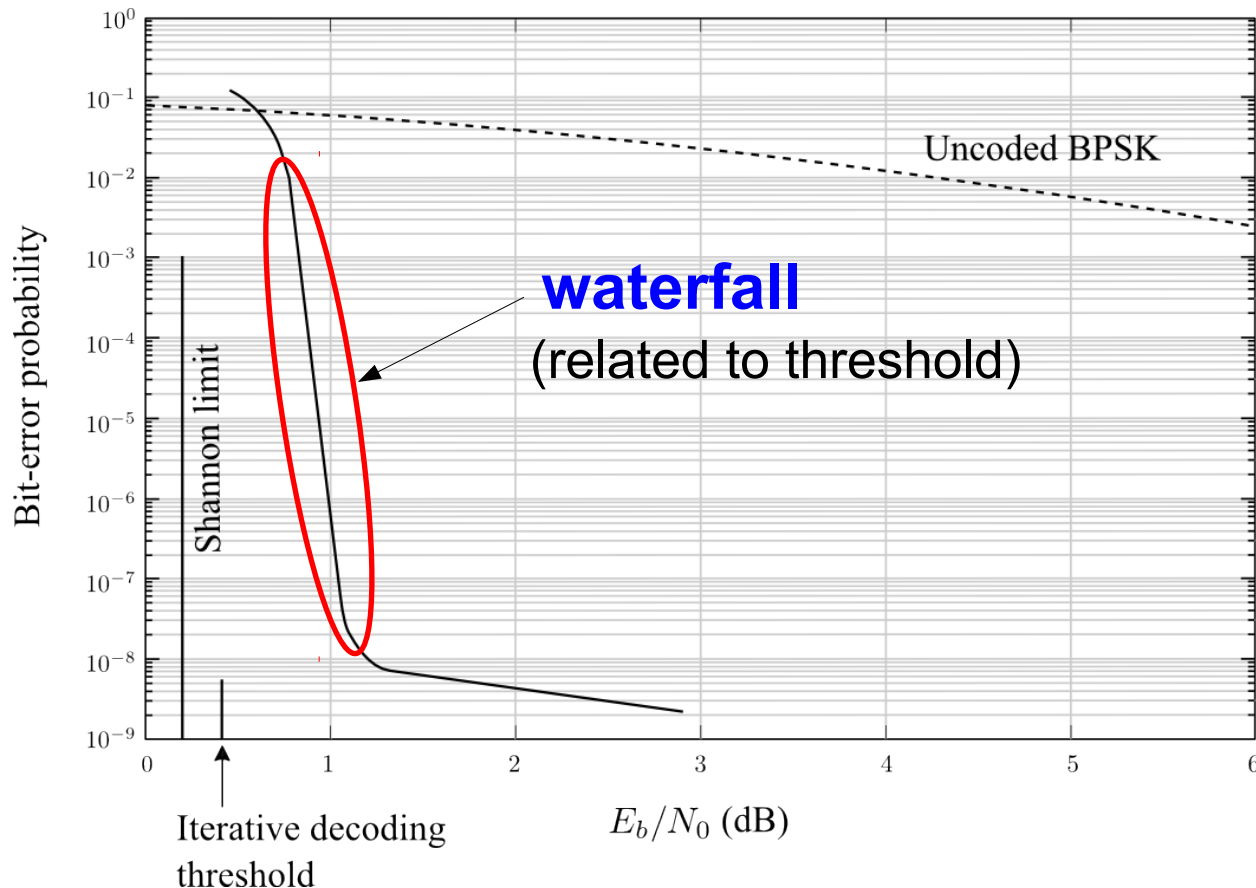
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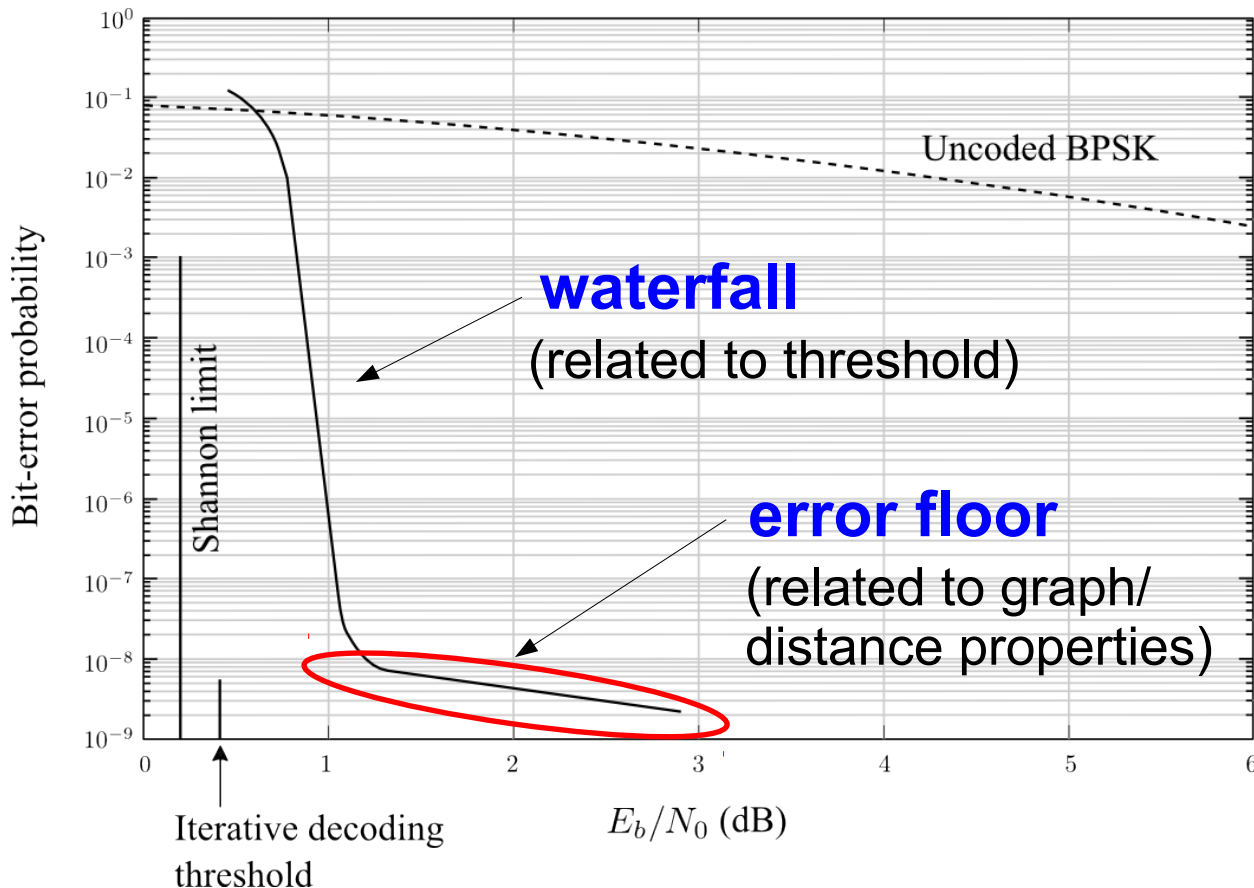


a **waterfall**



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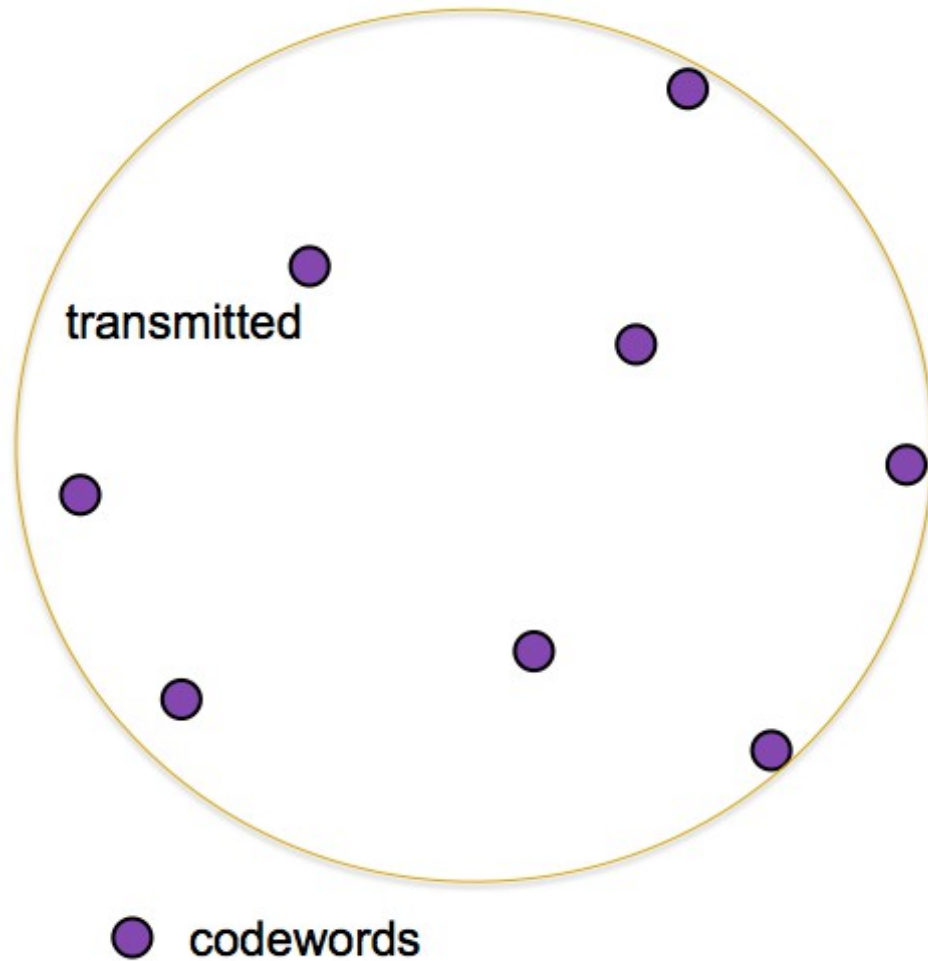


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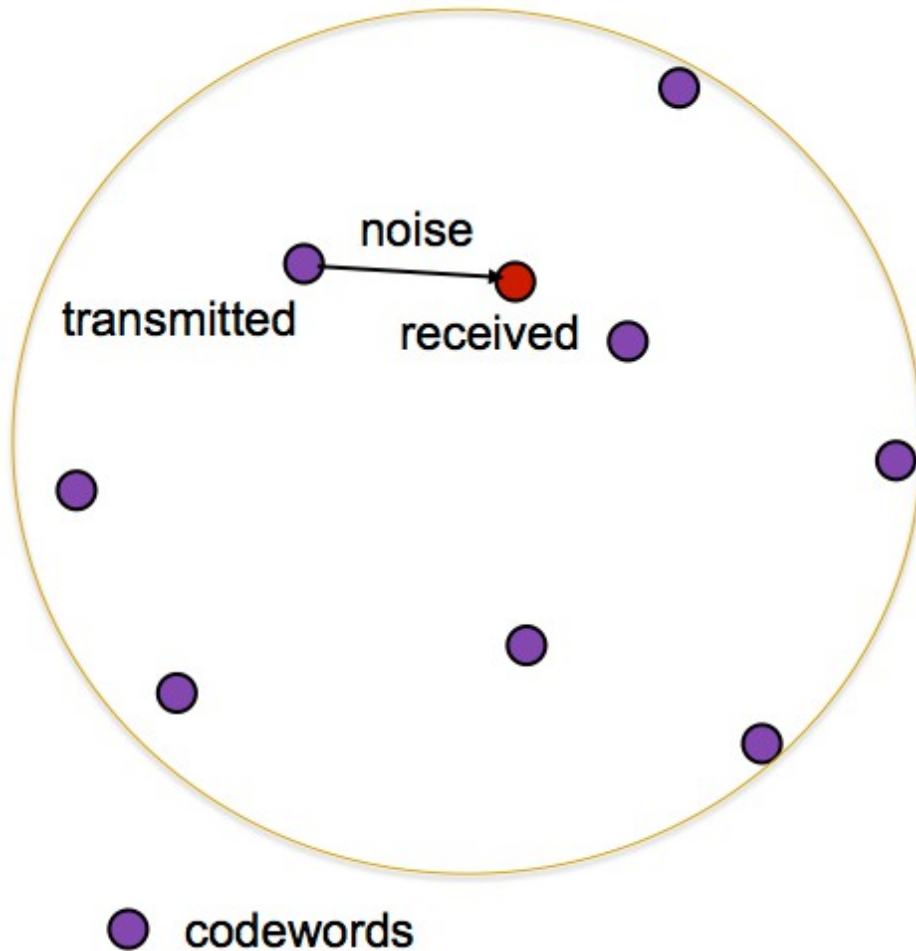


an **error floor**

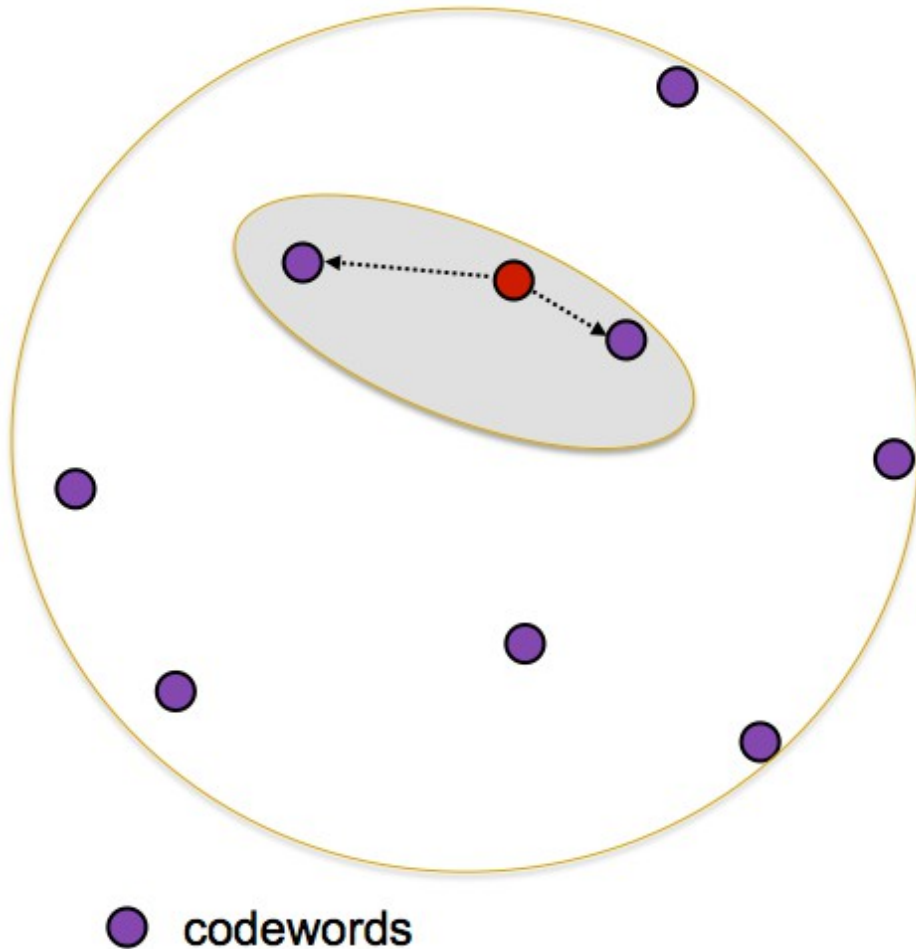
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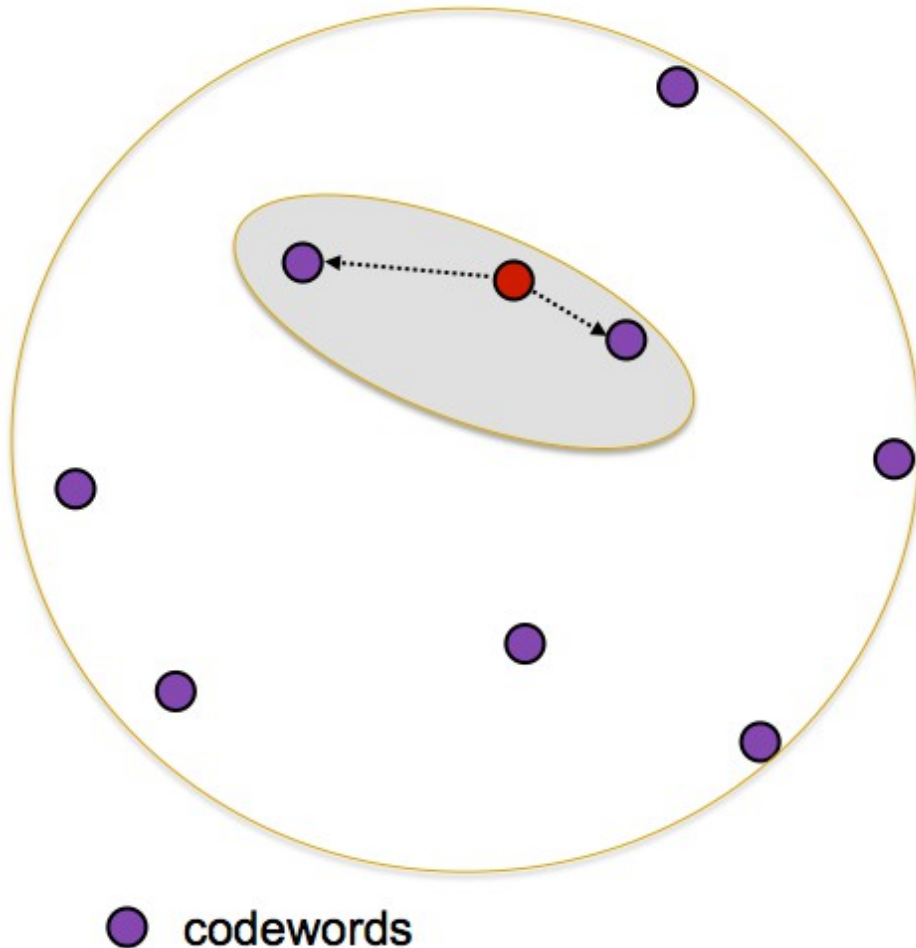



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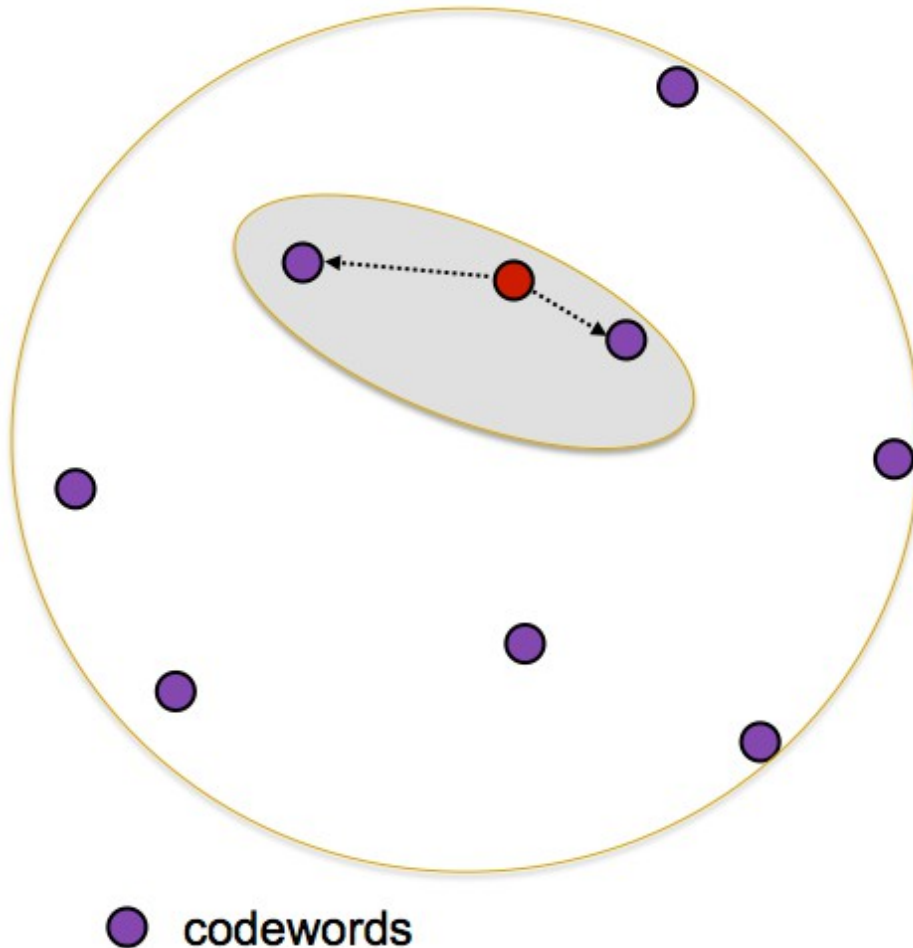
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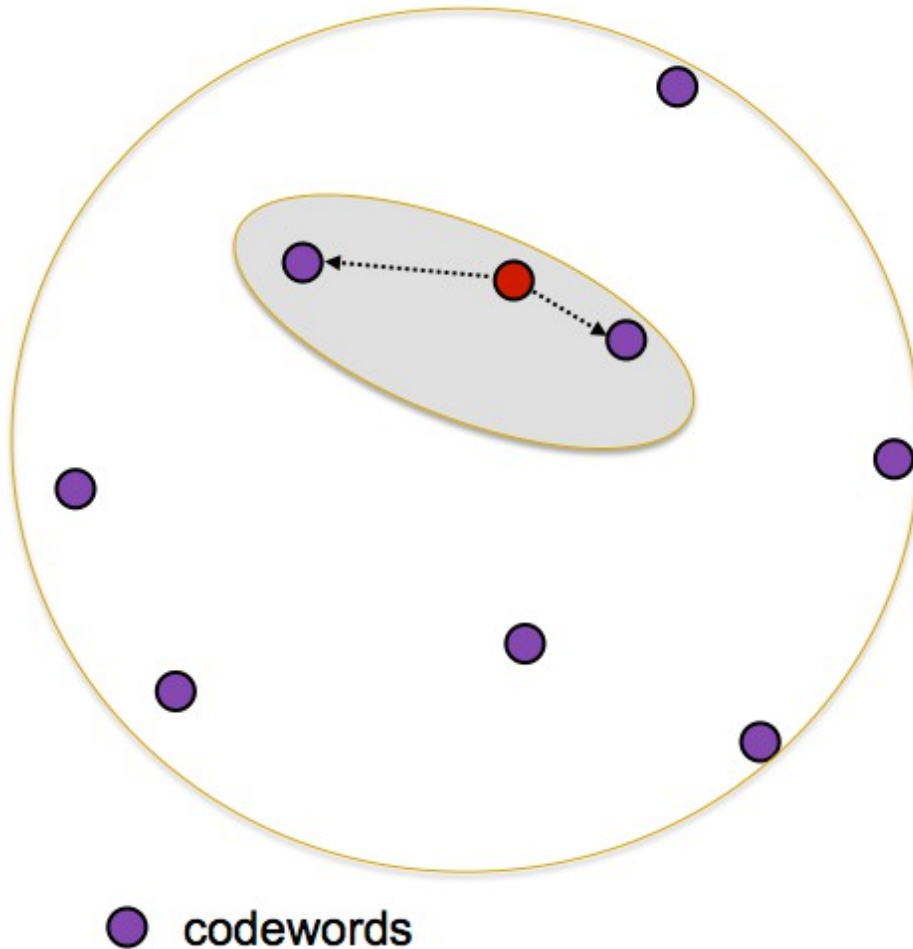
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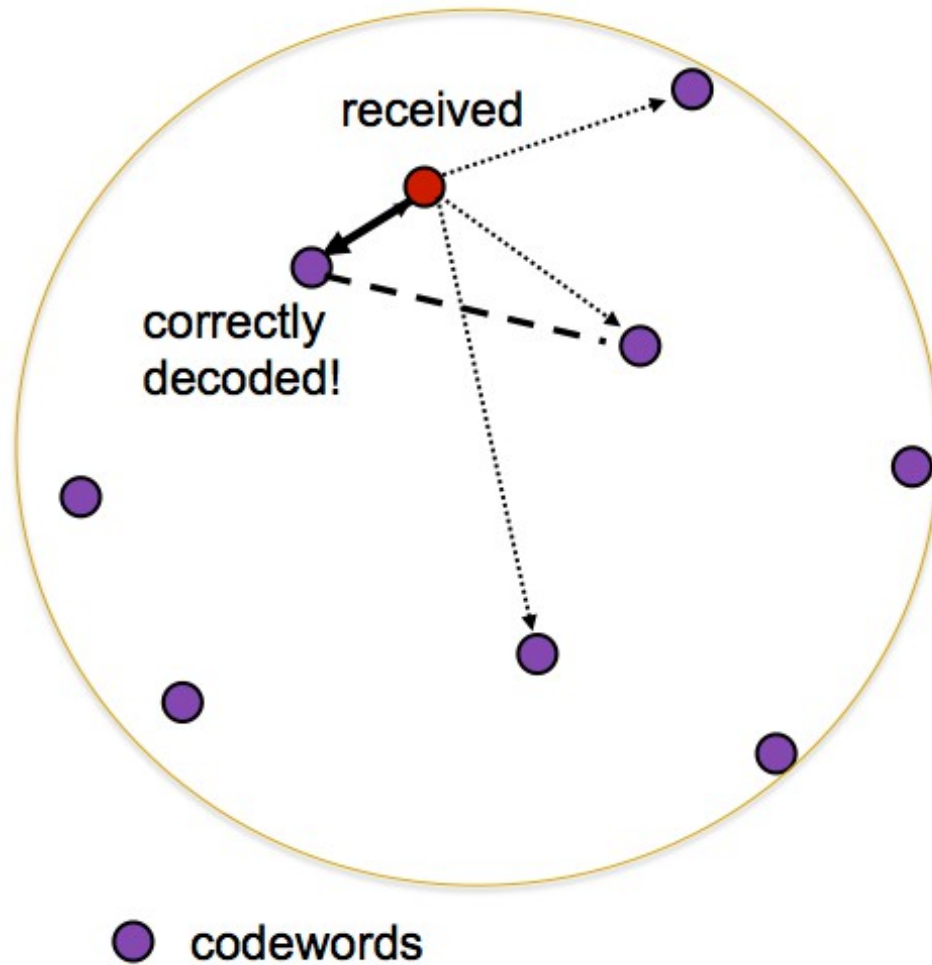
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  - Fewest bit flips

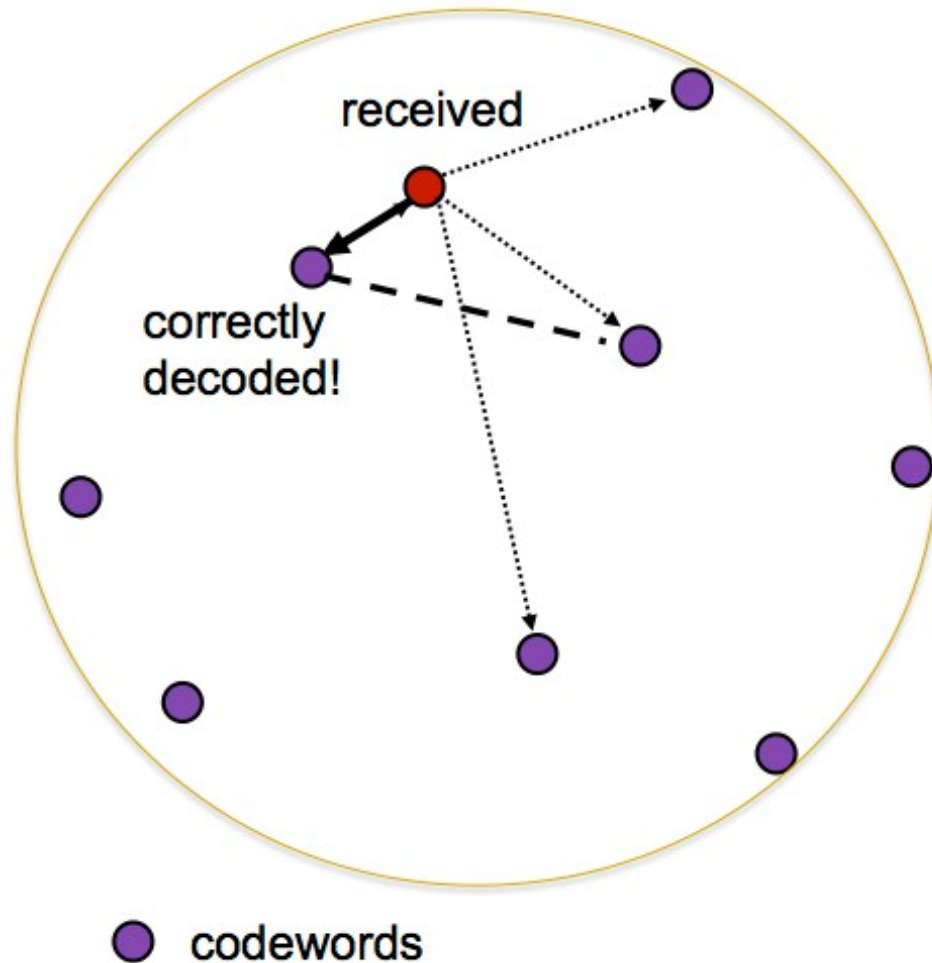
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- The minimum distance is the minimum separation between any two codewords



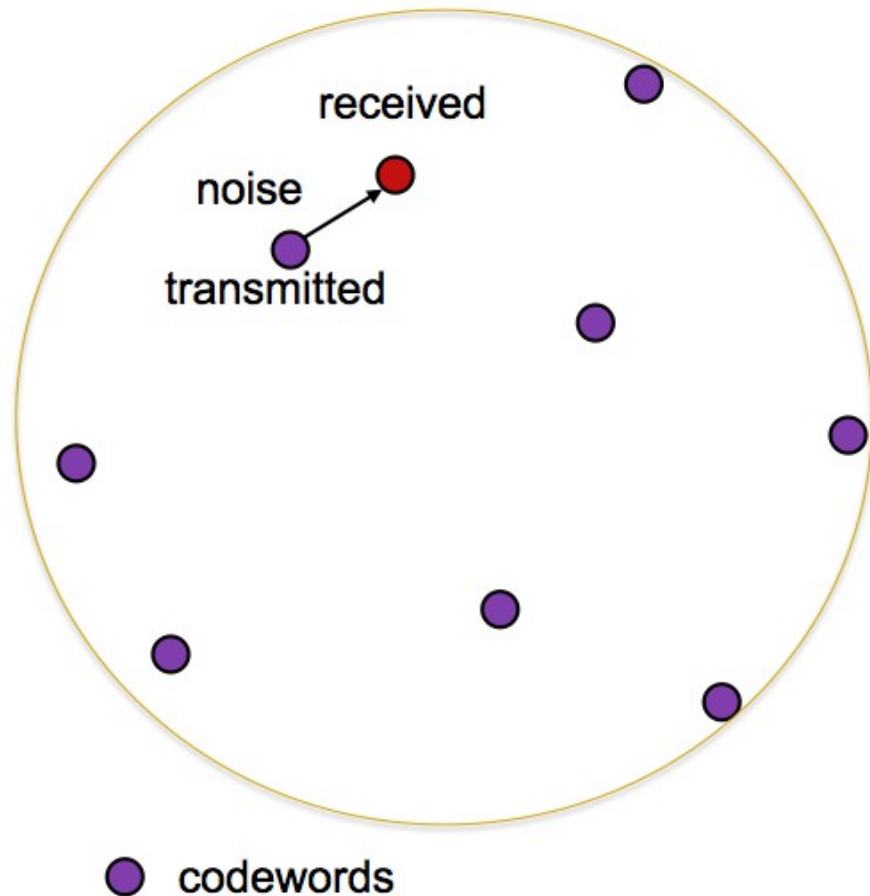
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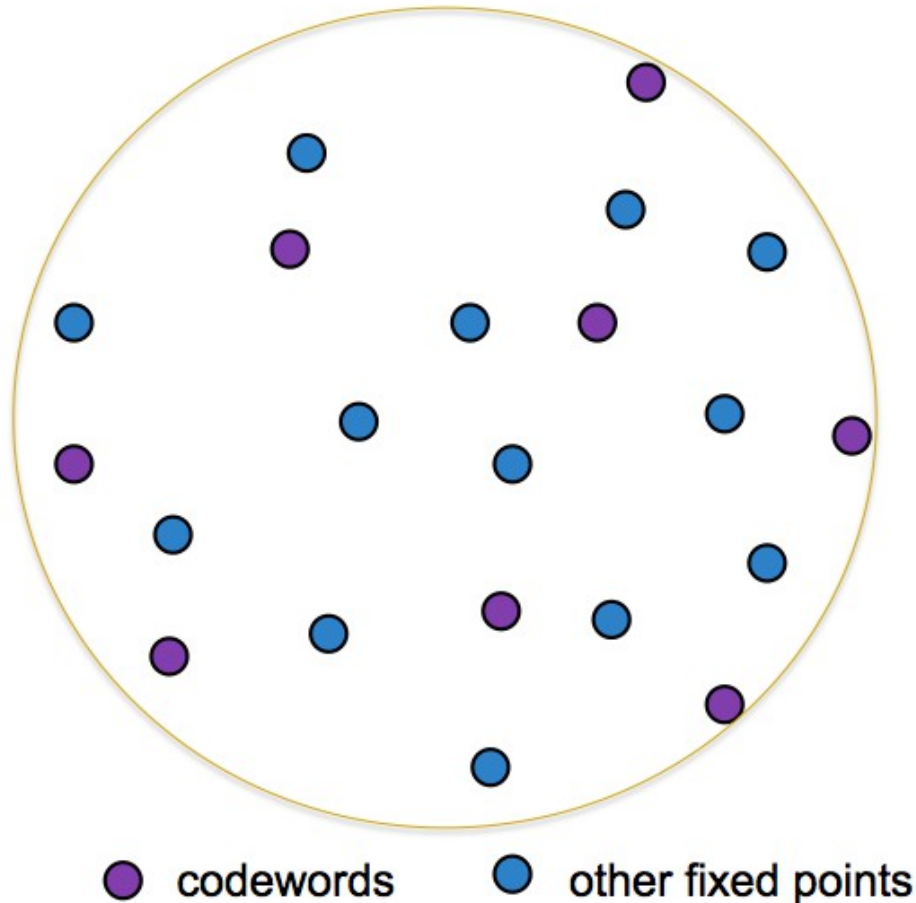
→ Large minimum distance implies high probability of successful decoding!

# Back to iterative decoding...



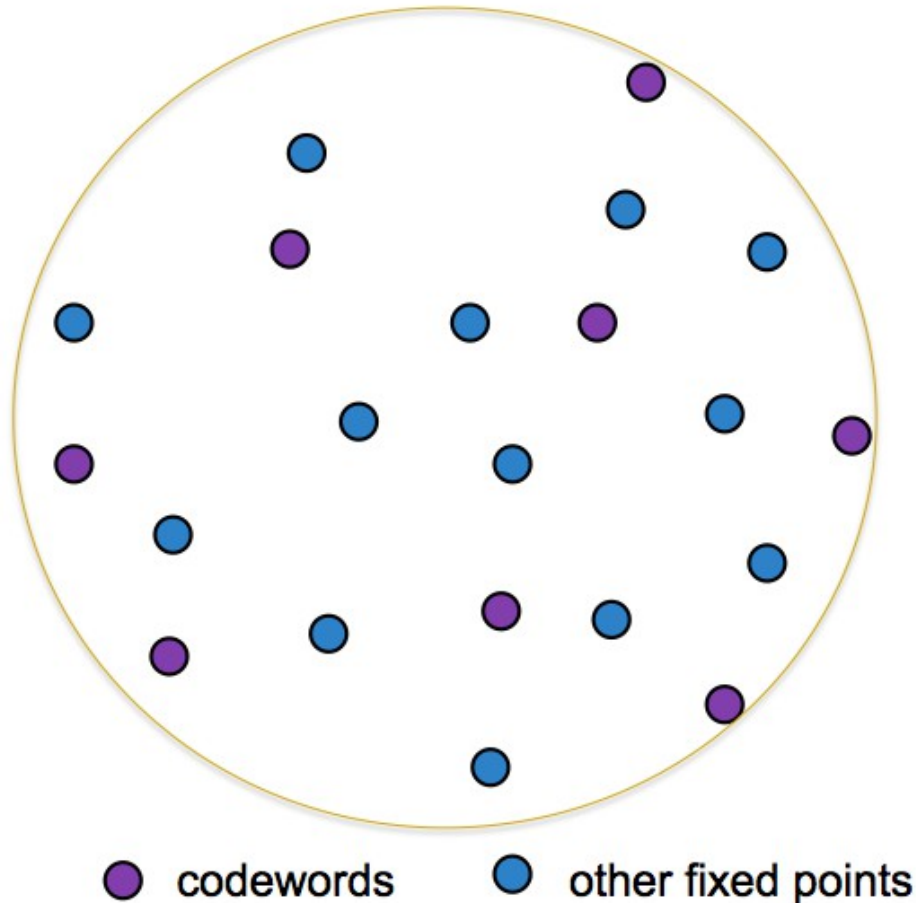
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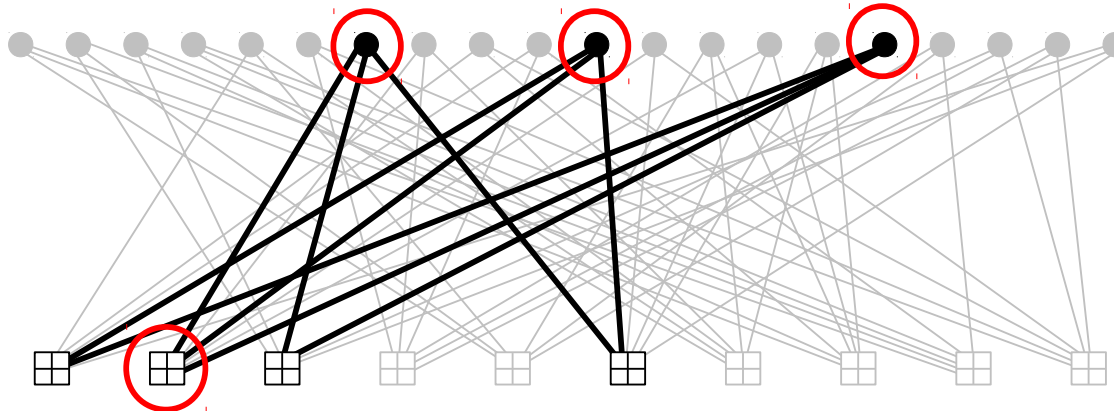
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- Codewords are fixed points, but are there other fixed points?
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- What are these other fixed points? How many are there? How can we avoid them?

- On the AWGNC, failures are attributed to **trapping sets** [Richardson '03].

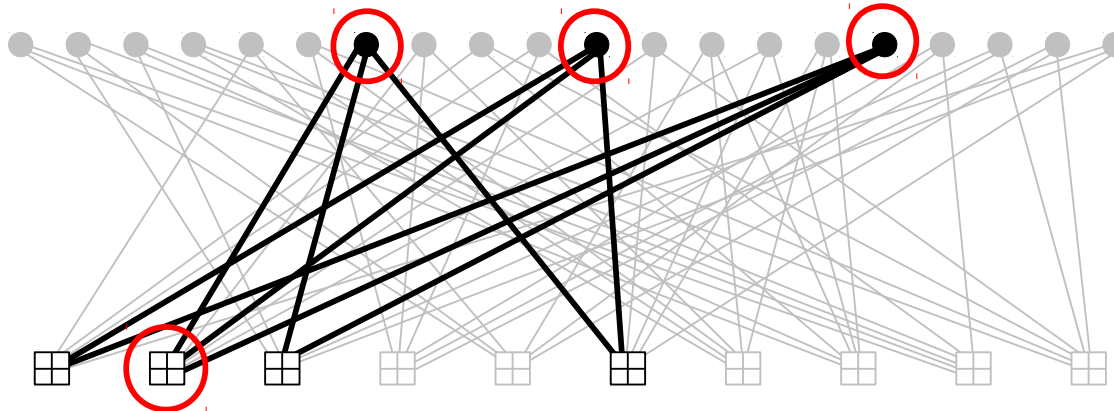
**Definition:** An  $(a,b)$  general trapping set  $\tau_{a,b}$  is a set of  $a$  variable nodes which induce a subgraph with exactly  $b$  odd-degree check nodes.



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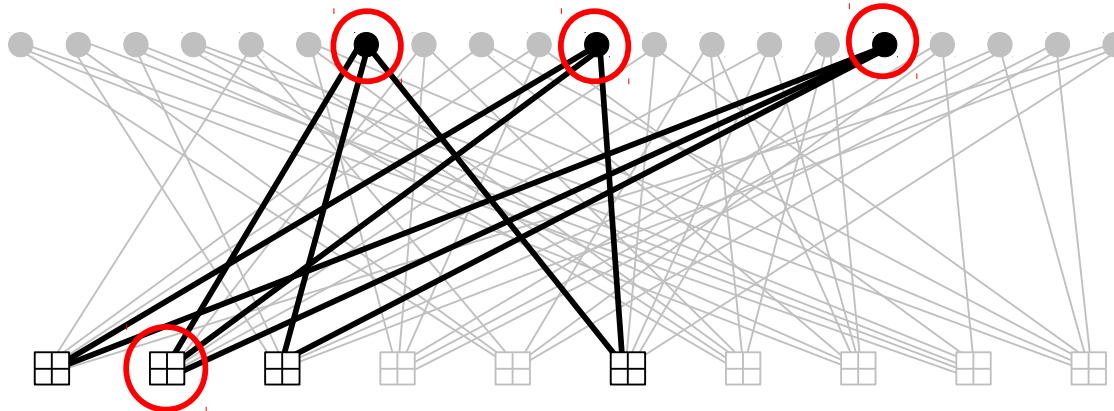


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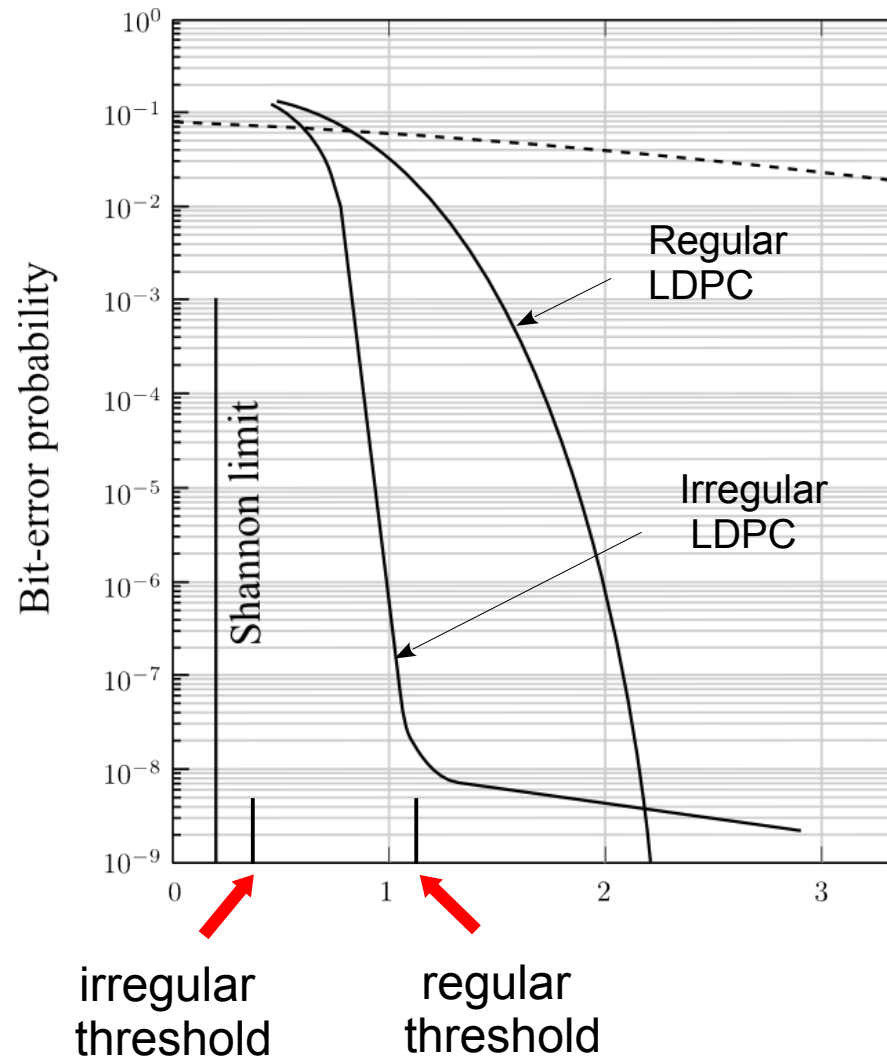
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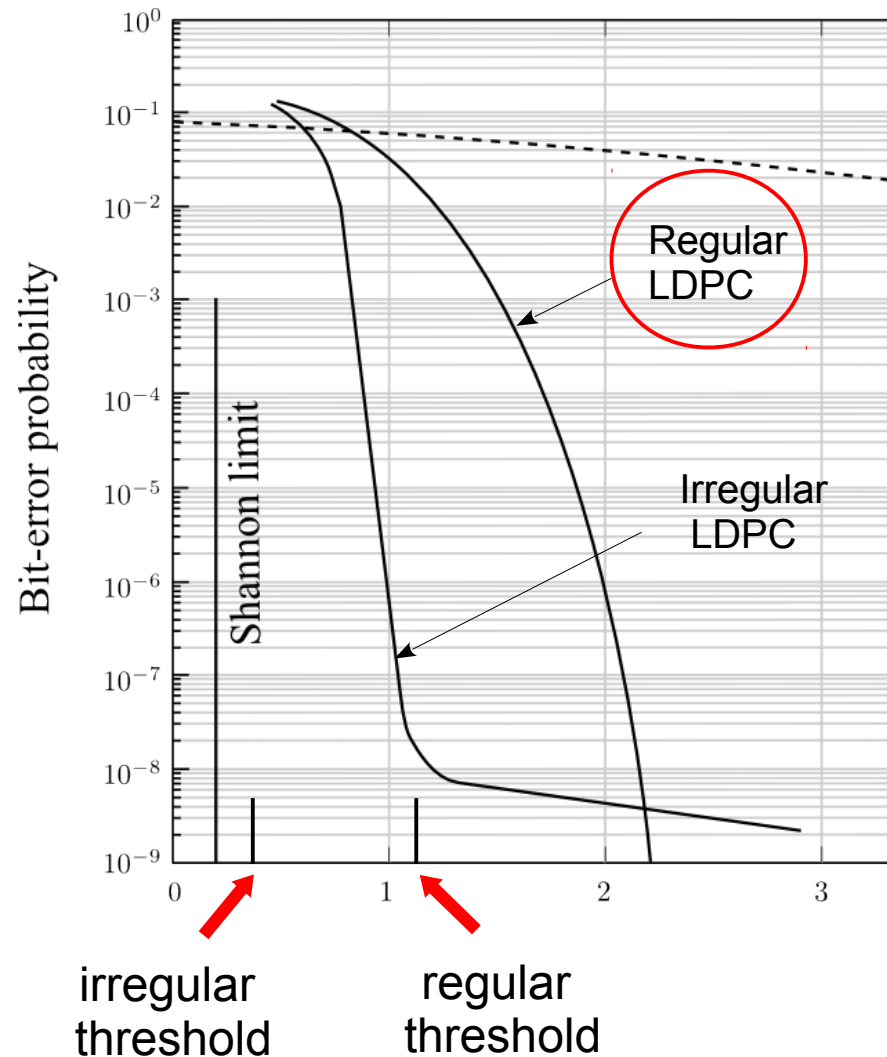
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- Certain types of trapping sets with small  $a$  and  $b$ , such as **elementary trapping sets** and **absorbing sets**, are known to be particularly harmful.

# Regular vs. Irregular LDPC codes





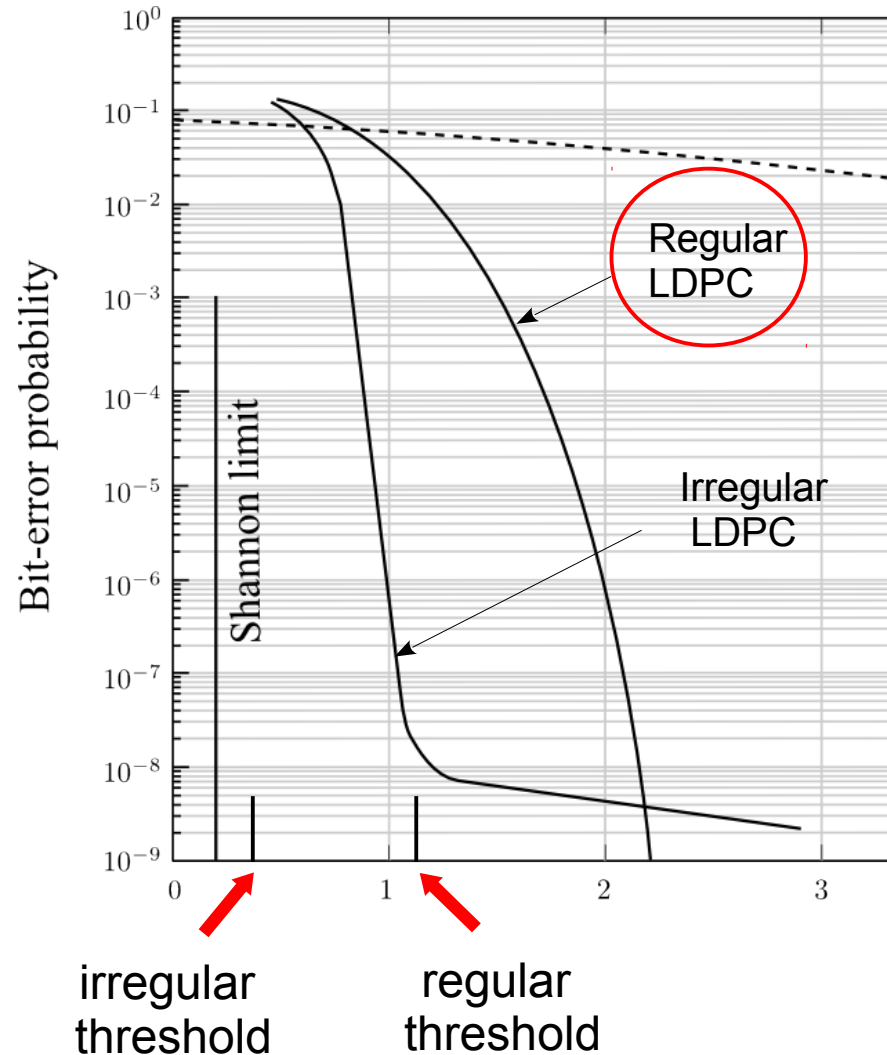
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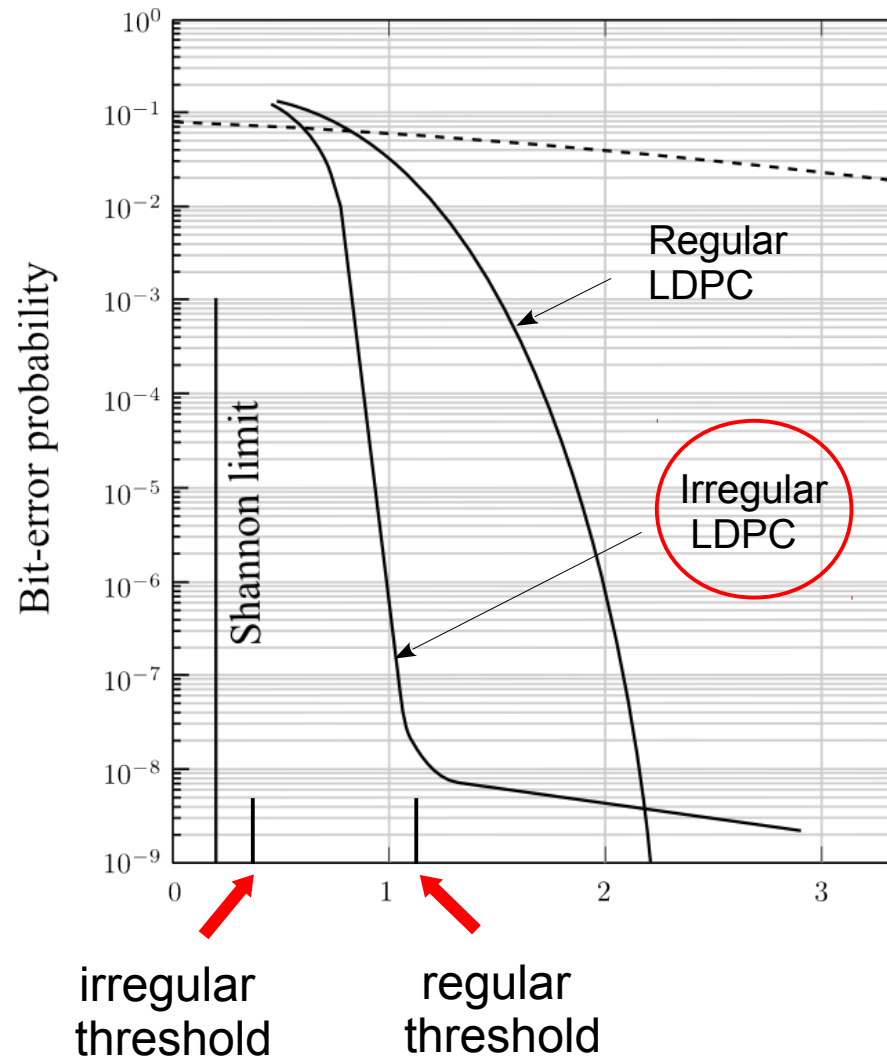
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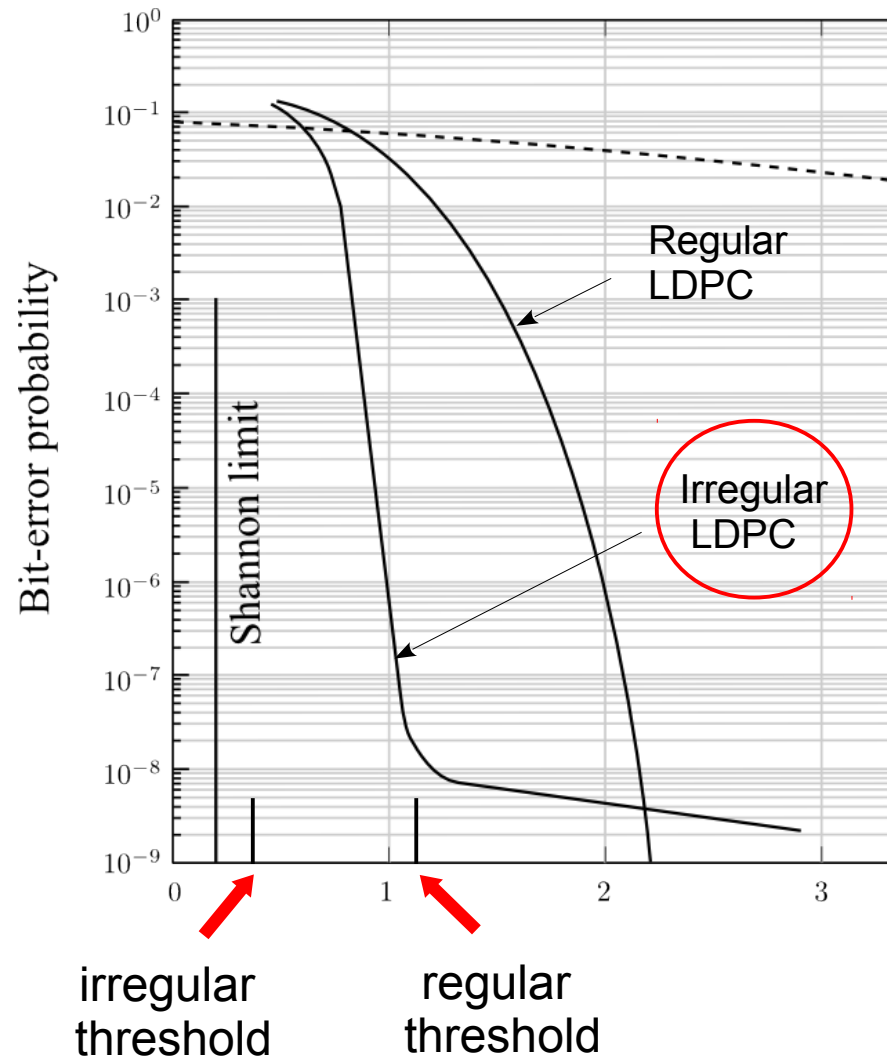
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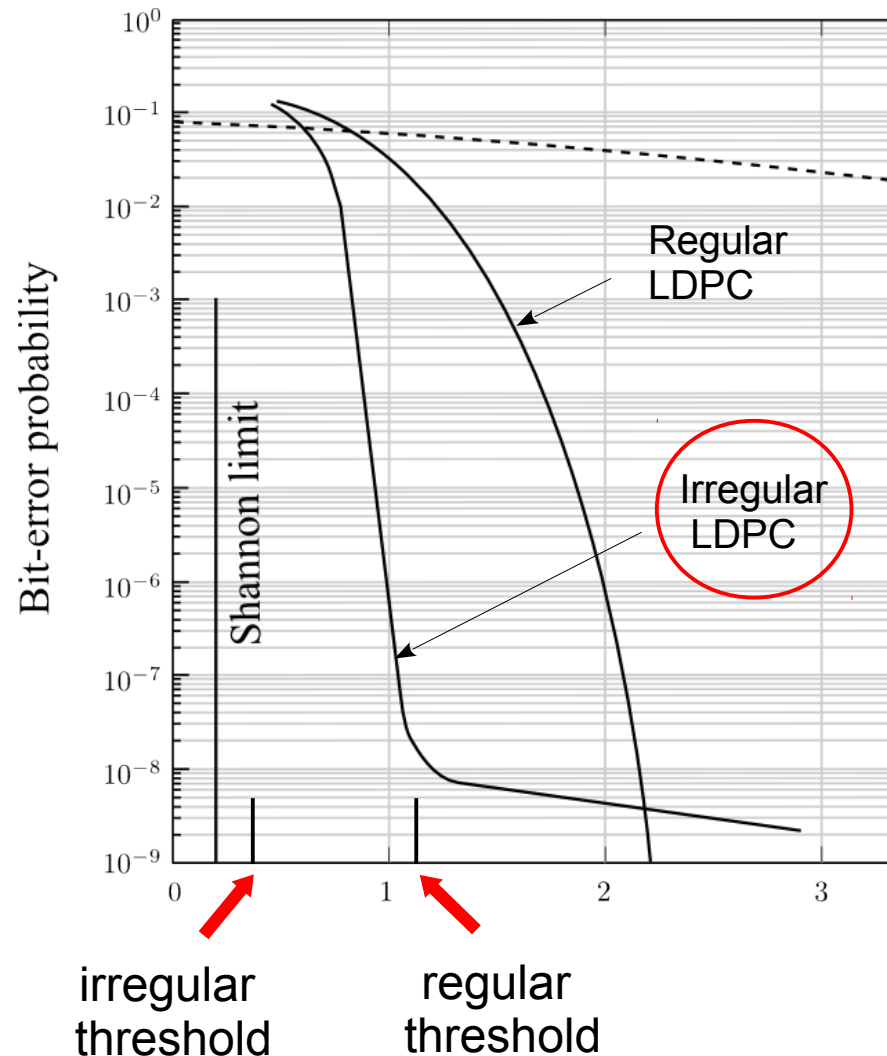
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- **Spatially coupled** LDPC codes combine all of the positive features!

- Let  $\mathbf{H}$  be composed of **permutation matrices** of size  $M$ :

$$\mathbf{H} = \begin{bmatrix} \mathbf{\Pi}_{1,1} & \mathbf{\Pi}_{1,2} & \cdots & \mathbf{\Pi}_{1,K} \\ \mathbf{\Pi}_{2,1} & \mathbf{\Pi}_{2,2} & \cdots & \mathbf{\Pi}_{2,K} \\ \vdots & \vdots & & \vdots \\ \mathbf{\Pi}_{J,1} & \mathbf{\Pi}_{J,2} & \cdots & \mathbf{\Pi}_{J,K} \end{bmatrix}_{J \times K}$$

$(J,K)$ -regular codes:

- Arbitrarily large girth [Gallager, '63]
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**Regular example:**

$J = 3, K = 6, n = 24,$   
 $M = 4$  ( $\mathbf{\Pi}_{i,j}$  is  $4 \times 4$ )

$$\begin{bmatrix} 0 & 1 & 0 & 0 & | & 0 & 0 & 0 & 1 & | & 0 & 1 & 0 & 0 & | & 0 & 0 & 1 & 0 & | & 0 & 0 & 1 & 0 & | & 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 & | & 0 & 1 & 0 & 0 & | & 0 & 0 & 0 & 1 & | & 0 & 1 & 0 & 0 & | & 0 & 0 & 0 & 1 & | & 1 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & | & 0 & 0 & 1 & 0 & | & 0 & 0 & 1 & 0 & | & 0 & 0 & 0 & 1 & | & 1 & 0 & 0 & 0 & | & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 & | & 1 & 0 & 0 & 0 & | & 1 & 0 & 0 & 0 & | & 1 & 0 & 0 & 0 & | & 0 & 1 & 0 & 0 & | & 0 & 0 & 1 & 0 \\ \hline 0 & 0 & 0 & 1 & | & 1 & 0 & 0 & 0 & | & 0 & 0 & 1 & 0 & | & 0 & 1 & 0 & 0 & | & 0 & 0 & 0 & 1 & | & 0 & 0 & 1 & 0 \\ 1 & 0 & 0 & 0 & | & 0 & 0 & 1 & 0 & | & 0 & 1 & 0 & 0 & | & 1 & 0 & 0 & 0 & | & 0 & 1 & 0 & 0 & | & 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 & | & 0 & 1 & 0 & 0 & | & 0 & 0 & 0 & 1 & | & 0 & 0 & 1 & 0 & | & 1 & 0 & 0 & 0 & | & 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 & | & 0 & 0 & 0 & 1 & | & 1 & 0 & 0 & 0 & | & 0 & 0 & 0 & 1 & | & 0 & 0 & 1 & 0 & | & 1 & 0 & 0 & 0 \\ \hline 0 & 1 & 0 & 0 & | & 0 & 1 & 0 & 0 & | & 0 & 0 & 1 & 0 & | & 1 & 0 & 0 & 0 & | & 0 & 0 & 0 & 1 & | & 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & | & 1 & 0 & 0 & 0 & | & 1 & 0 & 0 & 0 & | & 0 & 0 & 1 & 0 & | & 0 & 1 & 0 & 0 & | & 0 & 0 & 1 & 0 \\ 1 & 0 & 0 & 0 & | & 0 & 0 & 1 & 0 & | & 0 & 1 & 0 & 0 & | & 0 & 1 & 0 & 0 & | & 0 & 0 & 1 & 0 & | & 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 1 & | & 0 & 0 & 0 & 1 & | & 0 & 0 & 0 & 1 & | & 0 & 0 & 0 & 1 & | & 1 & 0 & 0 & 0 & | & 0 & 1 & 0 & 0 \end{bmatrix}$$

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**Irregular example:**

Replace some  
permutation matrices  
by  $\mathbf{0}$  matrix of size  $M$

$J_{\max} = 3, K_{\max} = 6$

0	0	1	0	1	0	0	0	0	0	1	0	1	0	0	0	0	0	1	0	0	0	0	0
1	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	1	0	0	0	0
0	0	0	1	0	0	1	0	0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	0
0	1	0	0	0	1	0	0	1	0	0	0	0	0	1	0	1	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	1	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0	1	0	0	0	1
0	0	0	1	0	0	0	0	0	0	0	1	0	1	0	0	1	0	0	0	1	0	0	0
0	0	1	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0
0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0
0	0	0	0	1	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0




- **Compact representation** of a structured LDPC code ensemble based on permutation matrices of size  $M \times M$  by a base matrix of size  $b_c \times b_v$ :

$$\mathbf{H} = \begin{bmatrix} \mathbf{\Pi}_{1,1} & \mathbf{\Pi}_{1,2} & \mathbf{\Pi}_{1,3} & \mathbf{\Pi}_{1,4} & \mathbf{\Pi}_{1,5} & \mathbf{0} \\ \mathbf{\Pi}_{2,1} & \mathbf{0} & \mathbf{\Pi}_{2,3} & \mathbf{\Pi}_{2,4} & \mathbf{\Pi}_{2,5} & \mathbf{\Pi}_{2,6} \\ \mathbf{0} & \mathbf{\Pi}_{3,2} & \mathbf{\Pi}_{3,3} & \mathbf{0} & \mathbf{0} & \mathbf{\Pi}_{3,6} \end{bmatrix}_{b_c M \times b_v M}$$

[Tho05] J. Thorpe, “Low-Density Parity-Check (LDPC) codes constructed from protographs”, *Jet Propulsion Laboratory INP Progress Report*, Vol. 42-154 Aug. 2003.

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## base matrix

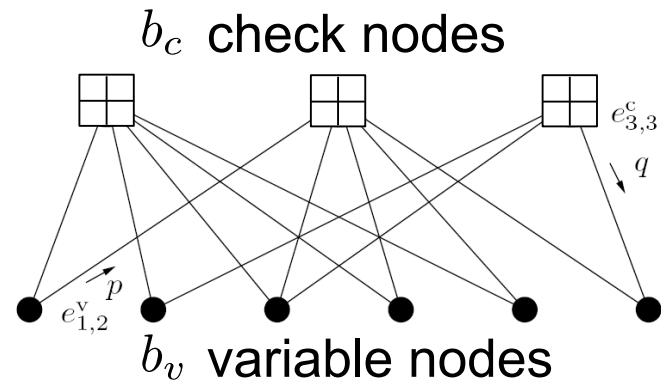
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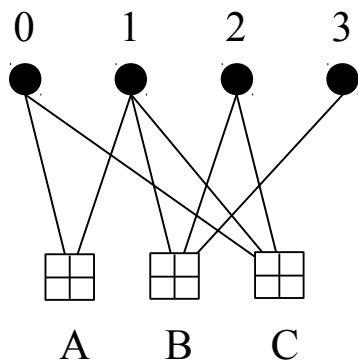
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**protograph**

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# Protograph Construction

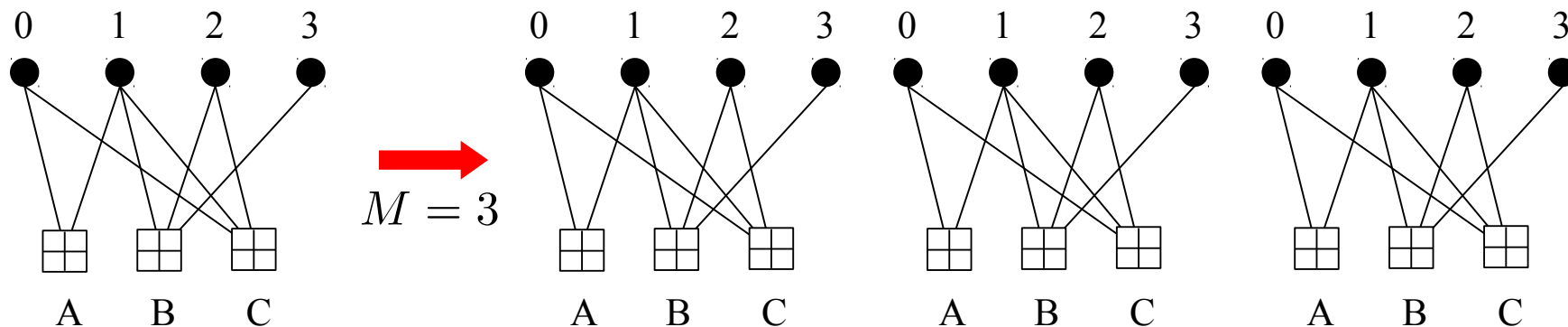
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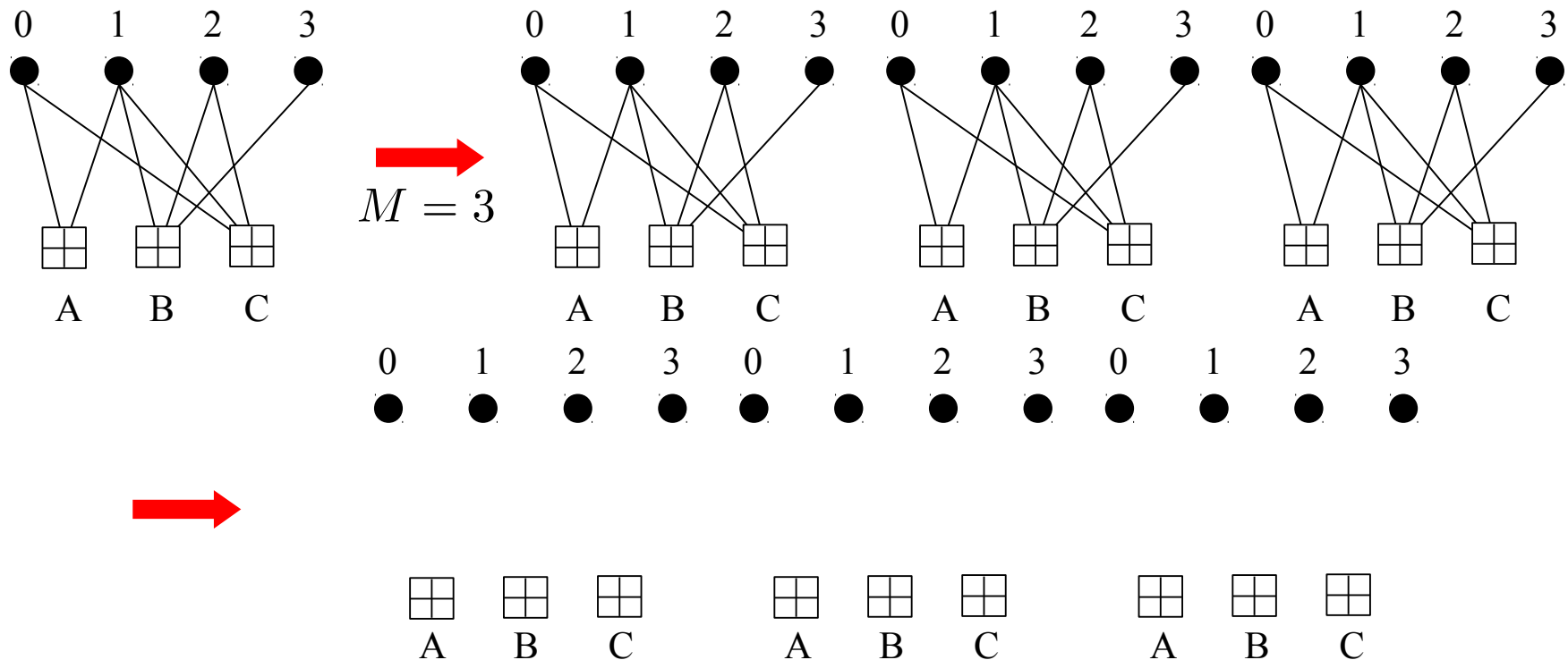
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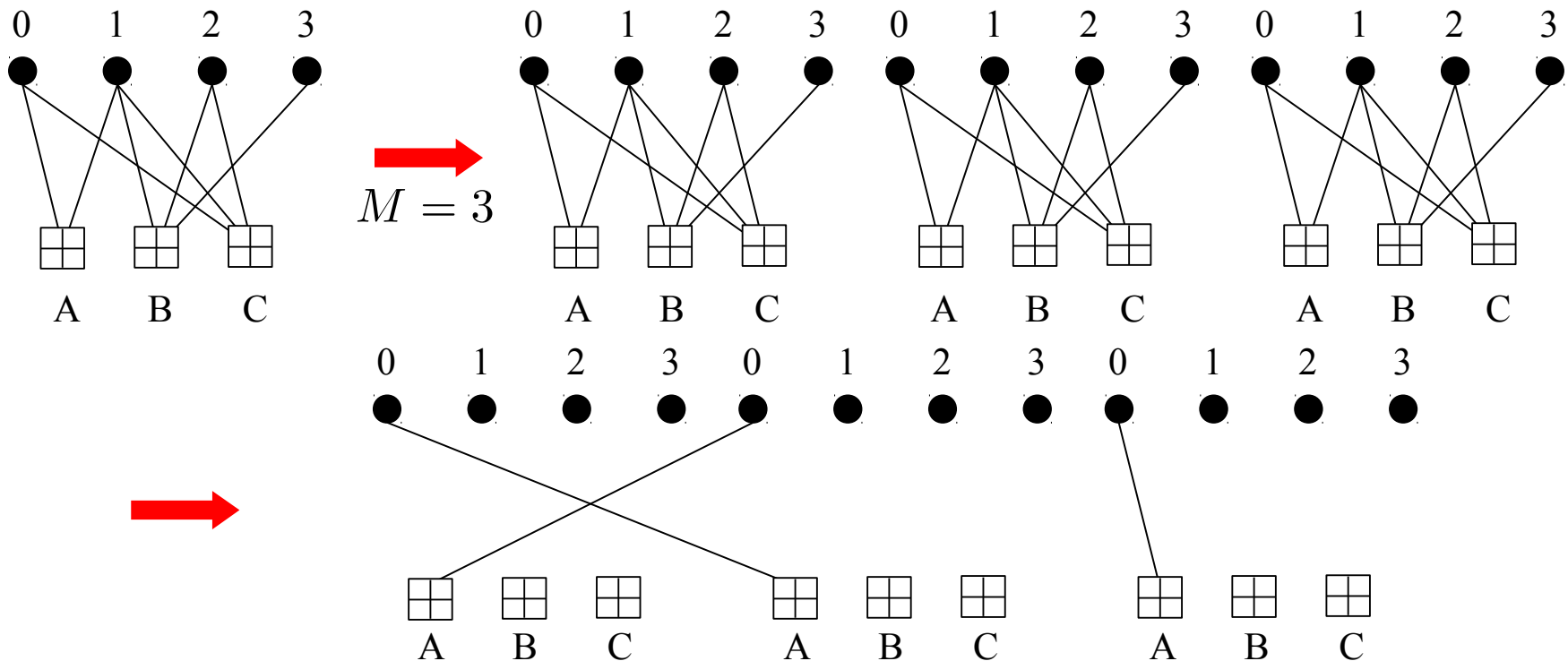
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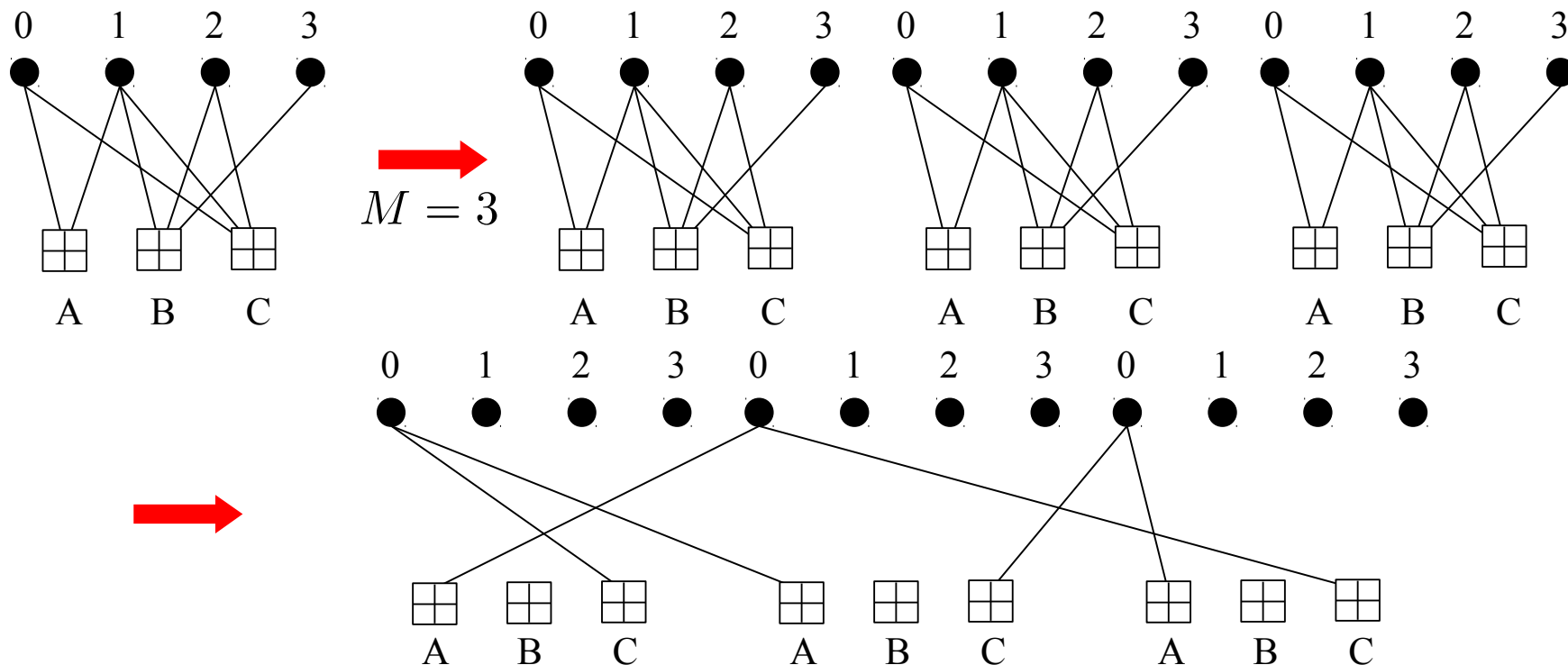
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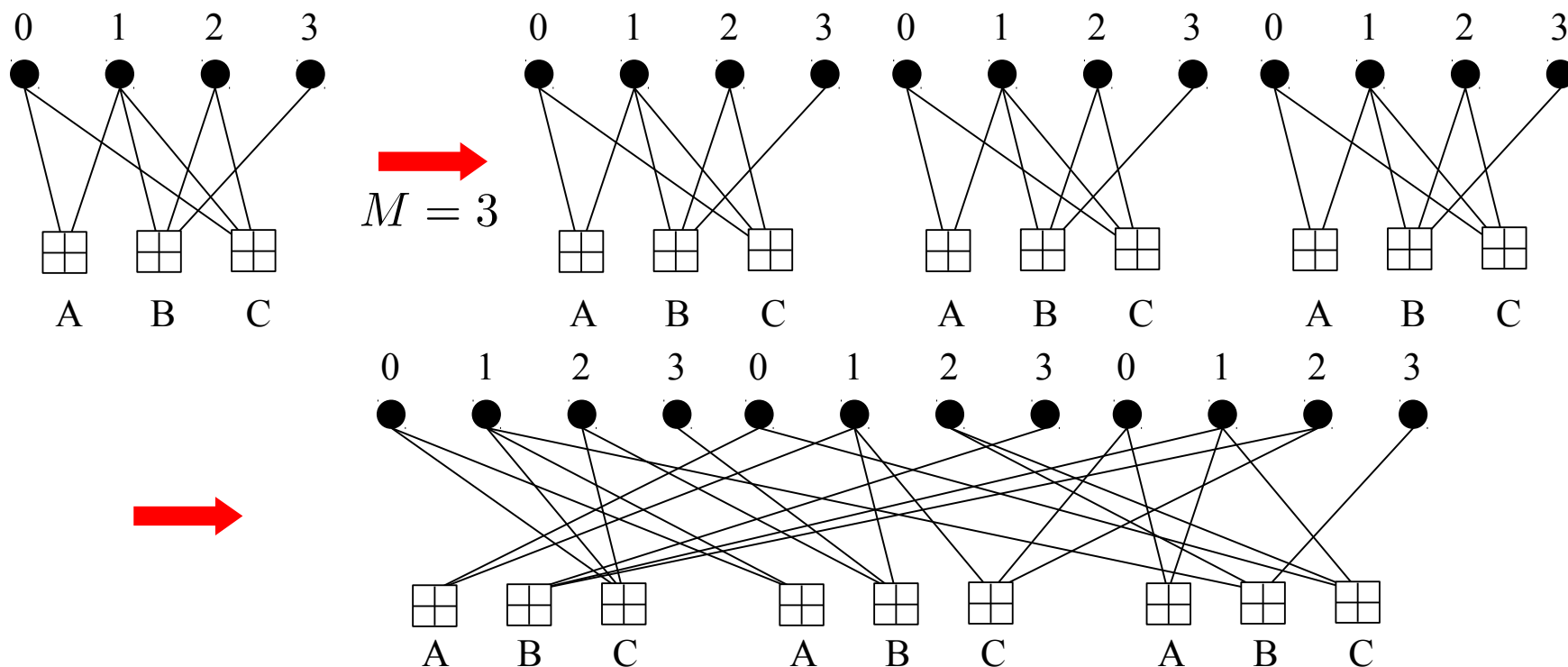


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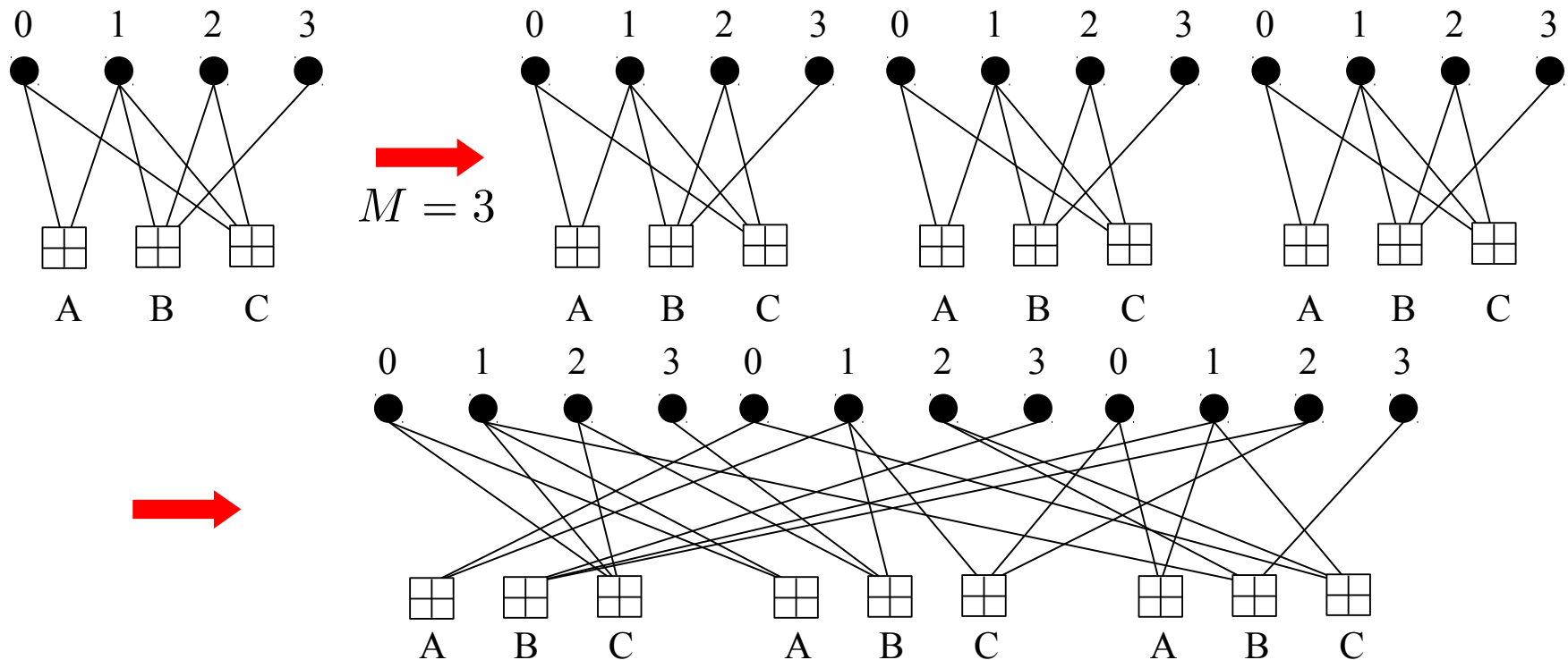
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- Code rate  $R \geq (b_v - b_c)/b_v$ , code length  $n = Mb_v$

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$$M = 7$$

$$n = Mb_v = 21$$

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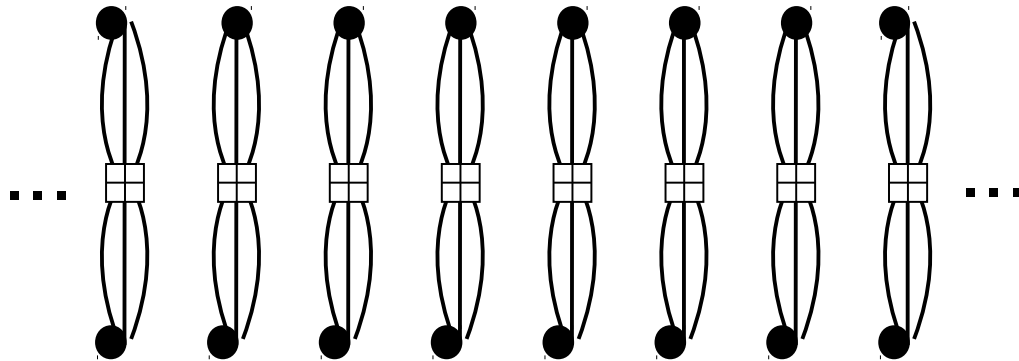
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- The QC sub-ensemble contains **atypical** codes. Thus ensemble average results **do not apply** to these highly structured codes.

- Introduction: From Shannon to Modern Coding Theory
  - ➔ Channel capacity, structured codes, random codes, LDPC codes
- LDPC Block Codes
  - ➔ Parity-check matrix and Tanner graph representations, minimum distance bounds, iterative decoding thresholds, protograph-based constructions
- **Spatially Coupled LDPC Codes**
  - ➔ Protograph representation, edge-spreading construction, termination
  - ➔ Iterative decoding thresholds, threshold saturation, minimum distance
- Practical Considerations
  - ➔ Window decoding, performance, latency, and complexity comparisons to LDPC block codes, rate-compatibility, implementation aspects

# Spatially Coupled Protographs

- Consider transmission of consecutive blocks (protograph representation):



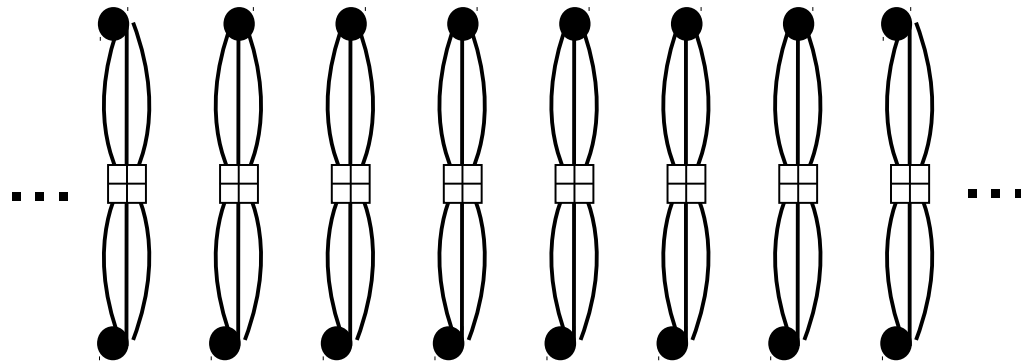
$$\mathbf{B} = \begin{bmatrix} 3 & 3 \end{bmatrix}_{b_c \times b_v}$$

(3,6)-regular  
LDPC-BC  
base matrix



# Spatially Coupled Protographs

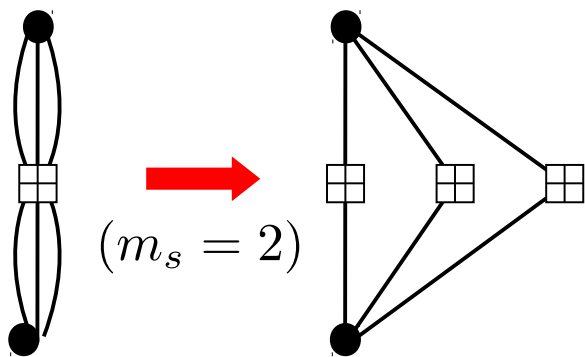
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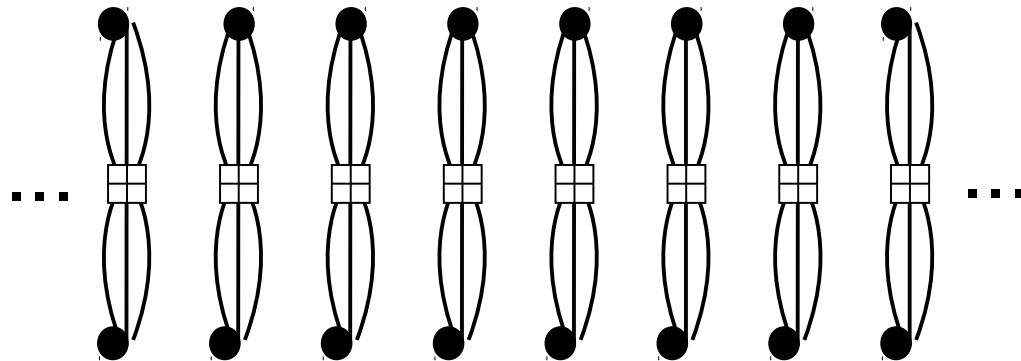
- Blocks are **spatially coupled** (introducing **memory**) by **spreading edges** over time:



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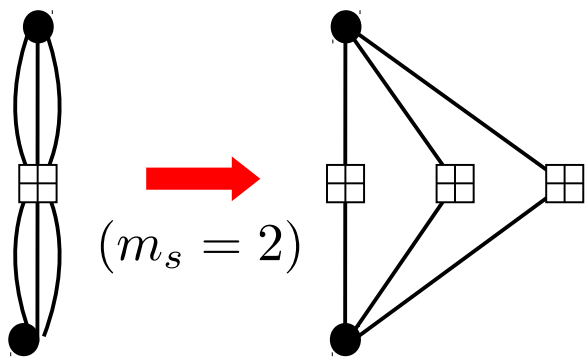
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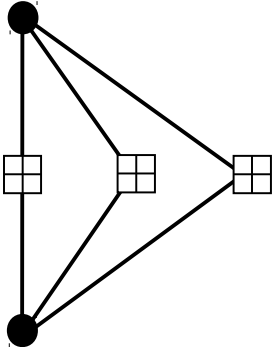
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- Spreading constraint:**

$$\sum_{i=0}^{m_s} \mathbf{B}_i = \mathbf{B} \quad (\mathbf{B}_i \text{ has size } b_c \times b_v)$$

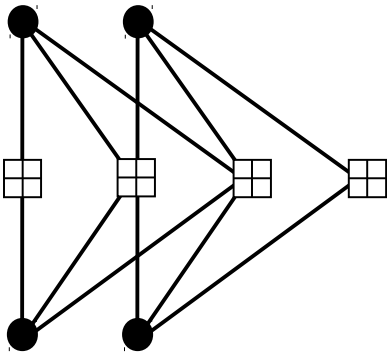
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- Transmission of consecutive spatially coupled (SC) blocks results in a **convolutional protograph**:



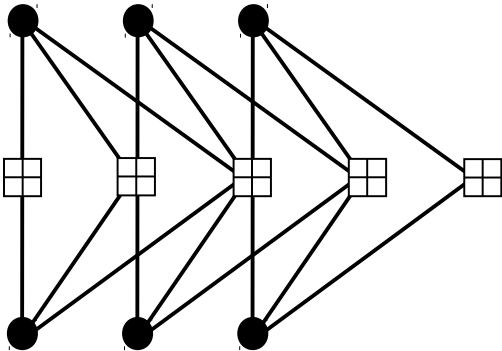
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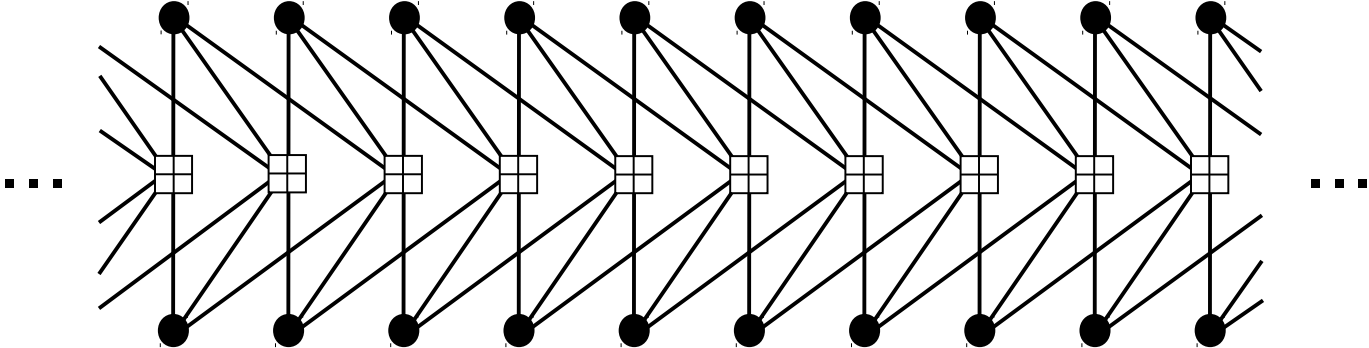
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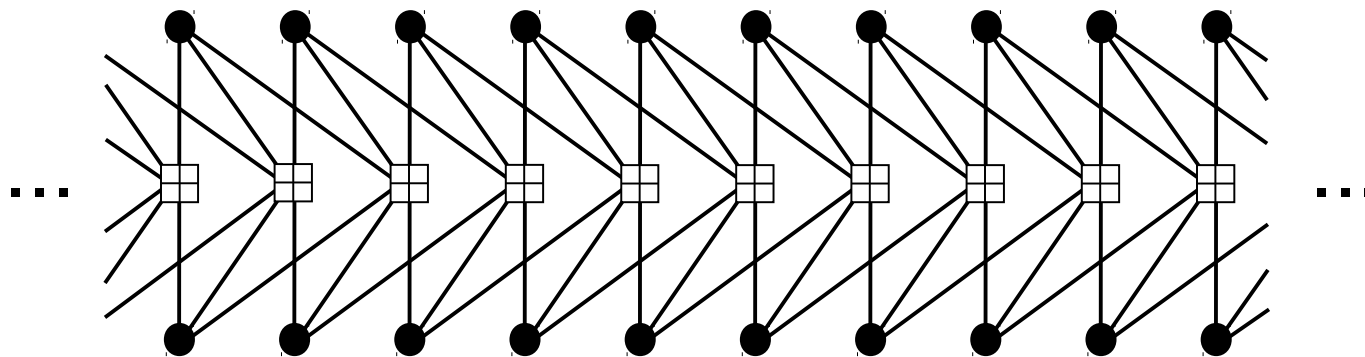
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- The bi-infinite convolutional protograph corresponds to a bi-infinite **convolutional base matrix**:

$$\mathbf{B}_{[-\infty, \infty]} = \begin{bmatrix} \ddots & & \ddots & \ddots & & & & & & & \\ & \mathbf{B}_{m_s} & \cdots & \mathbf{B}_1 & \mathbf{B}_0 & & & & & & \\ & & \ddots & & \ddots & \ddots & & & & & \\ & & & \mathbf{B}_{m_s} & \cdots & \mathbf{B}_1 & \mathbf{B}_0 & & & & \\ & & & & \ddots & & \ddots & \ddots & & & \\ & & & & & \ddots & & \ddots & \ddots & & \\ & & & & & & \ddots & & \ddots & \ddots & \end{bmatrix}$$

**Rate:**

$$R = \frac{b_v - b_c}{b_v}$$

**Constraint length:**

$$\nu_s = b_v(m_s + 1)$$

- An ensemble of (3,6)-regular SC-LDPC codes can be created from the **convolutional protograph** by the graph lifting operation

$$\mathbf{B}_{[-\infty, \infty]} = \left[ \begin{array}{cccc} \ddots & \ddots & \ddots & \\ \mathbf{B}_2 & \mathbf{B}_1 & \mathbf{B}_0 & \\ & \mathbf{B}_2 & \mathbf{B}_1 & \mathbf{B}_0 \\ & & \mathbf{B}_2 & \mathbf{B}_1 & \mathbf{B}_0 \\ & & & \ddots & \ddots & \ddots \end{array} \right]$$



# SC-LDPC Code Ensembles

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1	1	1	1	1	1				
		1	1	1	1	1	1		
			1	1	1	1	1	1	1

$$\mathbf{B}_i = \begin{bmatrix} 1 & 1 \end{bmatrix}$$

$$b_c = 1$$

$$b_v = 2$$

$$m_s = 2$$

**Graph lifting:**  $\Pi_{i,j}$  is an  $M \times M$  permutation matrix

$$\nu_s = Mb_v(m_s + 1) = 6M$$

$$\mathbf{H}_{cc} = \left[ \begin{array}{cccccc} \ddots & & & & & \\ & \ddots & & & & \\ & & \ddots & & & \\ & & & \ddots & & \\ & & & & \ddots & \\ & & & & & \ddots \end{array} \right]$$

$\Pi_{5,t}$	$\Pi_{4,t}$	$\Pi_{3,t}$	$\Pi_{2,t}$	$\Pi_{1,t}$	$\Pi_{0,t}$				
	$\Pi_{5,t+1}$	$\Pi_{4,t+1}$	$\Pi_{3,t+1}$	$\Pi_{2,t+1}$	$\Pi_{1,t+1}$	$\Pi_{0,t+1}$			
		$\Pi_{5,t+2}$	$\Pi_{4,t+2}$	$\Pi_{3,t+2}$	$\Pi_{2,t+2}$	$\Pi_{1,t+2}$	$\Pi_{0,t+2}$		

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- If each permutation matrix  $\Pi_{i,j}$  is **circulant**, the codes are **quasi-cyclic**

# Terminated Spatially Coupled Codes

- Consider **terminating**  $\mathbf{B}_{[-\infty, \infty]}$  to a (block code) **base matrix** of length  $Lb_v$ :

$$\mathbf{B}_{[0, L-1]} = \begin{bmatrix} \mathbf{B}_0 & & & \\ \vdots & \ddots & & \\ \mathbf{B}_{m_s} & & \mathbf{B}_0 & \\ & \ddots & \vdots & \\ & & \mathbf{B}_{m_s} & \end{bmatrix}_{(L+m_s)b_c \times Lb_v}$$

**Code rate:**

$$R_L = \frac{Lb_v - (L + m_s)b_c}{Lb_v}.$$

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**Code rate:**

$$R_L = \frac{Lb_v - (L + m_s)b_c}{Lb_v}.$$

- For large  $L$ ,  $R_L$  approaches the **unterminated** code rate  $R = (b_v - b_c)/b_v$ .

# Terminated Spatially Coupled Codes

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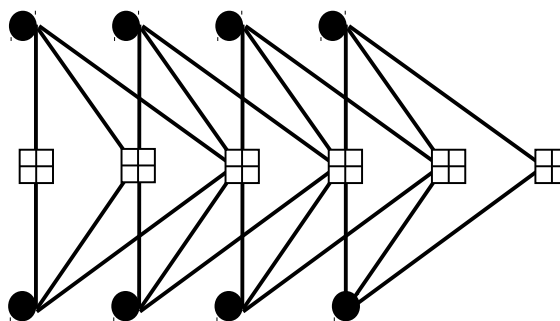
$$\mathbf{B}_{[0, L-1]} = \begin{bmatrix} \mathbf{B}_0 & & & & & & & \\ \vdots & & \ddots & & & & & \\ \mathbf{B}_{m_s} & & & & \mathbf{B}_0 & & & \\ & & & & & & \vdots & \\ & & & & & & & \mathbf{B}_{m_s} \end{bmatrix} \quad (L+m_s)b_c \times Lb_v$$

**Code rate:**

$$R_L = \frac{Lb_v - (L + m_s)b_c}{Lb_v}.$$

- For large  $L$ ,  $R_L$  approaches the **unterminated** code rate  $R = (b_v - b_c)/b_v$ .
- **Example:** (3,6)-regular base matrix  $\mathbf{B} = \begin{bmatrix} 3 & 3 \end{bmatrix}$ ,  $m_s = 2$ ,  $L = 4$ ,  $R_4 = 1/4$

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(**check node degrees lower at the ends**)

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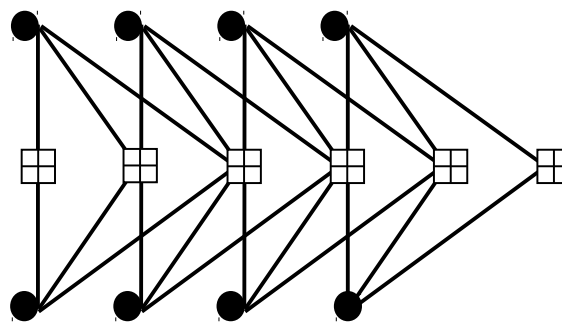
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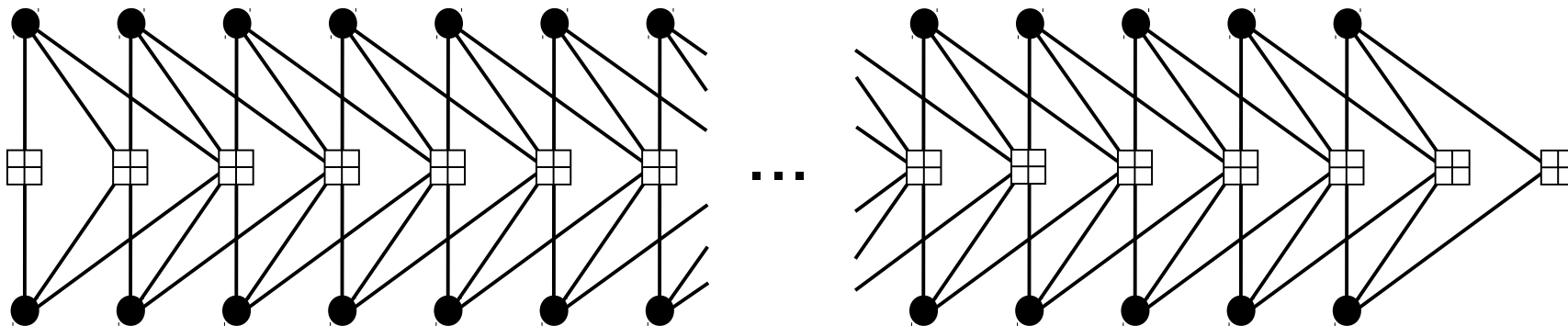


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- Codes can be **lifted** to **different lengths and rates** by varying  $M$  and  $L$ .

# Thresholds of SC-LDPC Codes

- Variable nodes all have the **same degree** as the block code.
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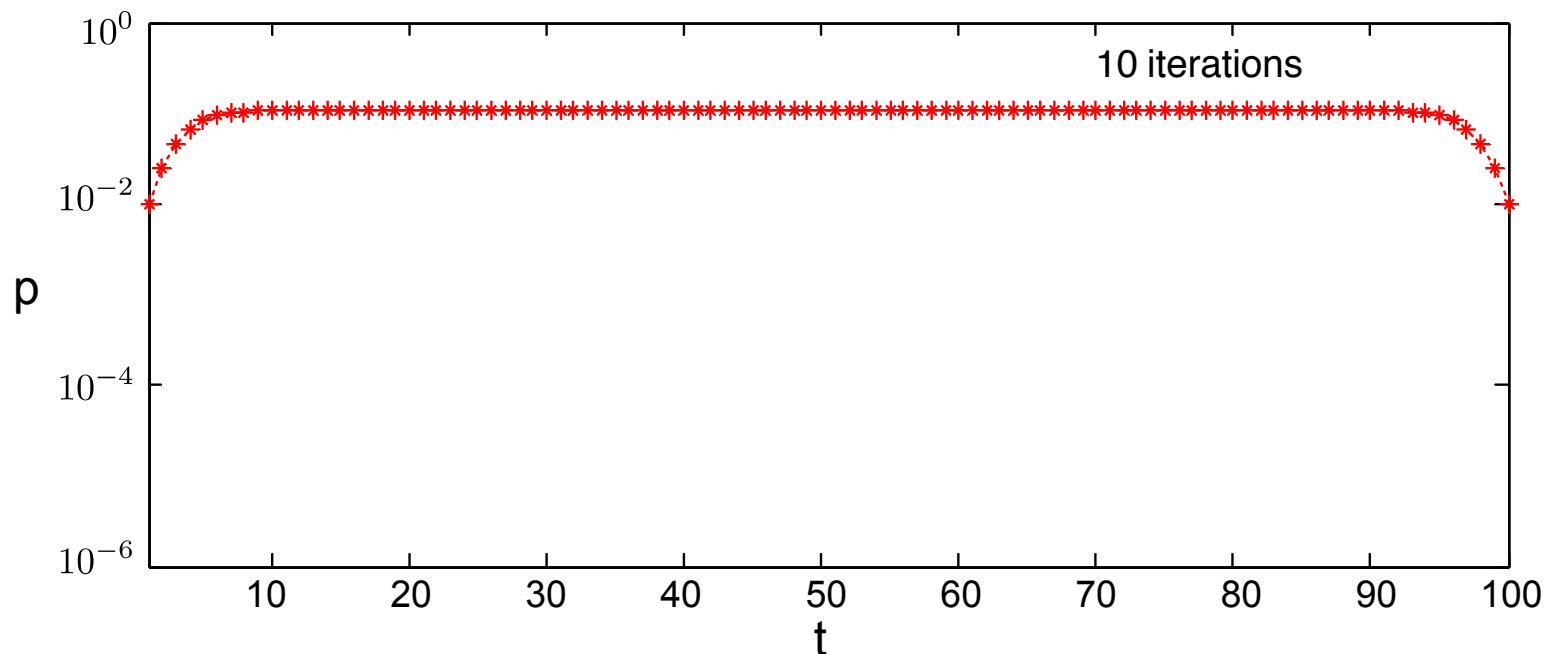




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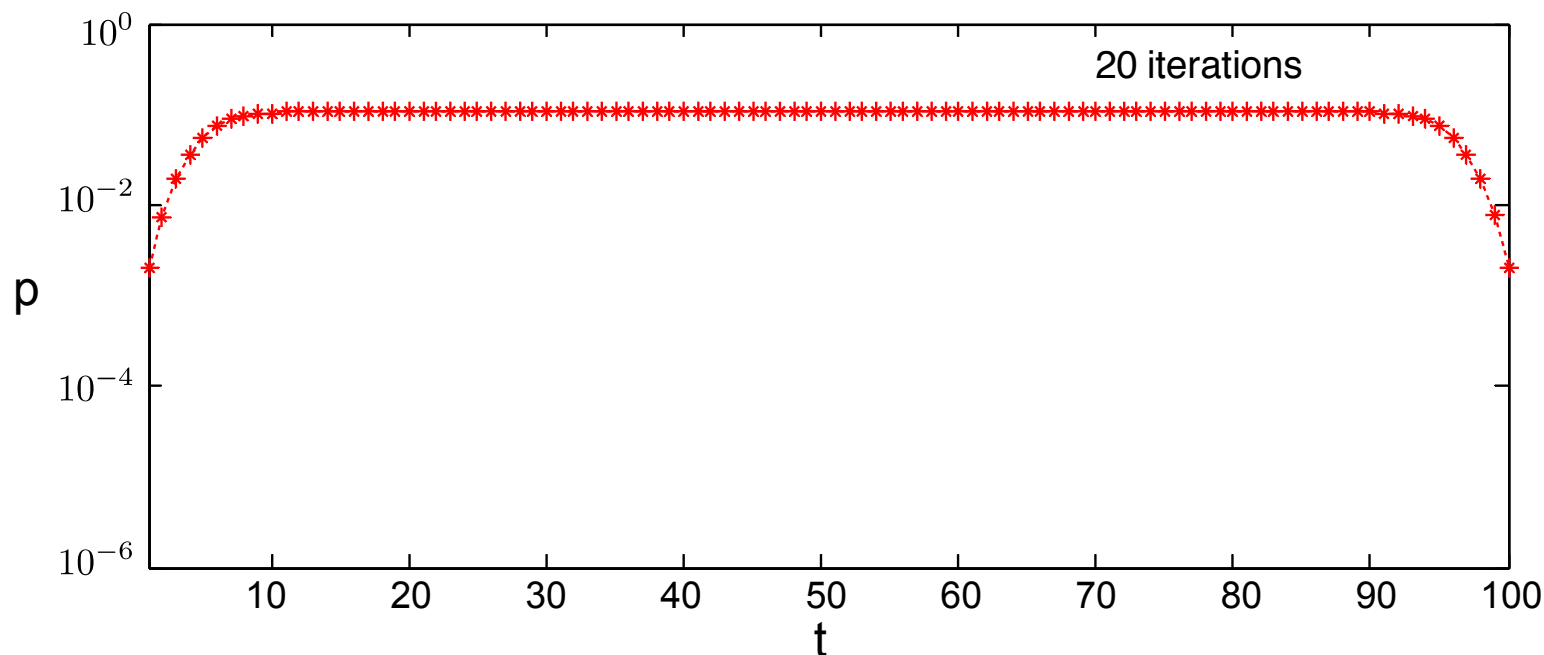
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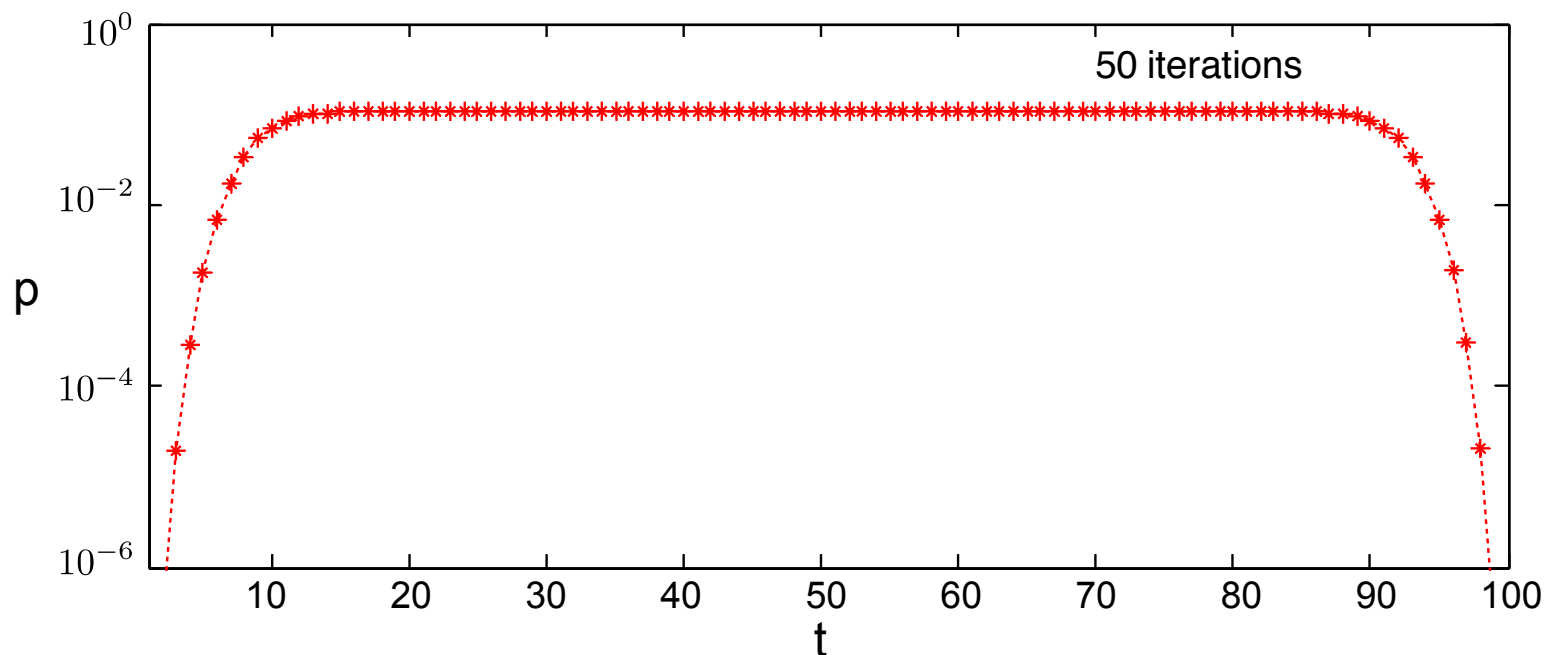
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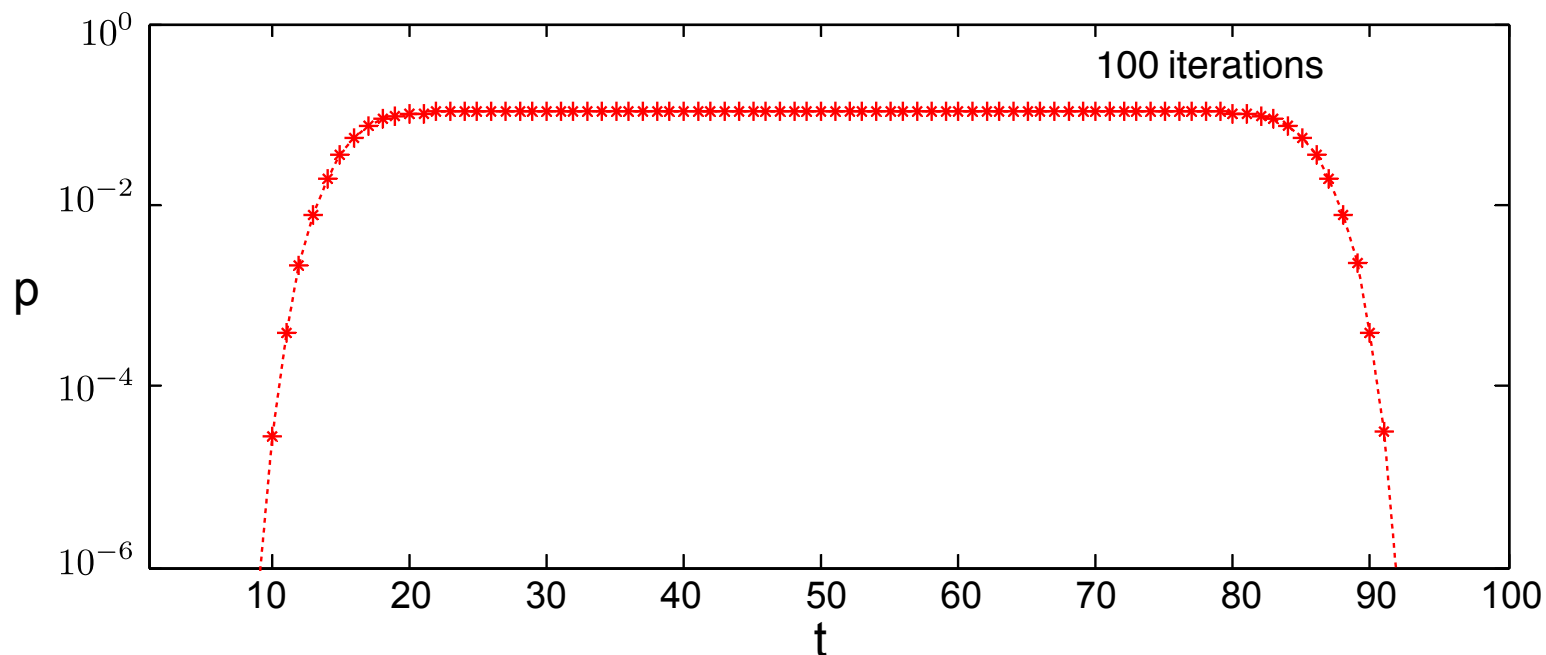
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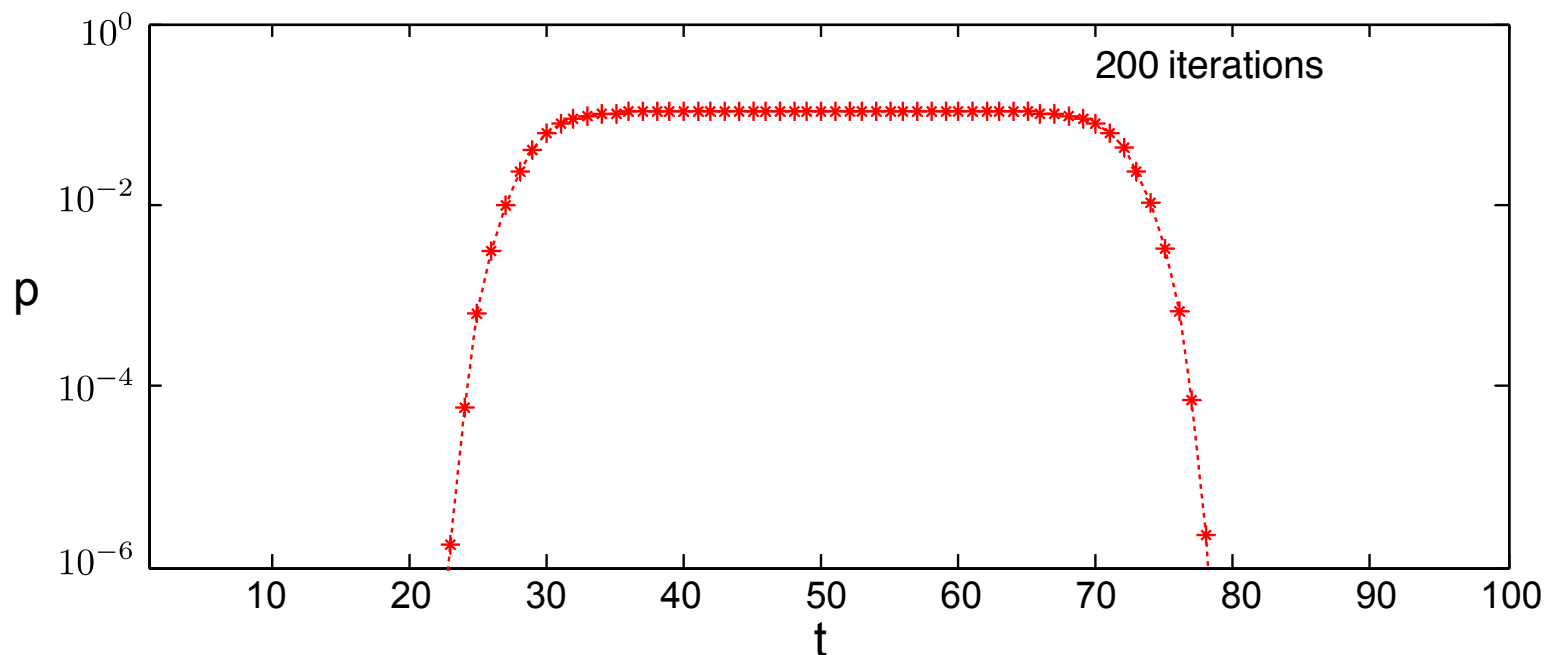
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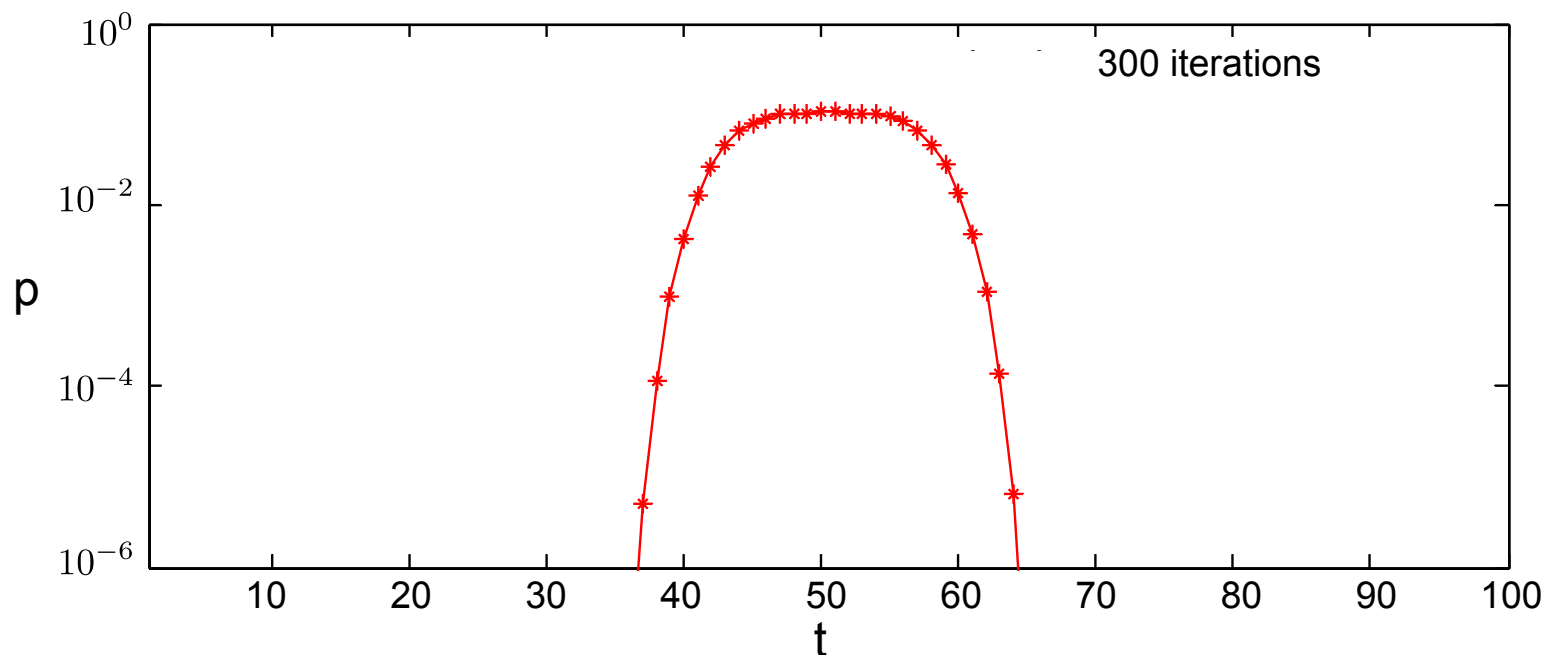
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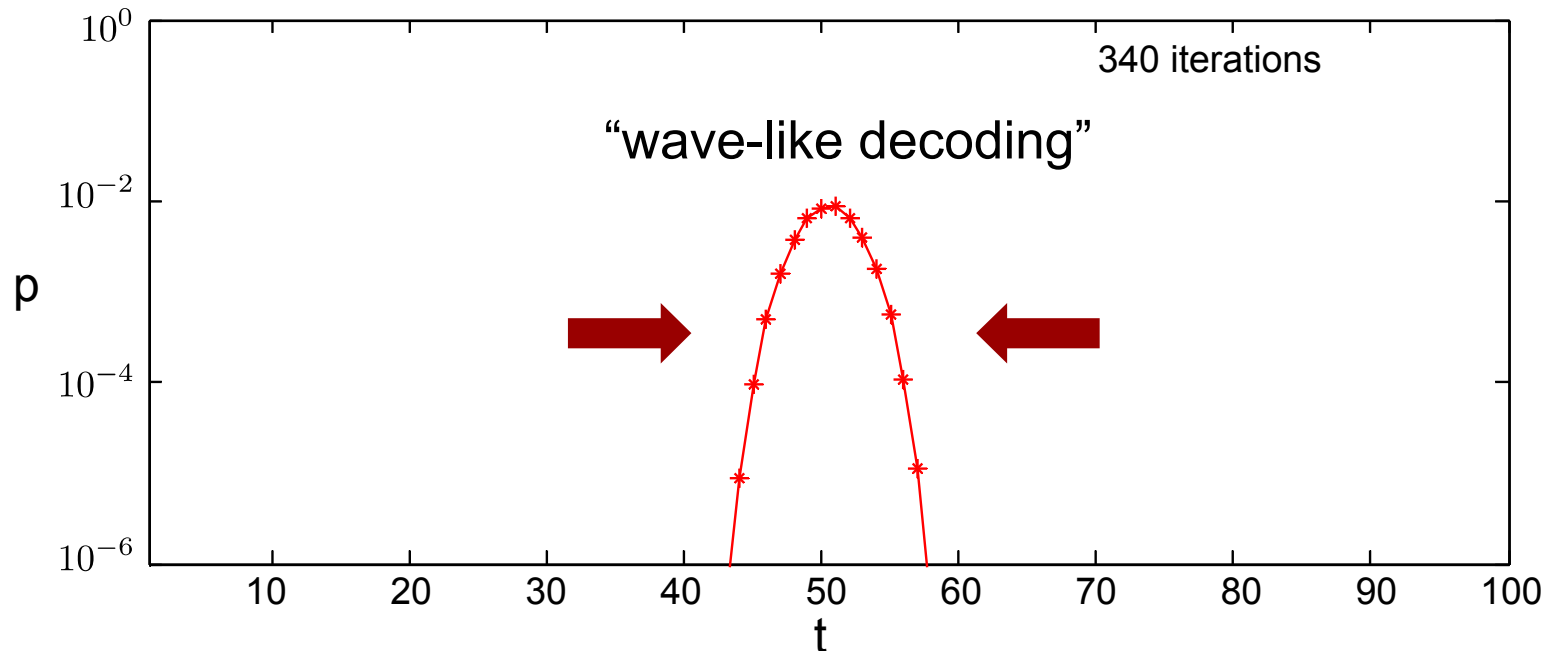
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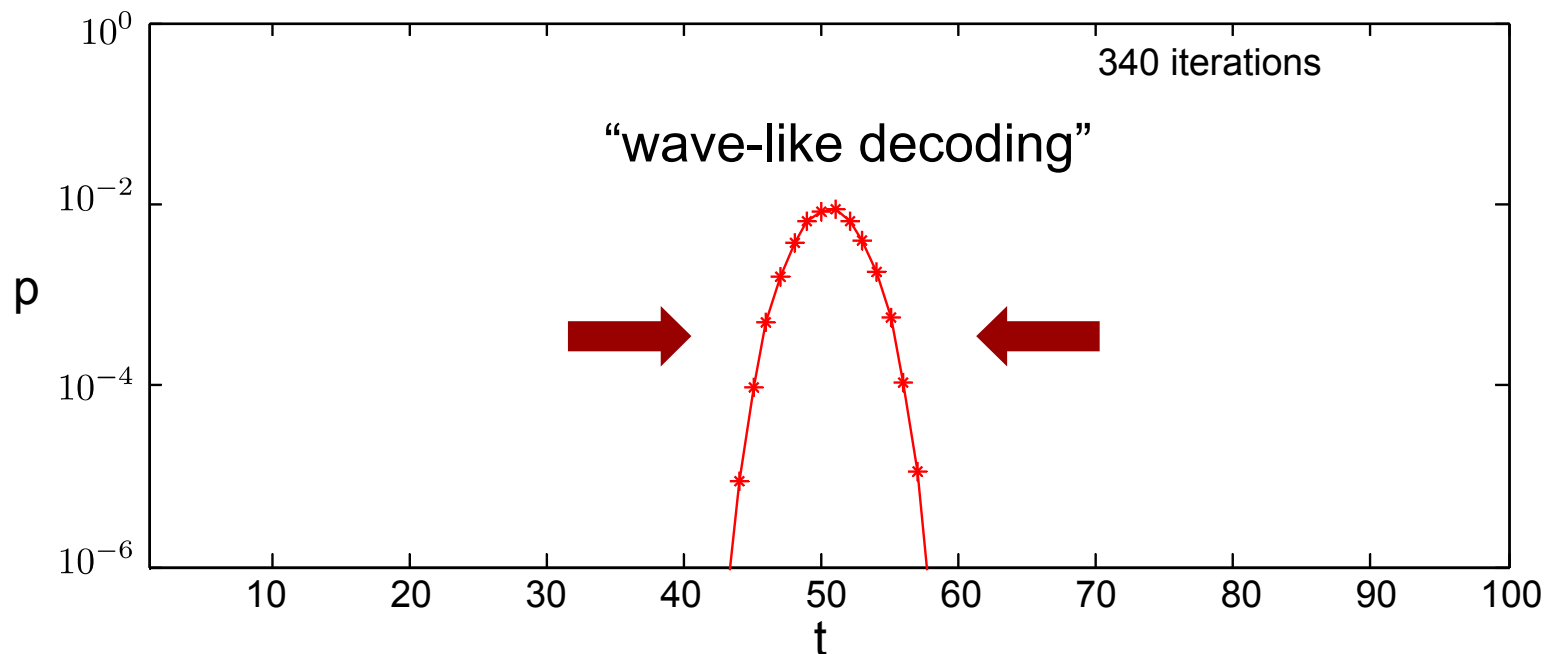
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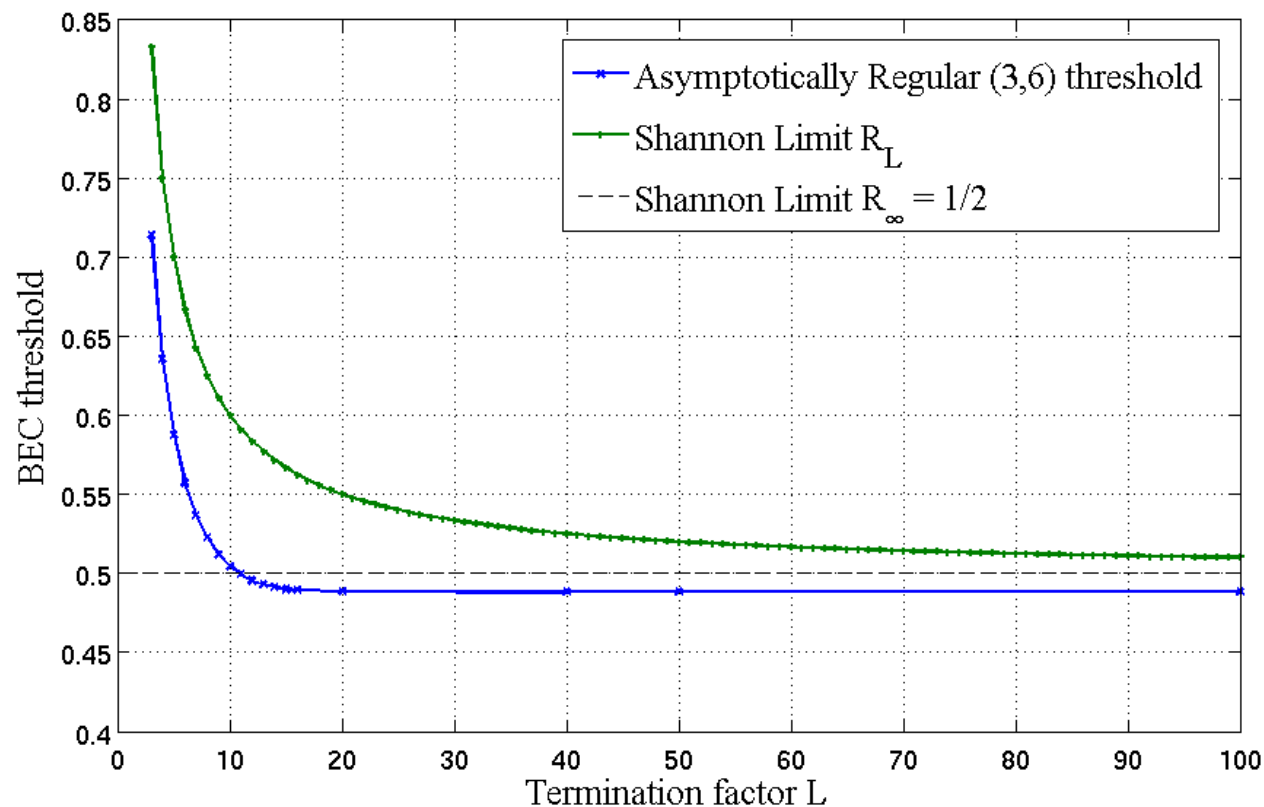


- Note: the **fraction** of **lower degree** nodes tends to zero as  $L \rightarrow \infty$ , i.e., the codes are **asymptotically regular**.



- **Density evolution** can be applied to the protograph-based ensembles with  $M \rightarrow \infty$  [Sridharan et al. '04]:

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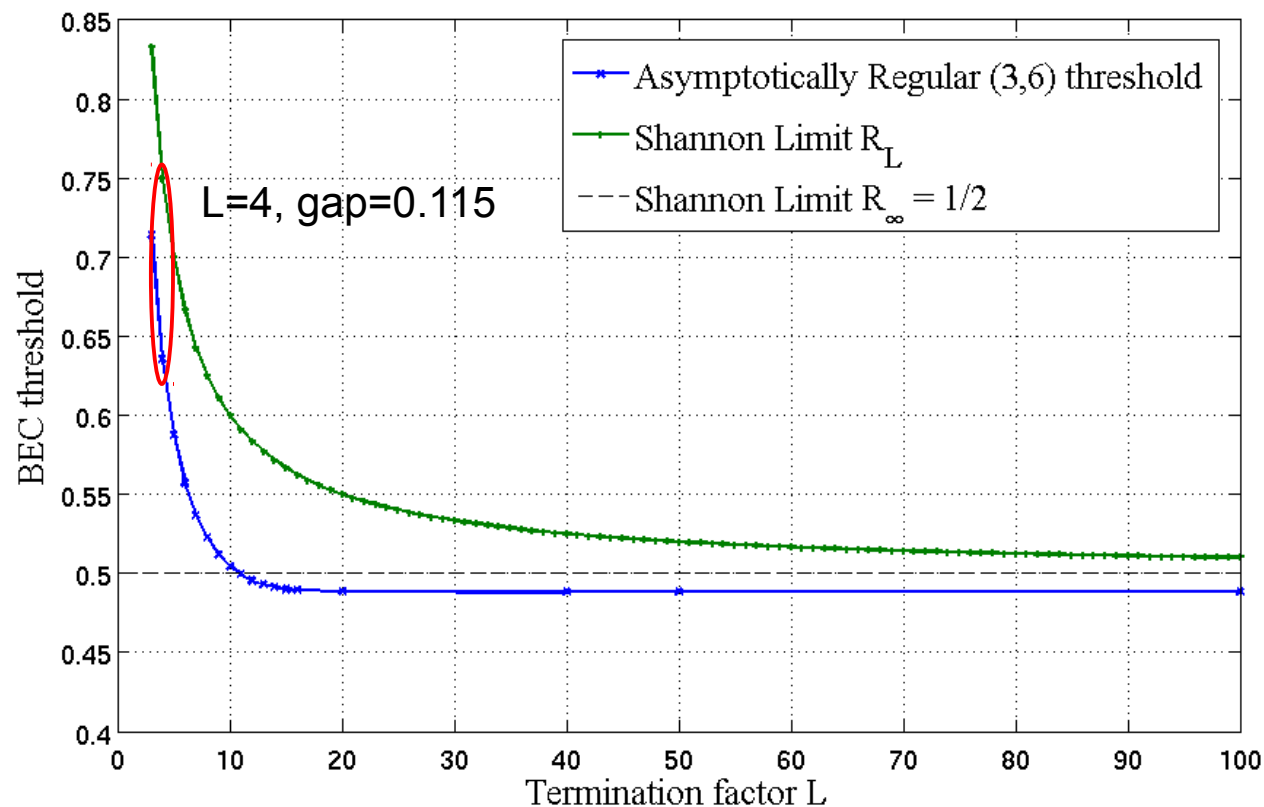


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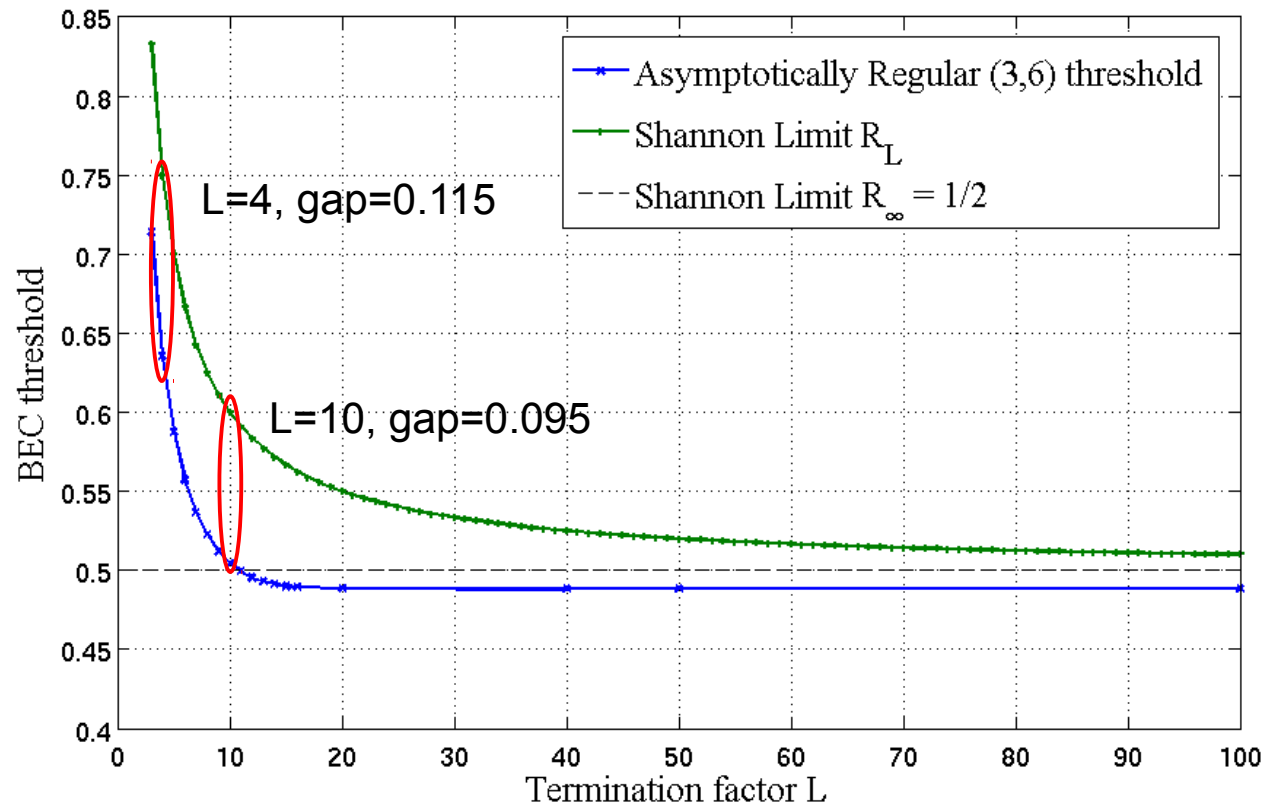
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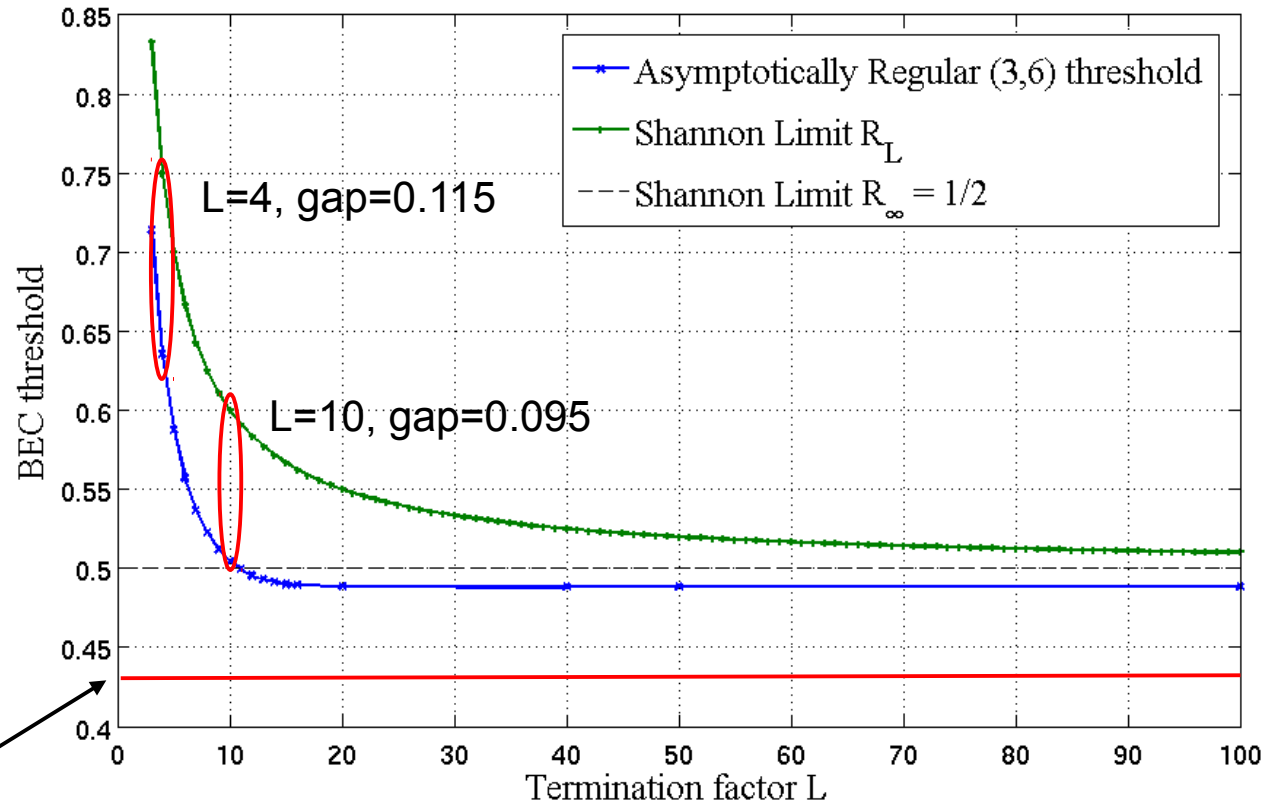
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⋮

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(3,6)-regular block code:

$\epsilon^* = 0.429$



## Iterative decoding thresholds (protograph-based ensembles)

BEC

$(J, K)$	$\epsilon_{SC}^*$	$\epsilon_{blk}^*$
(3,6)	0.488	0.429
(4,8)	0.497	0.383
(5,10)	0.499	0.341

AWGN

$(J, K)$	$E_b/N_{o_{SC}}$	$E_b/N_{o_{blk}}$
(3,6)	0.46 dB	1.11 dB
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- We observe a **significant improvement** in the thresholds of SC-LDPC codes compared to the associated LDPC block codes (LDPC-BCs) due to the lower degree check nodes at the ends of the graph and the wave-like decoding.

[LSCZ10] M. Lentmaier, A. Sridharan, D. J. Costello, Jr., and K.Sh. Zigangirov, "Iterative decoding threshold analysis for LDPC convolutional codes," *IEEE Trans. Inf. Theory*, 56:10, Oct. 2010.

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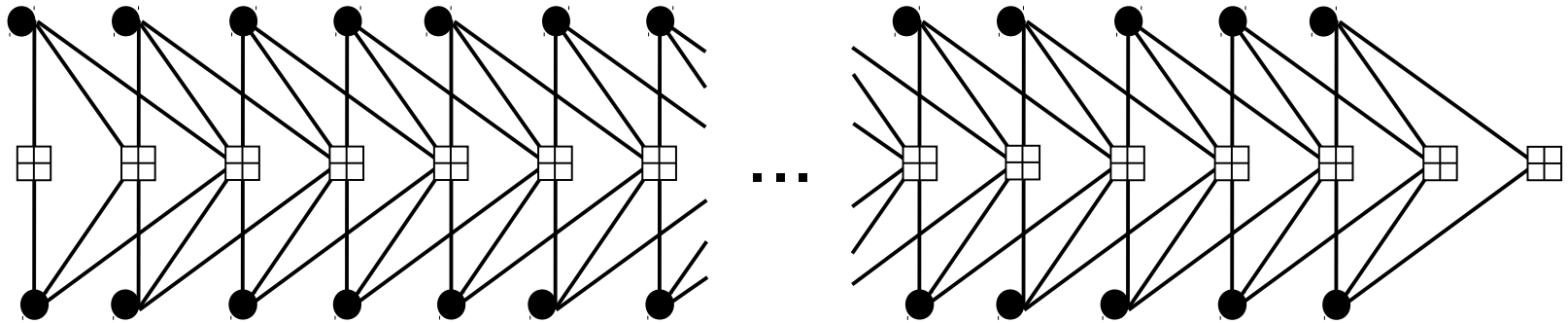
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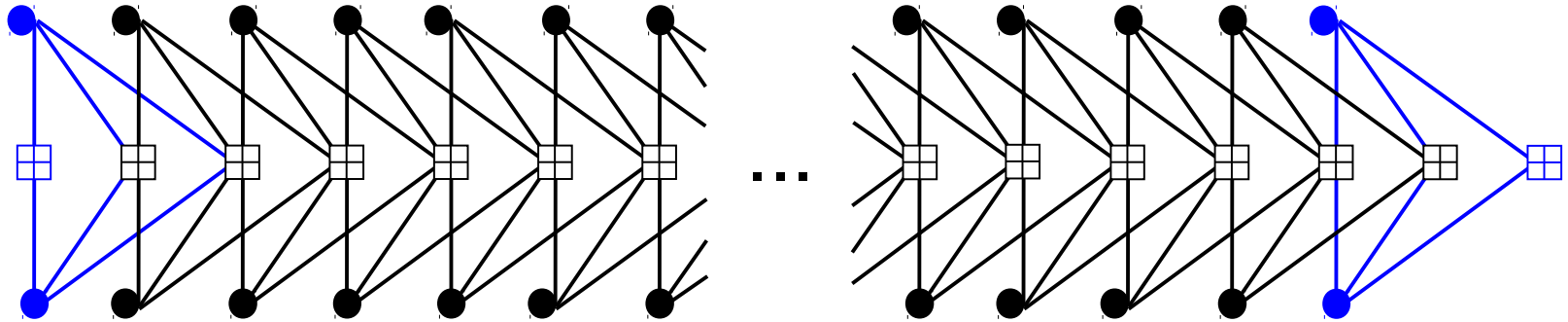
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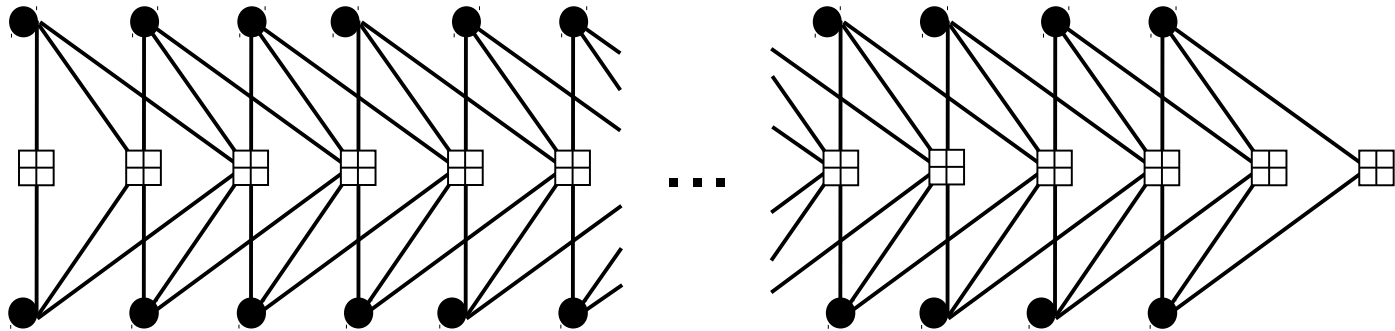
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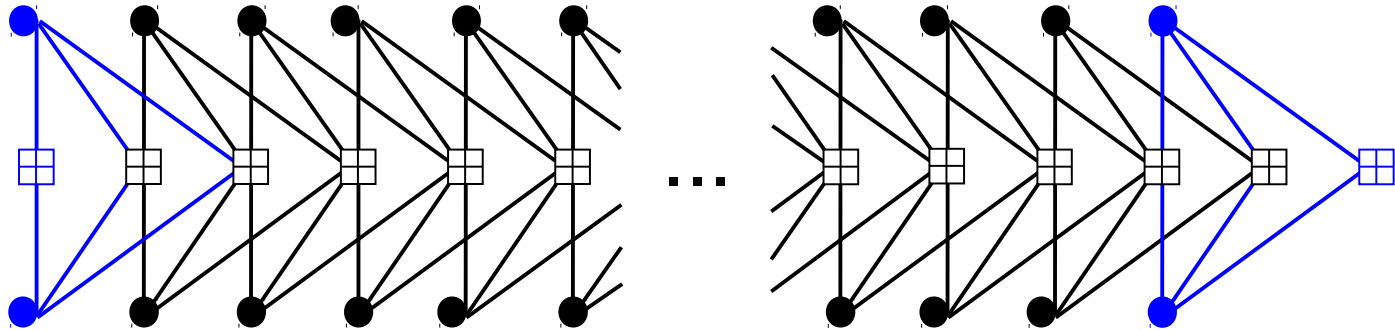
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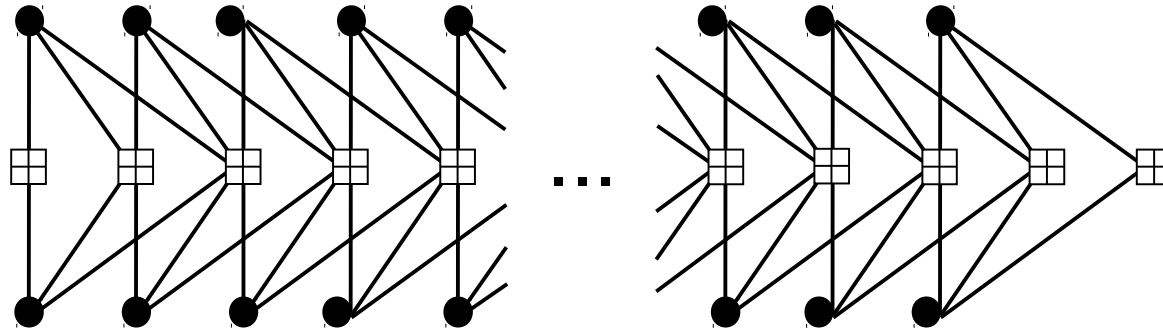
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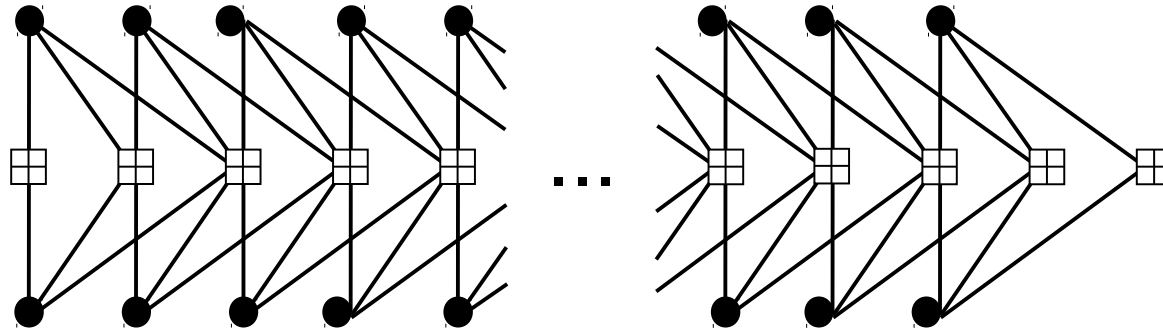
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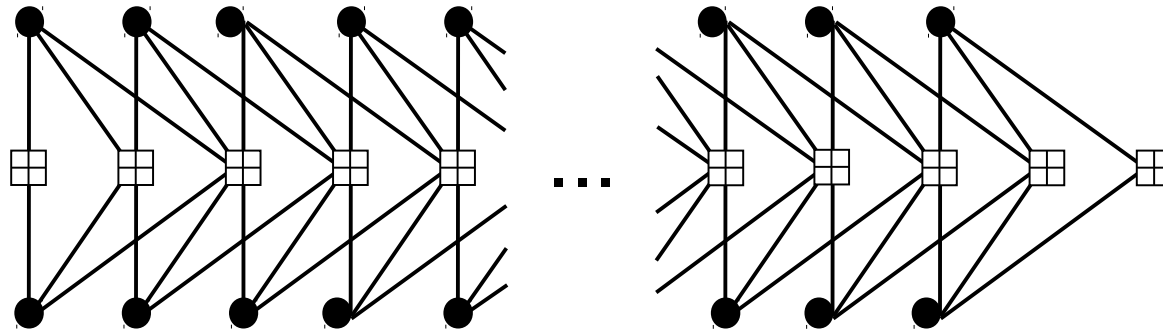


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- For a more random-like ensemble, this has been proven analytically, first for the BEC [KRU11], then for all BMS channels [KRU13].

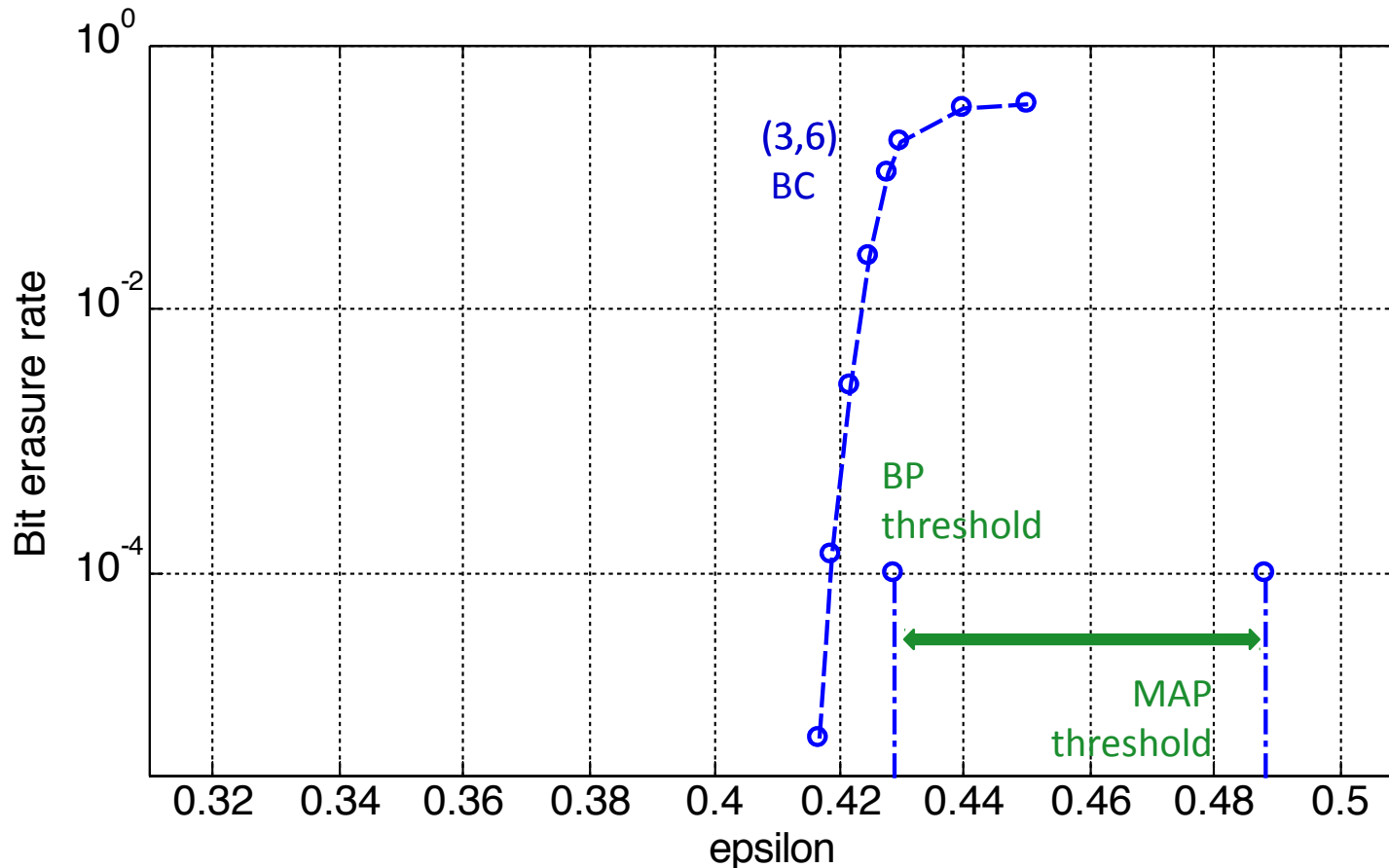
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[KRU11] S. Kudekar, T. J. Richardson and R. Urbanke, “Threshold saturation via spatial coupling: why convolutional LDPC ensembles perform so well over the BEC”, *IEEE Trans. on Inf. Theory*, 57:2, 2011

[KRU13] S. Kudekar, T. J. Richardson and R. Urbanke, “Spatially coupled ensembles universally achieve capacity under belief propagation”, *IEEE Trans. on Inf. Theory*, 59:12, 2013.

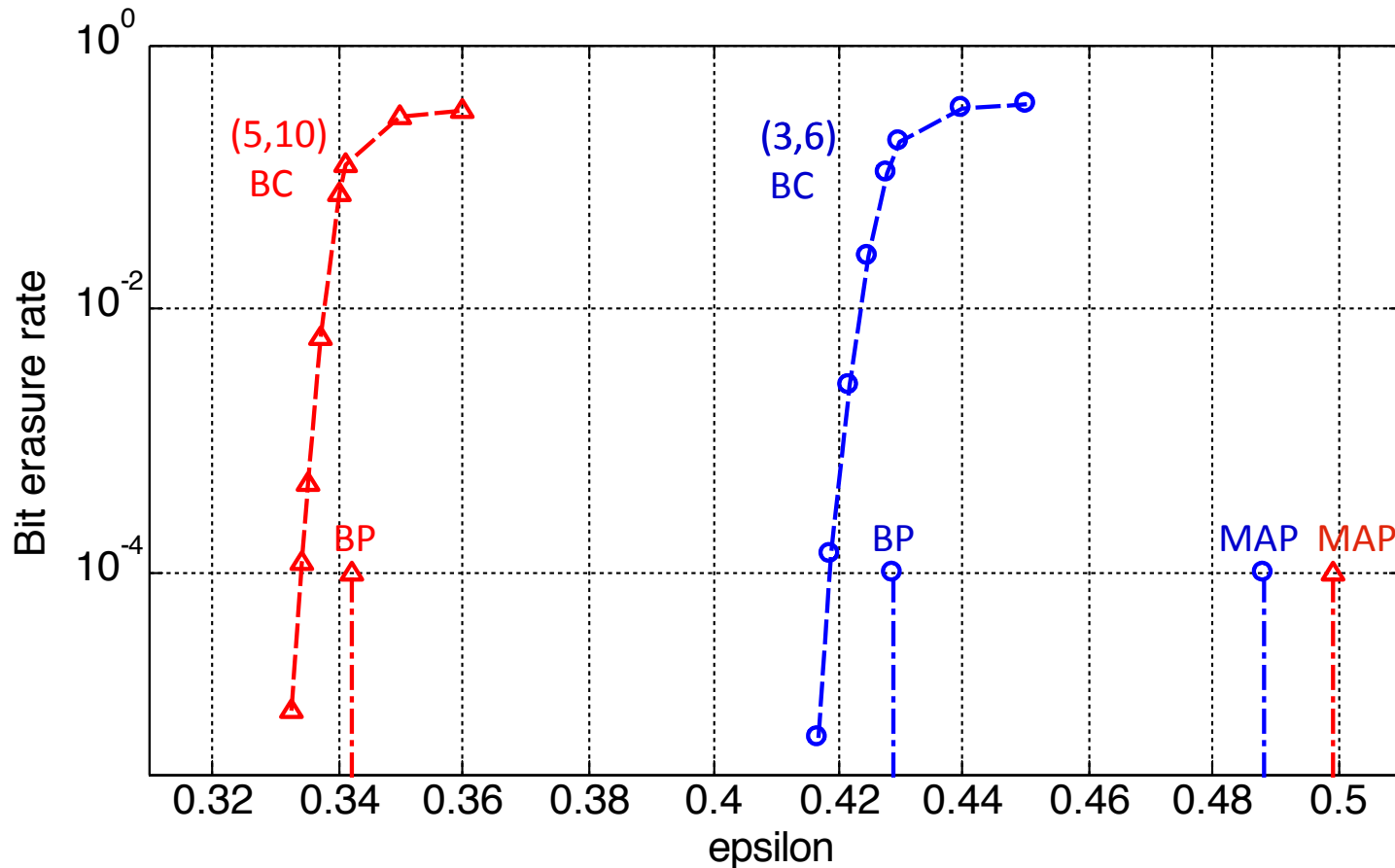
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BP = iterative (suboptimal) decoding threshold  
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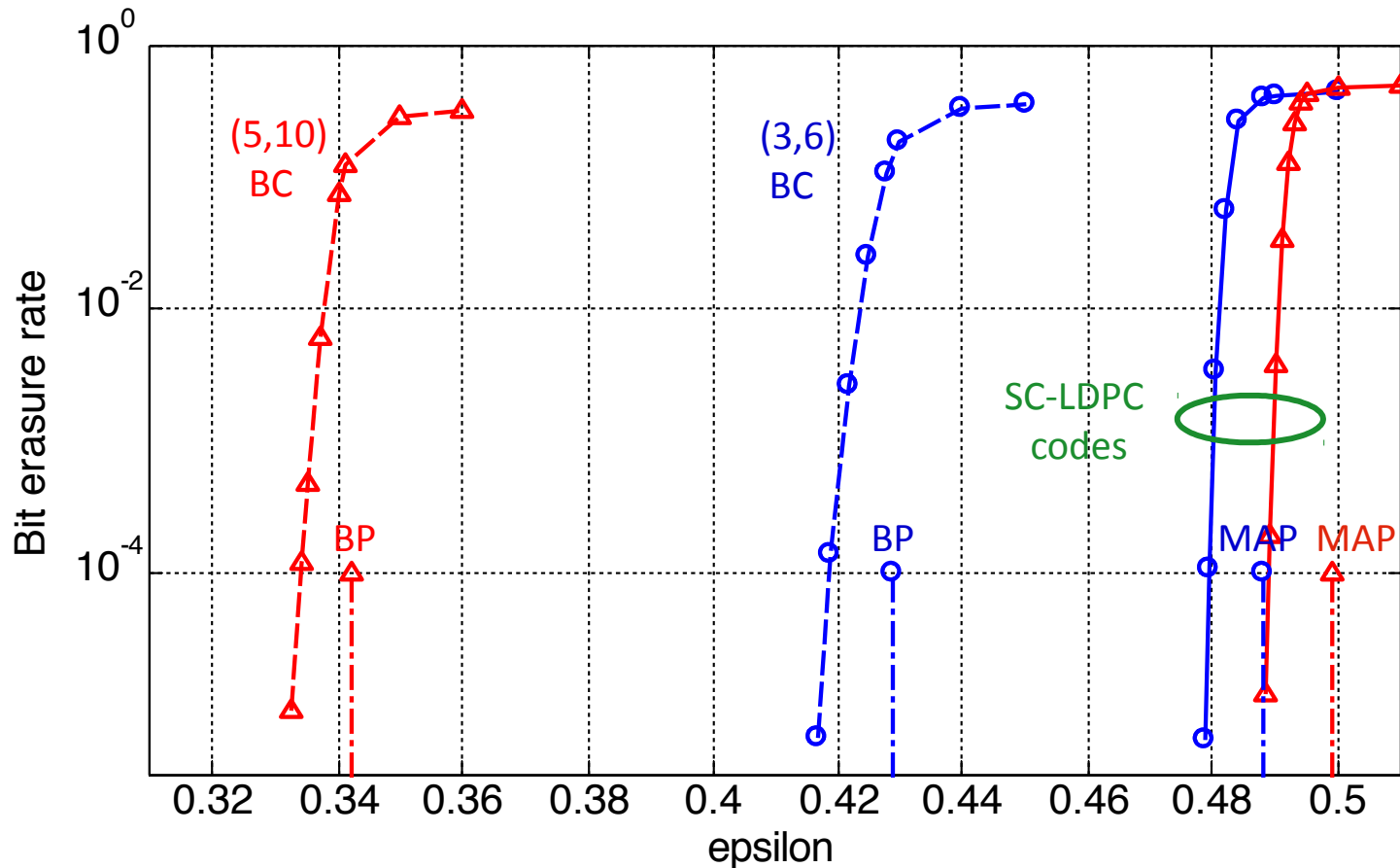
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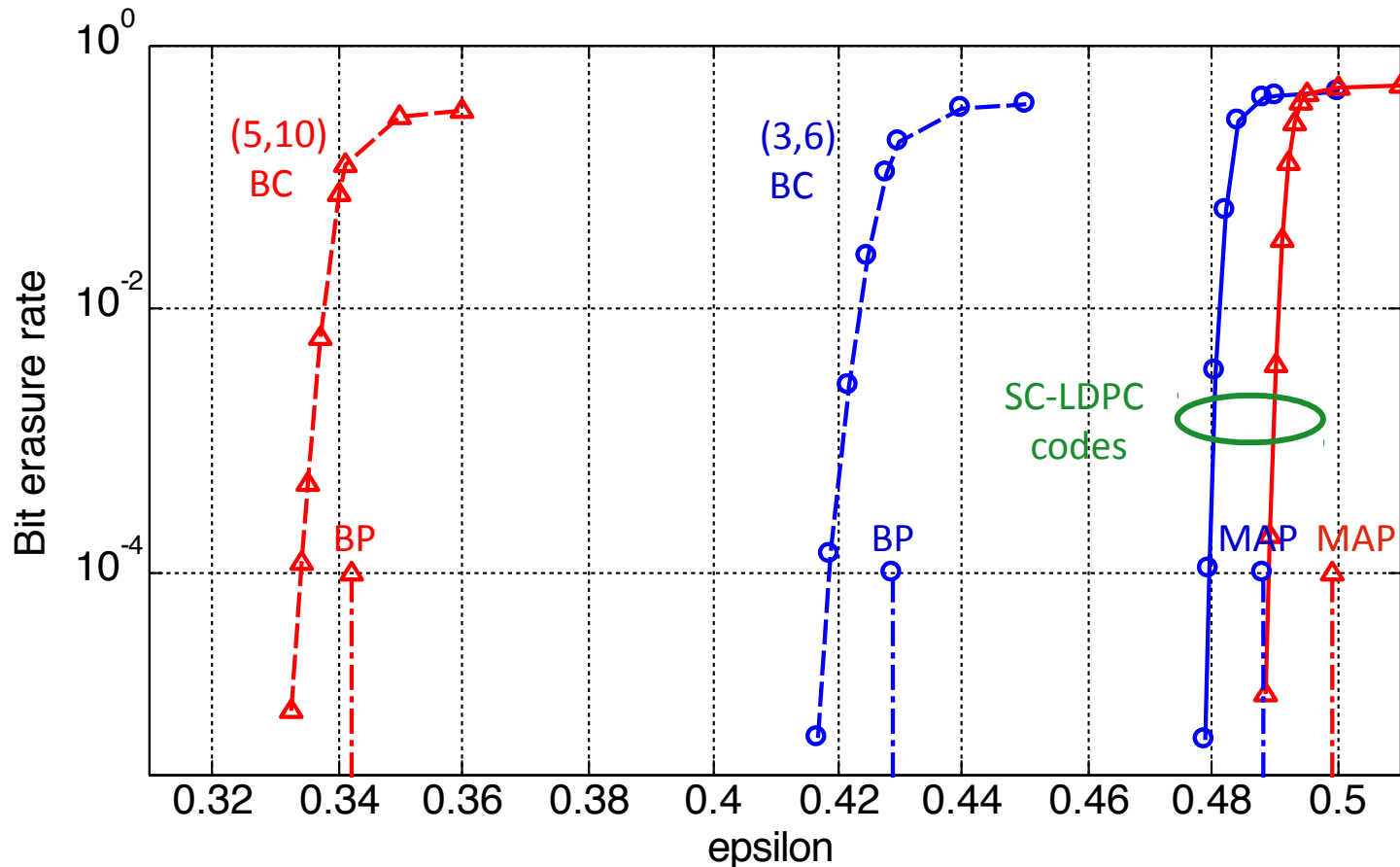
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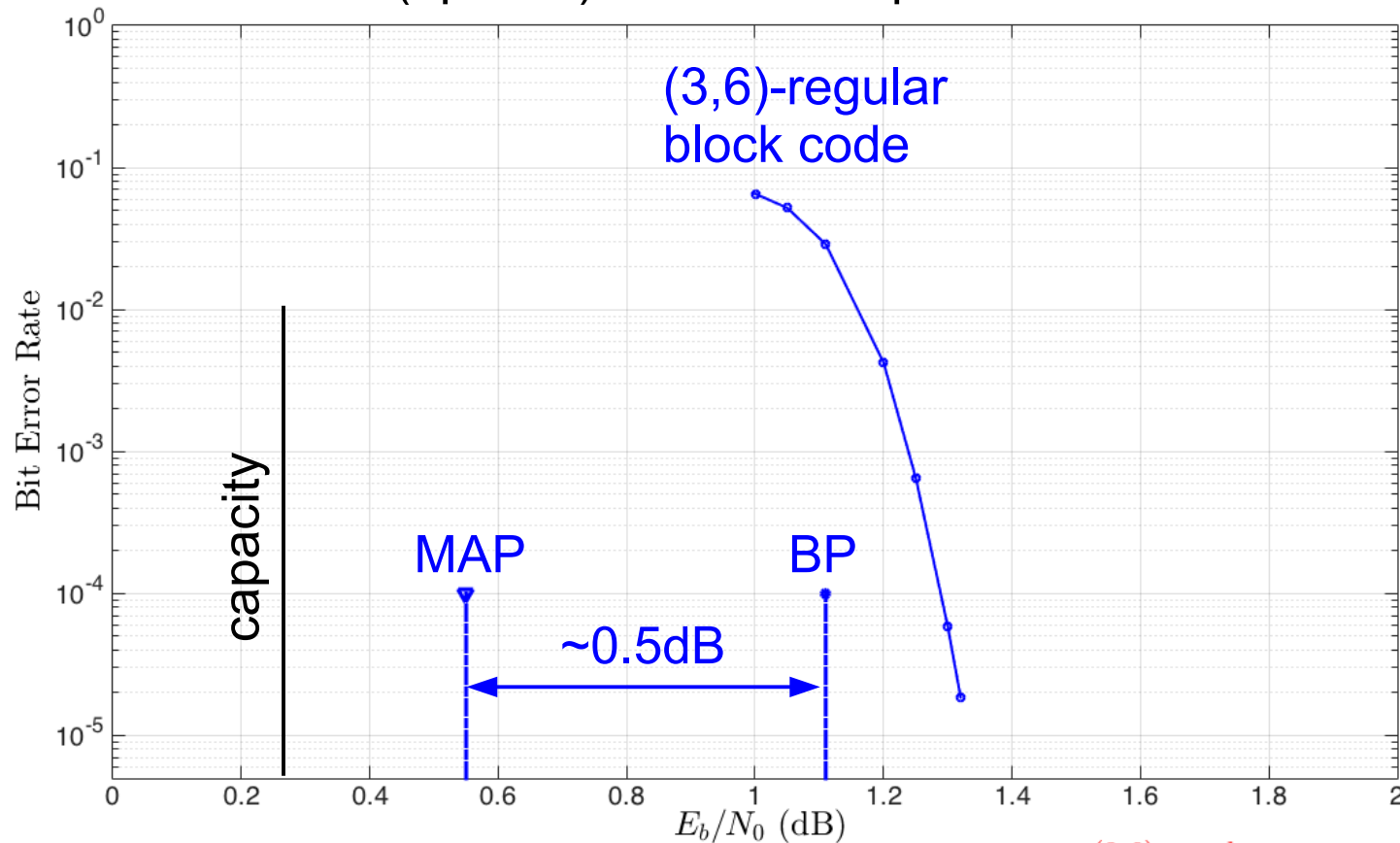
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➔ **optimal** decoding performance with a **suboptimal** iterative algorithm!

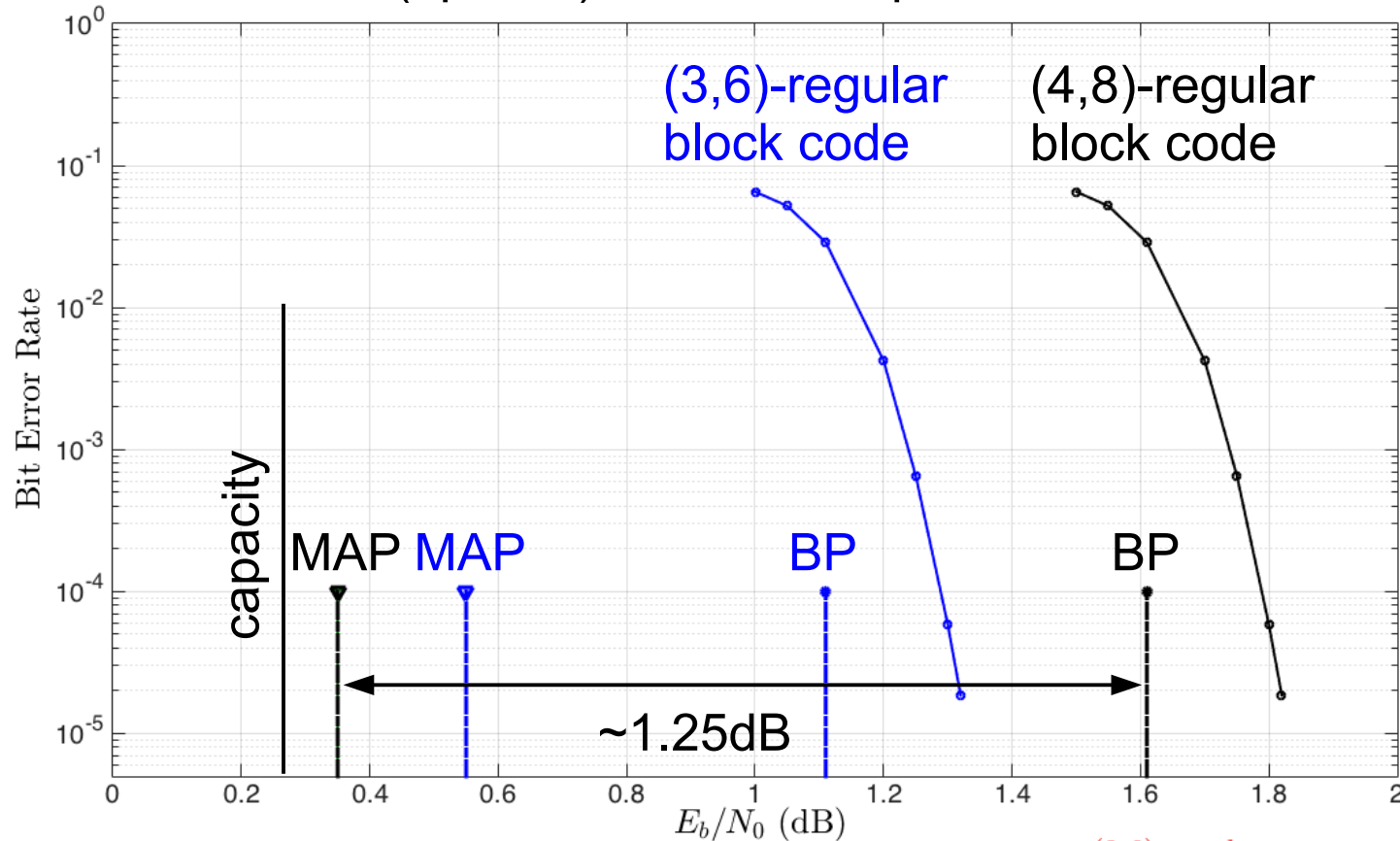
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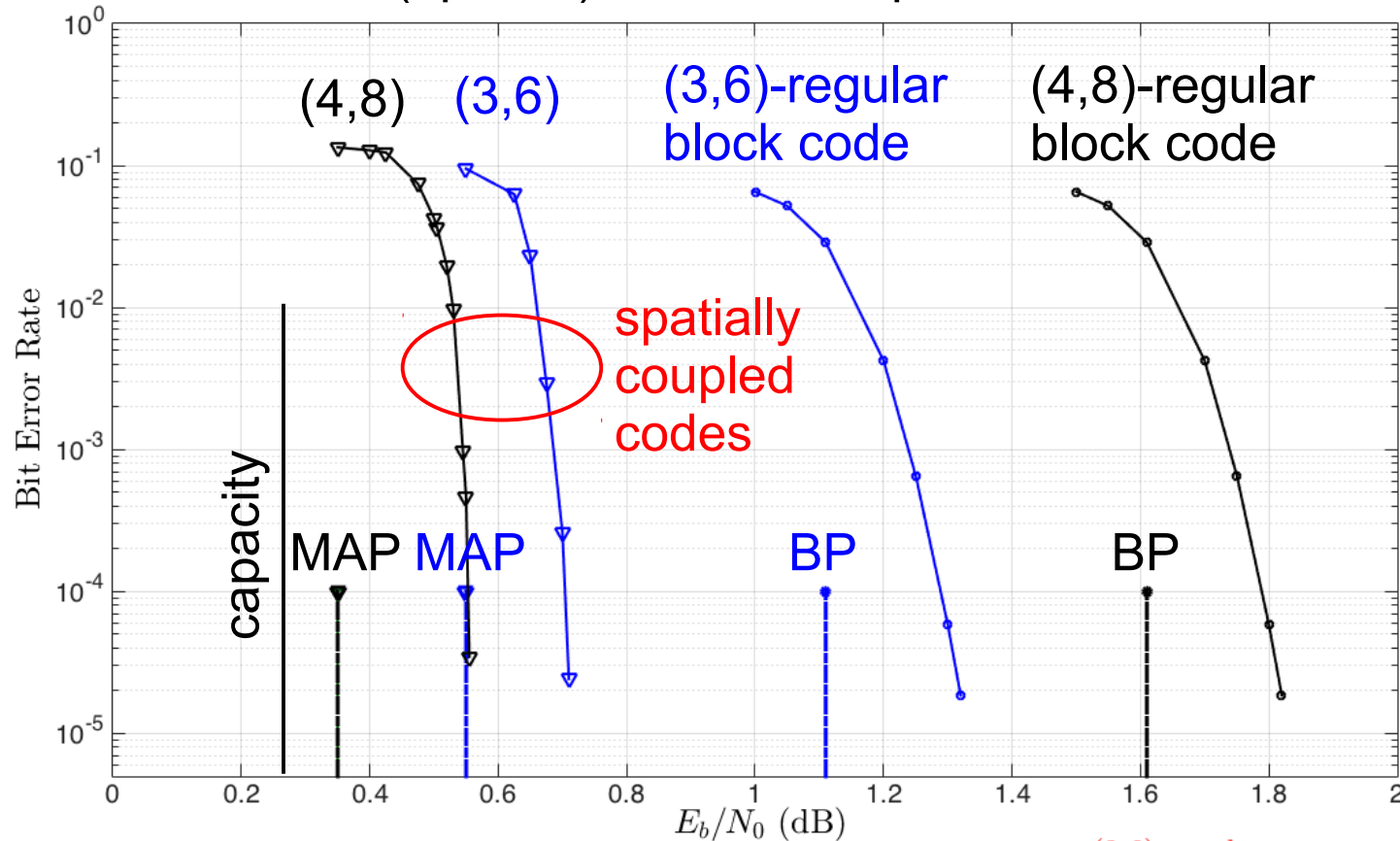
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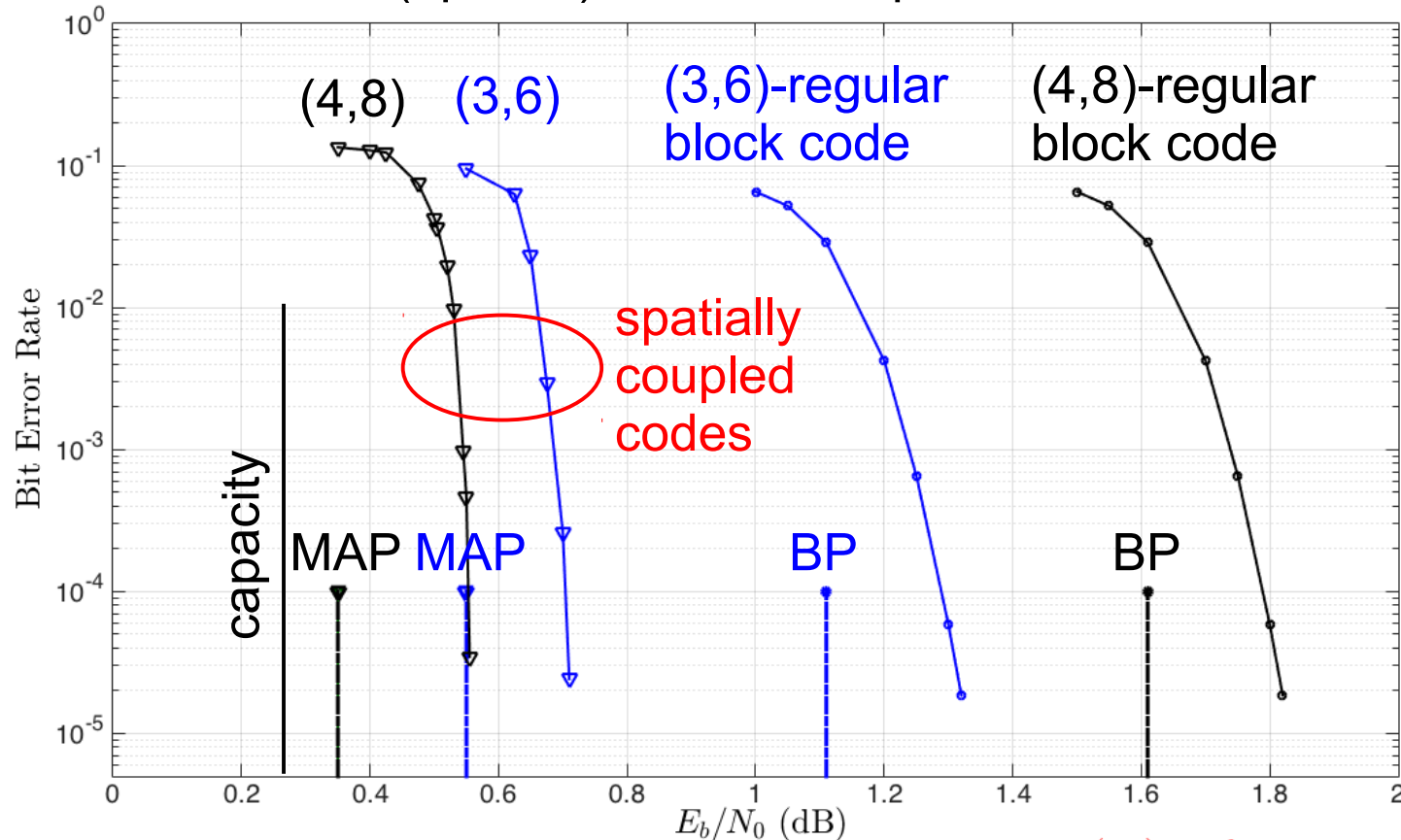
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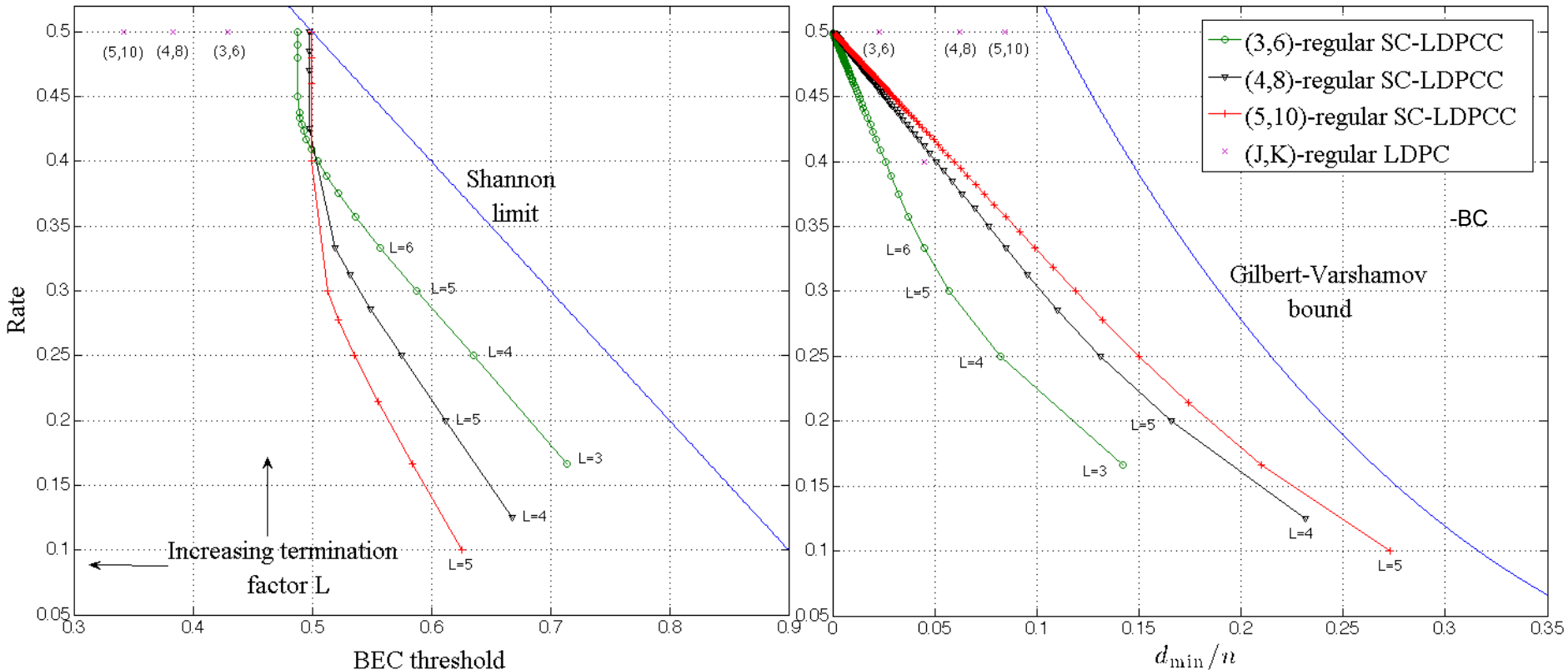
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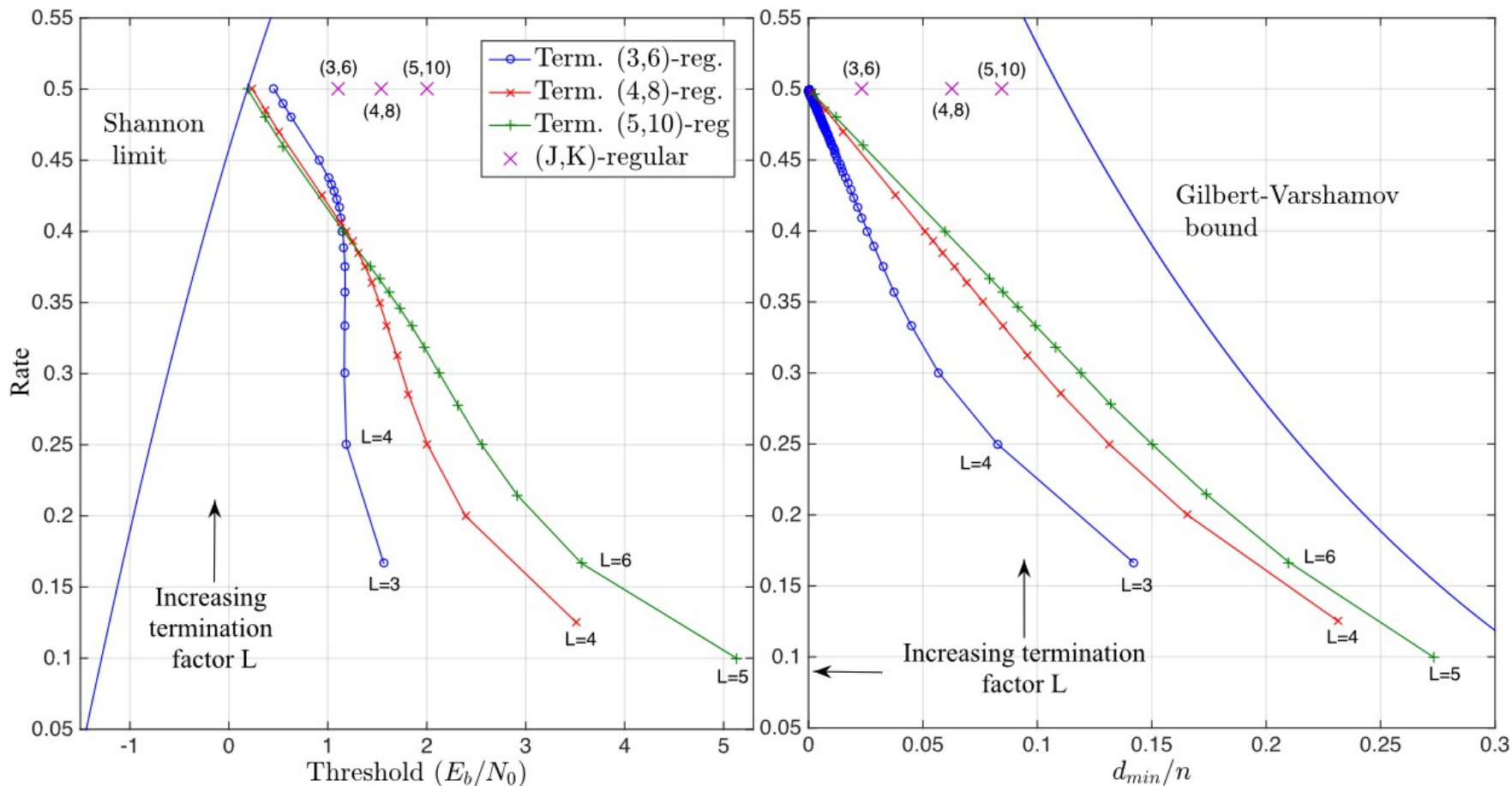
# BEC Thresholds vs Distance Growth

- By increasing  $J$  and  $K$ , we obtain **capacity achieving**  $(J,K)$ -regular SC-LDPC code ensembles with linear minimum distance growth.



- $(J,K)$ -regular SC-LDPC codes combine the best features of irregular and regular LDPC-BCs, i.e., capacity approaching thresholds and linear distance growth.

■ Similar results are obtained for the AWGNC



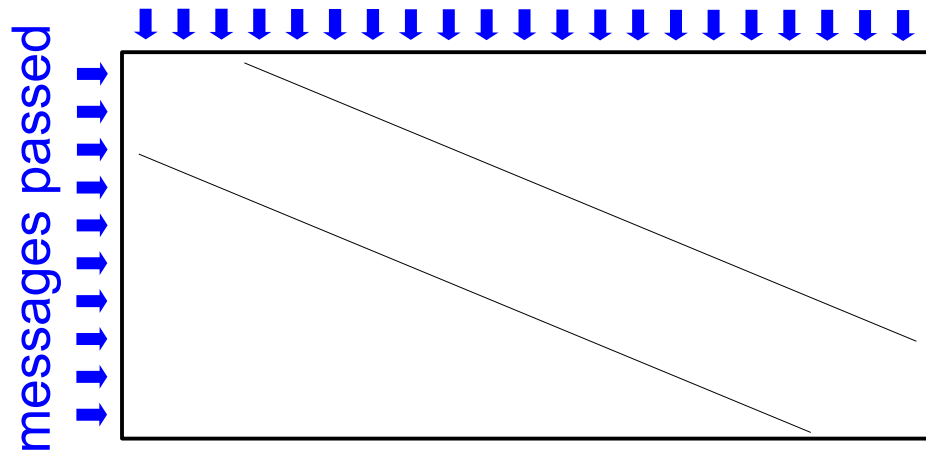
[MLC10] D. G. M. Mitchell, M. Lentmaier and D. J. Costello, Jr., "AWGN Channel Analysis of Terminated LDPC Convolutional Codes", *Proc. Information Theory and Applications Workshop*, San Diego, Feb. 2011.

- **Introduction: From Shannon to Modern Coding Theory**
  - ➔ Channel capacity, structured codes, random codes, LDPC codes
- **LDPC Block Codes**
  - ➔ Parity-check matrix and Tanner graph representations, minimum distance bounds, iterative decoding thresholds, protograph-based constructions
- **Spatially Coupled LDPC Codes**
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  - ➔ Iterative decoding thresholds, threshold saturation, minimum distance
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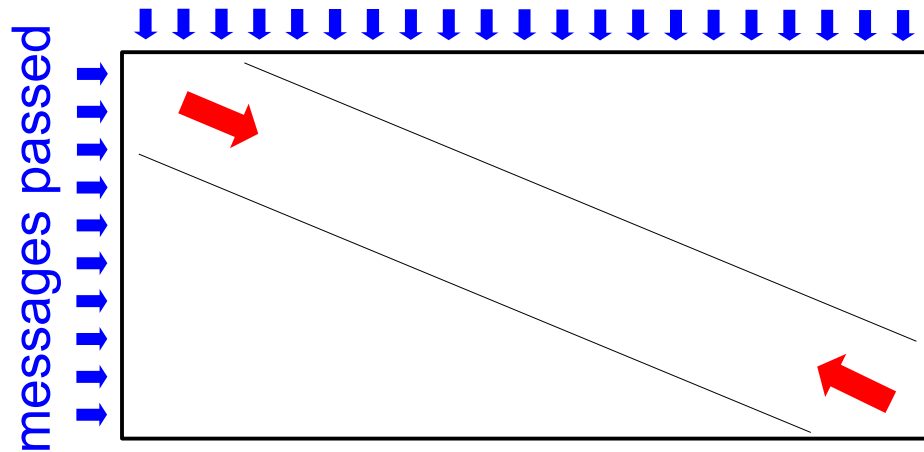
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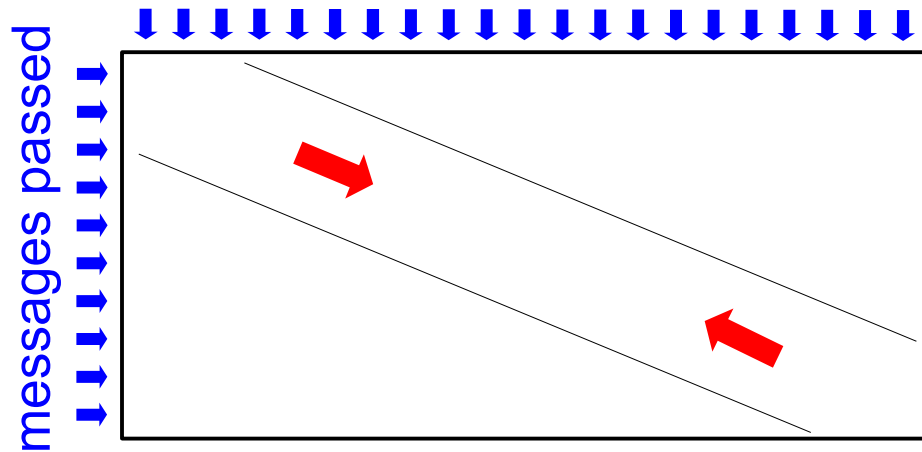
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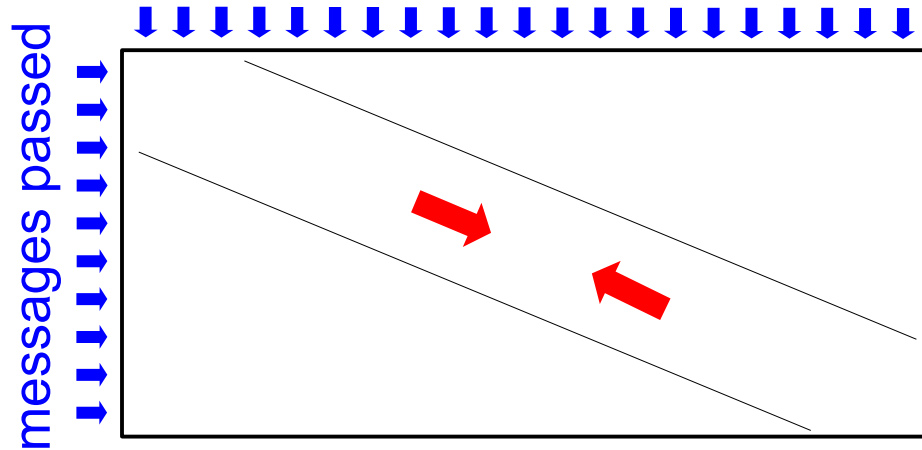
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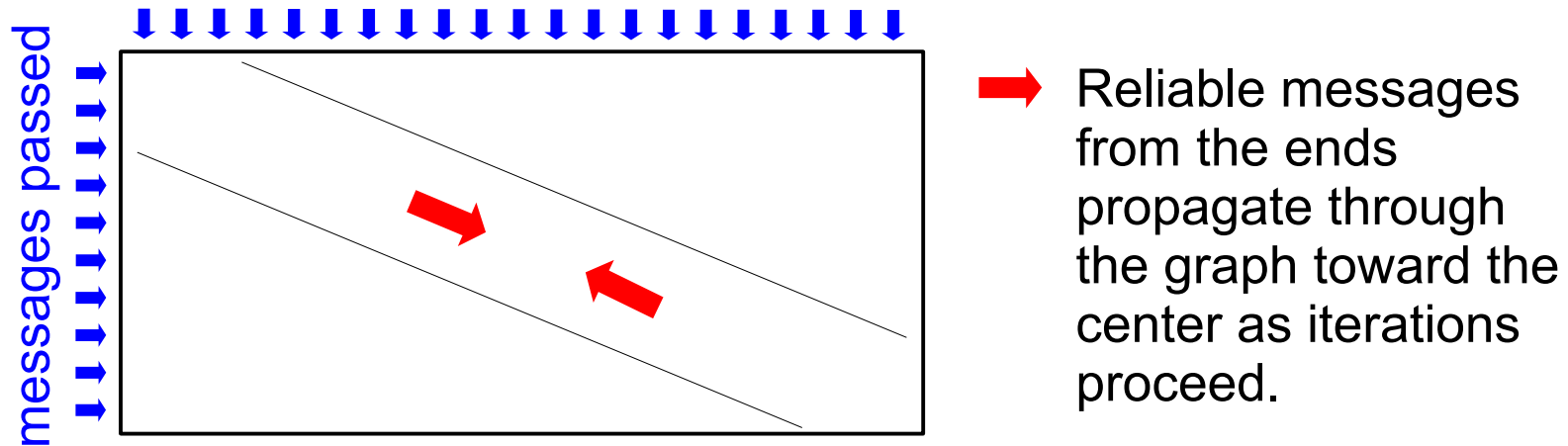
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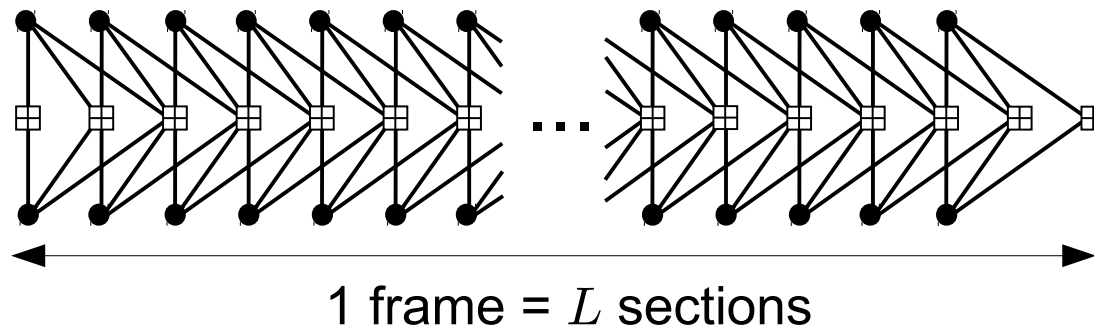
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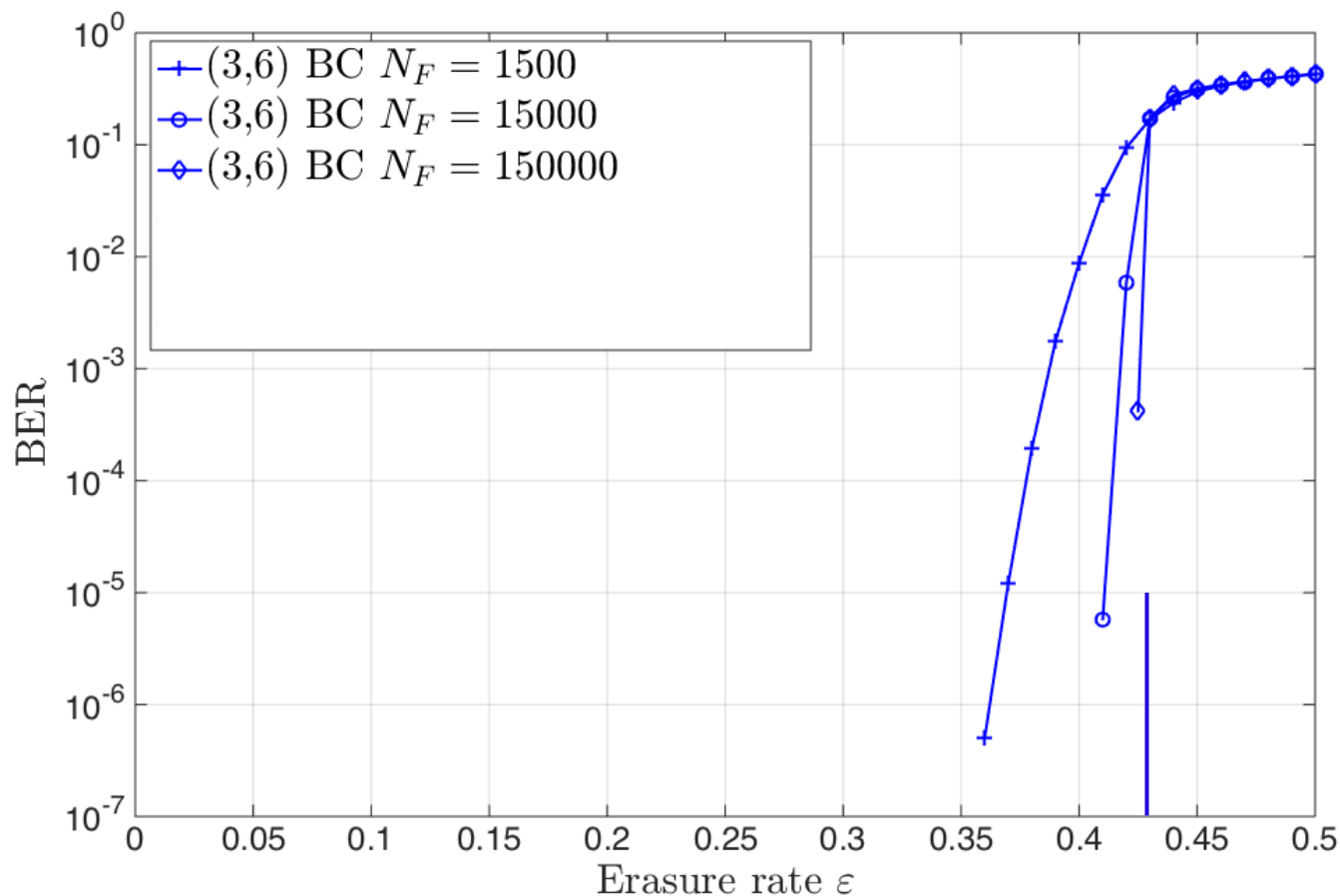
- The **frame error rate (FER)** of a terminated graph can be analyzed



→ The **FER depends on  $L$**  ( $\text{FER} \xrightarrow{L \rightarrow \infty} 1$ )

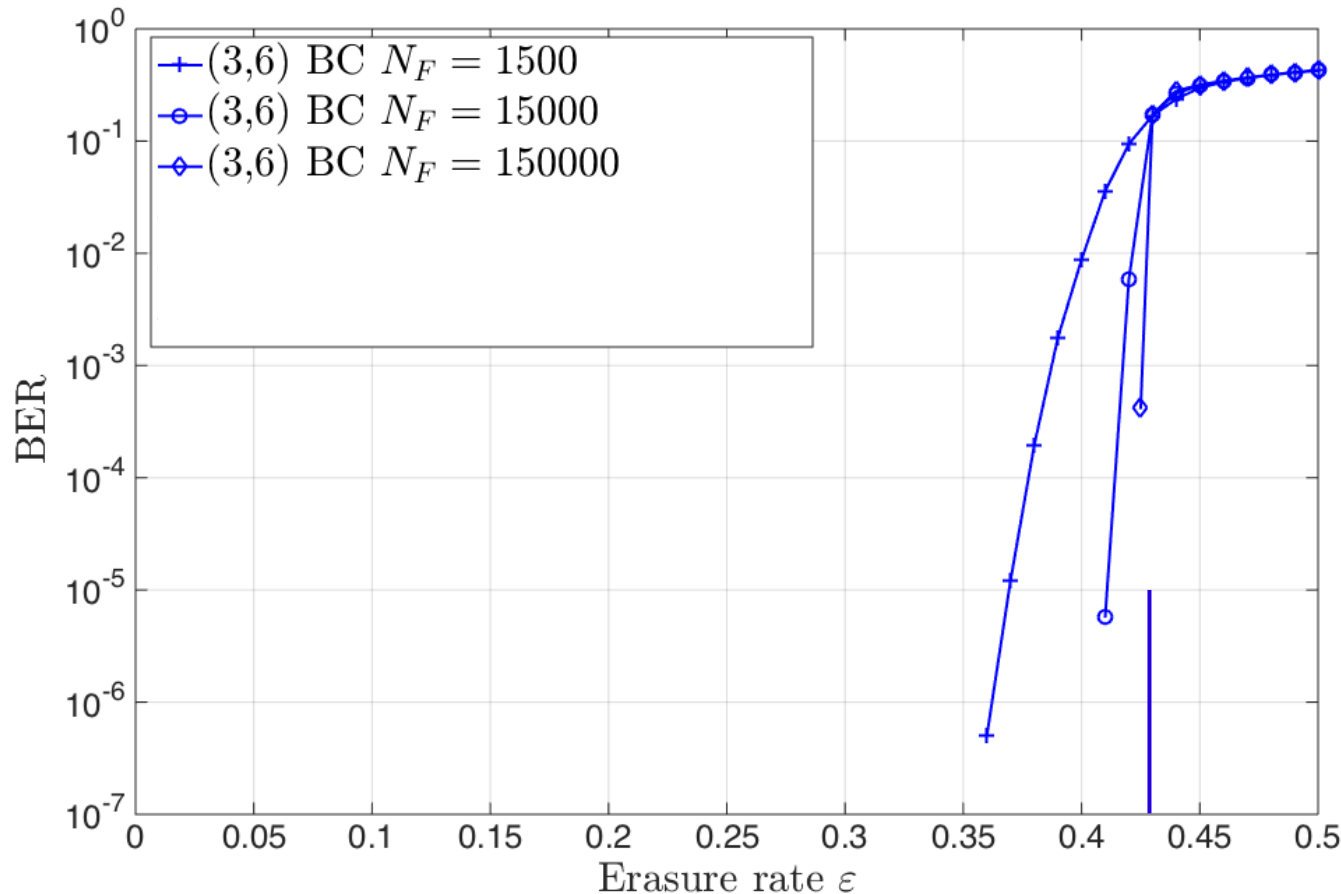
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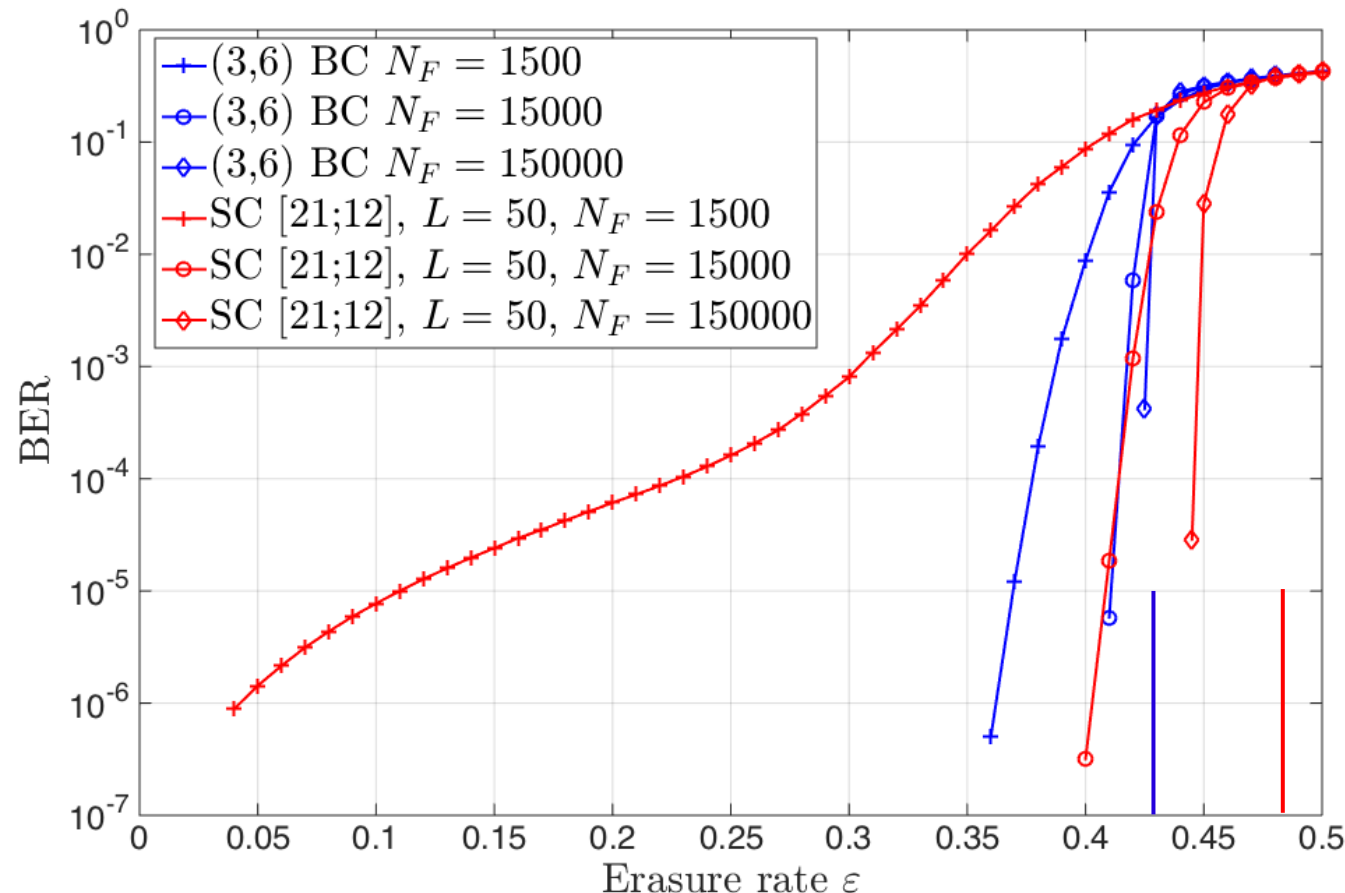


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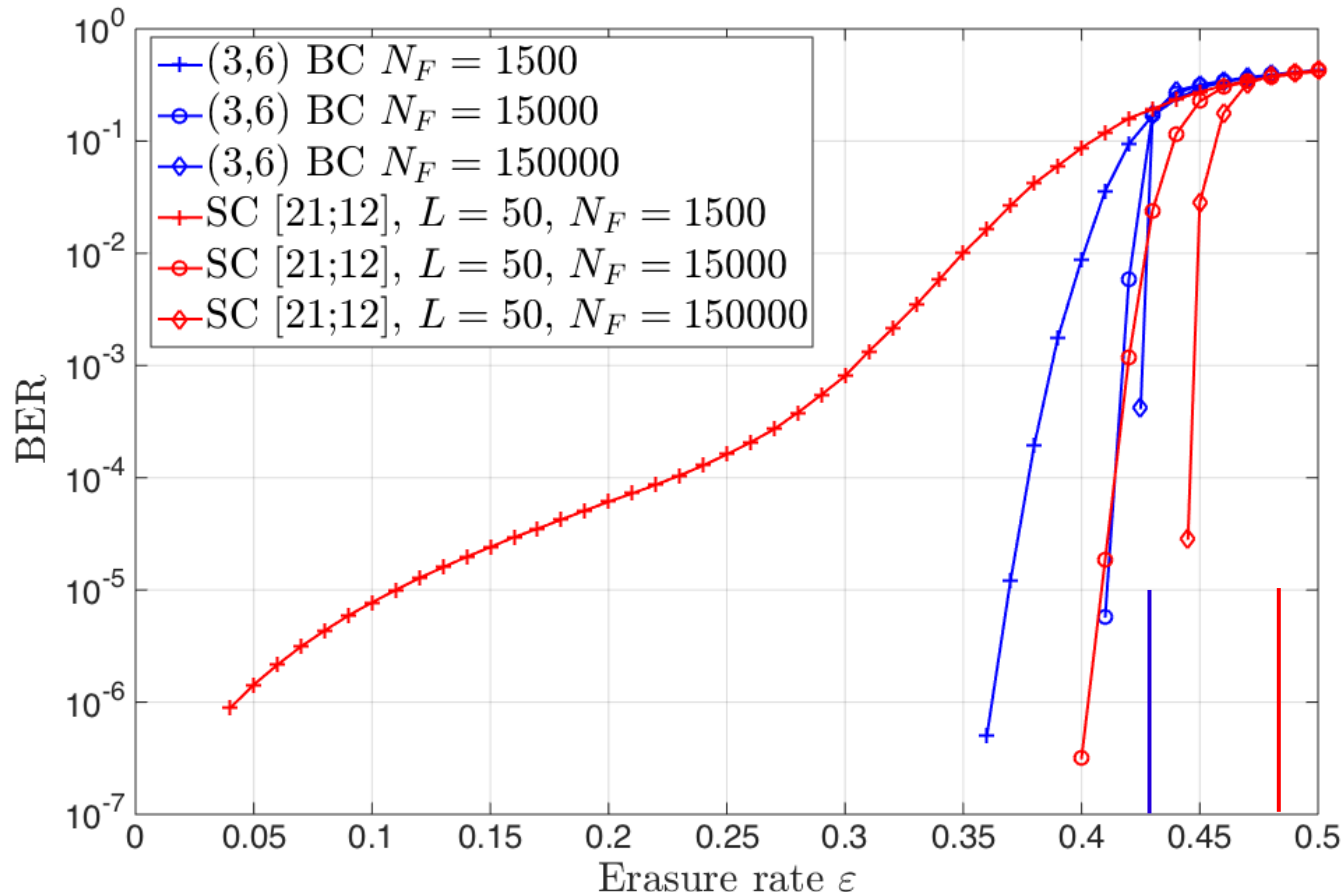
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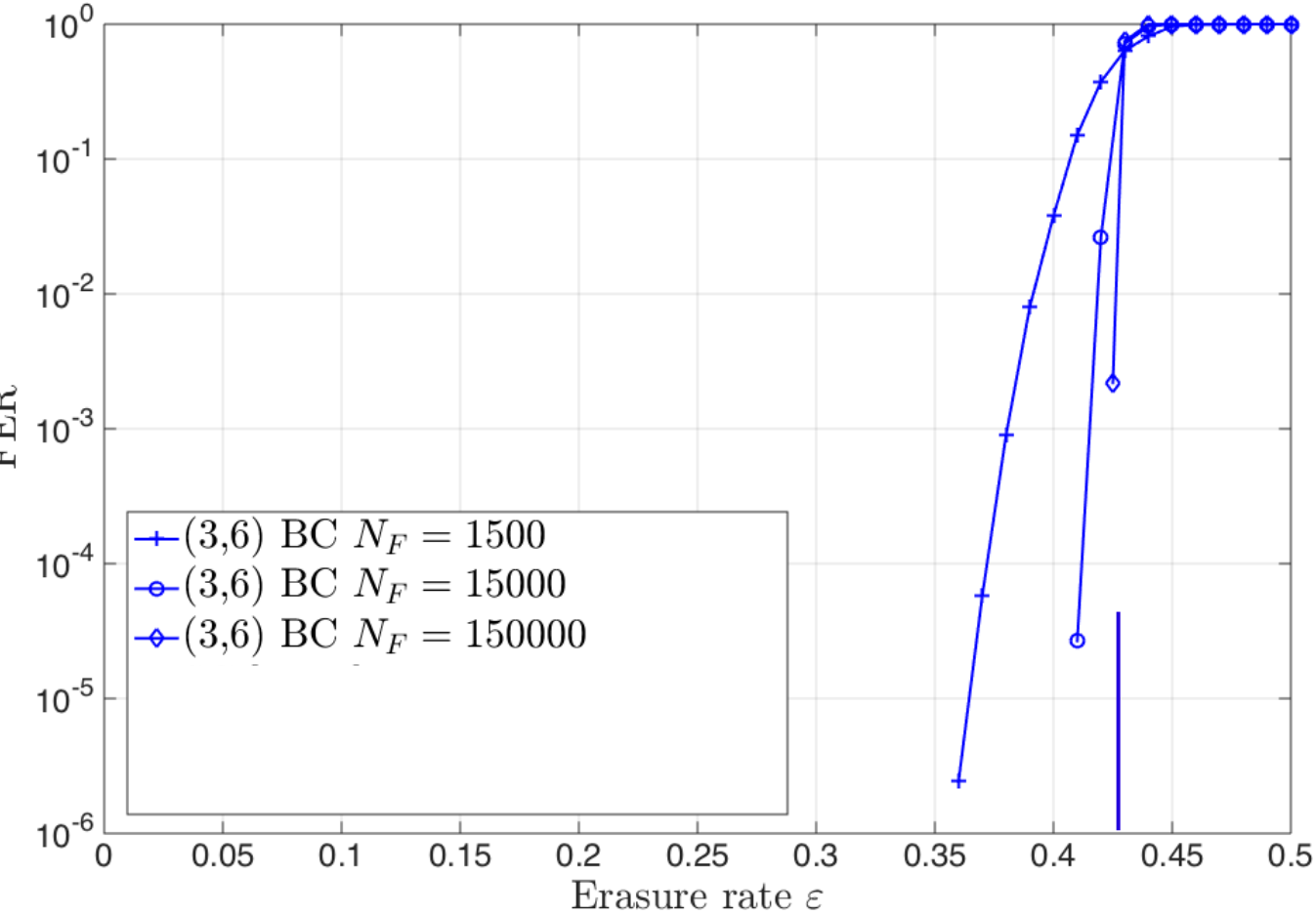


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- As  $N_F = 2LM$  increases (fixed  $L$  and increasing  $M = 15, 150, 1500$ ), the SC-LDPC code performance approaches  $\epsilon^* = 0.488$ , outperforming the LDPC-BC

# Flooding Decoding Scaling (2) – BEC



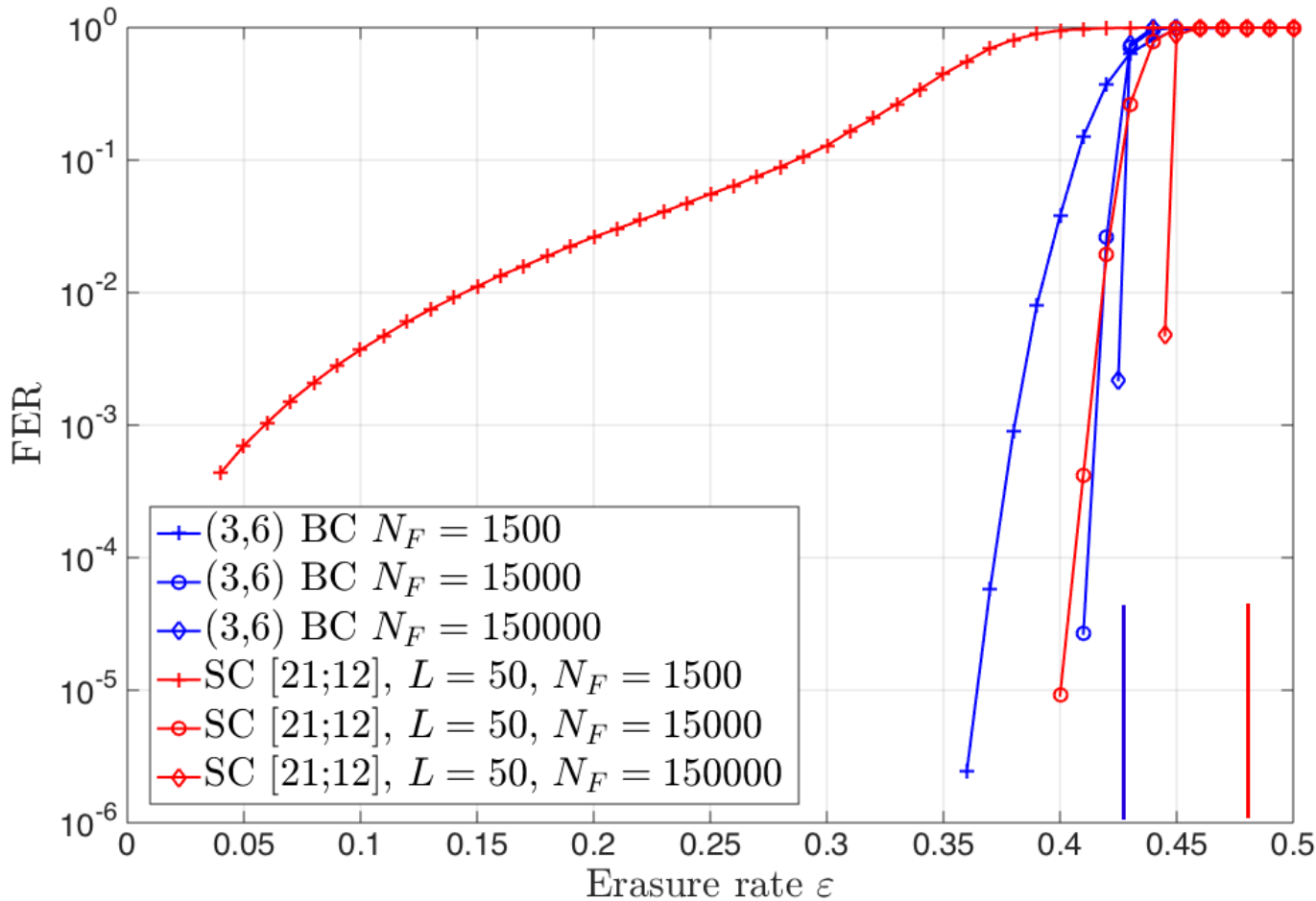
Consider LDPC-BCs and SC-LDPC codes with increasing frame length  $N_F$



We now consider the FER performance

# Flooding Decoding Scaling (2) – BEC

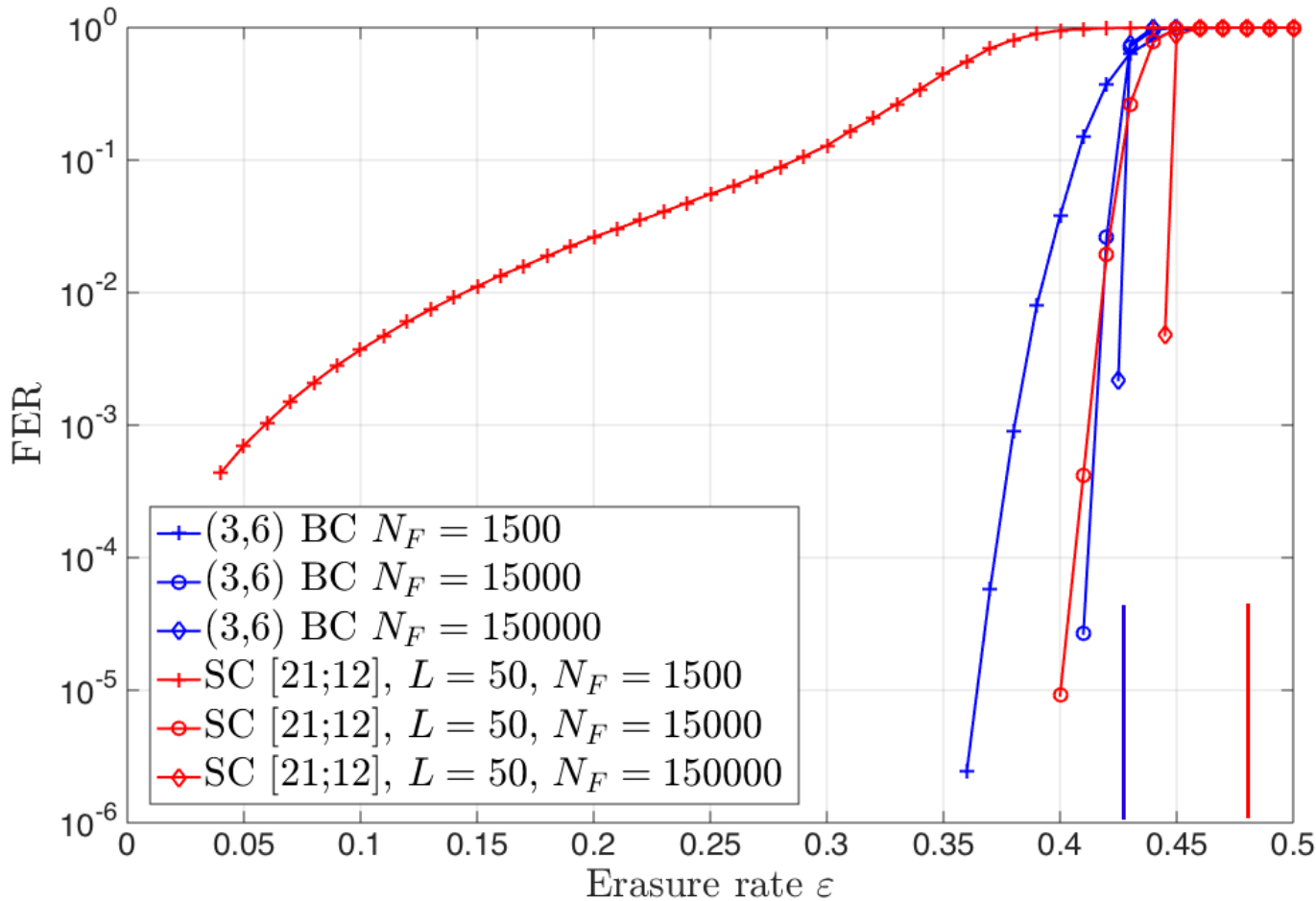
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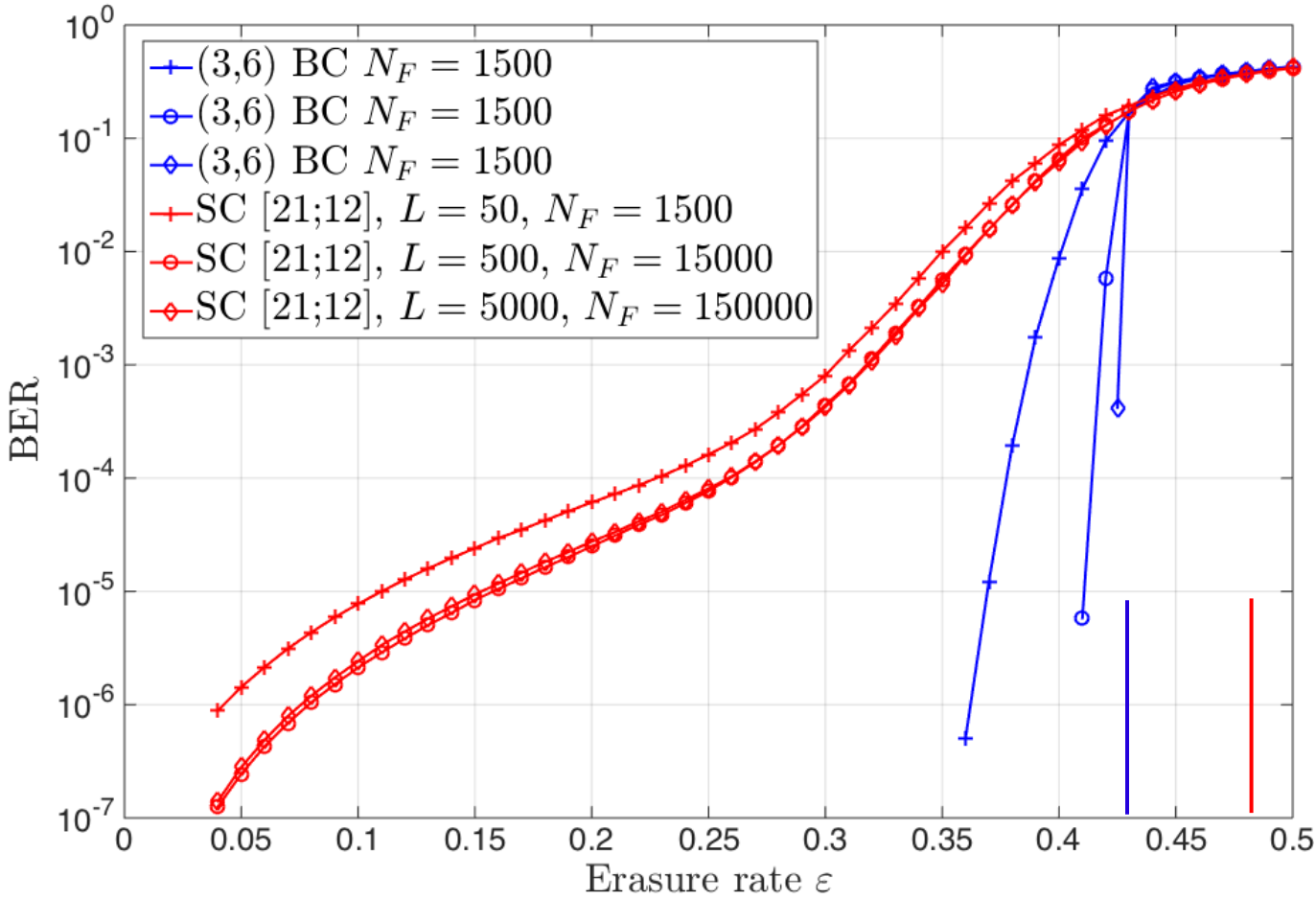
The FER curves display similar behavior for fixed  $L$  and increasing  $M$

➔ The SC-LDPC codes outperform LDPC-BCs for large  $N_F$

# Flooding Decoding Scaling (3) – BEC



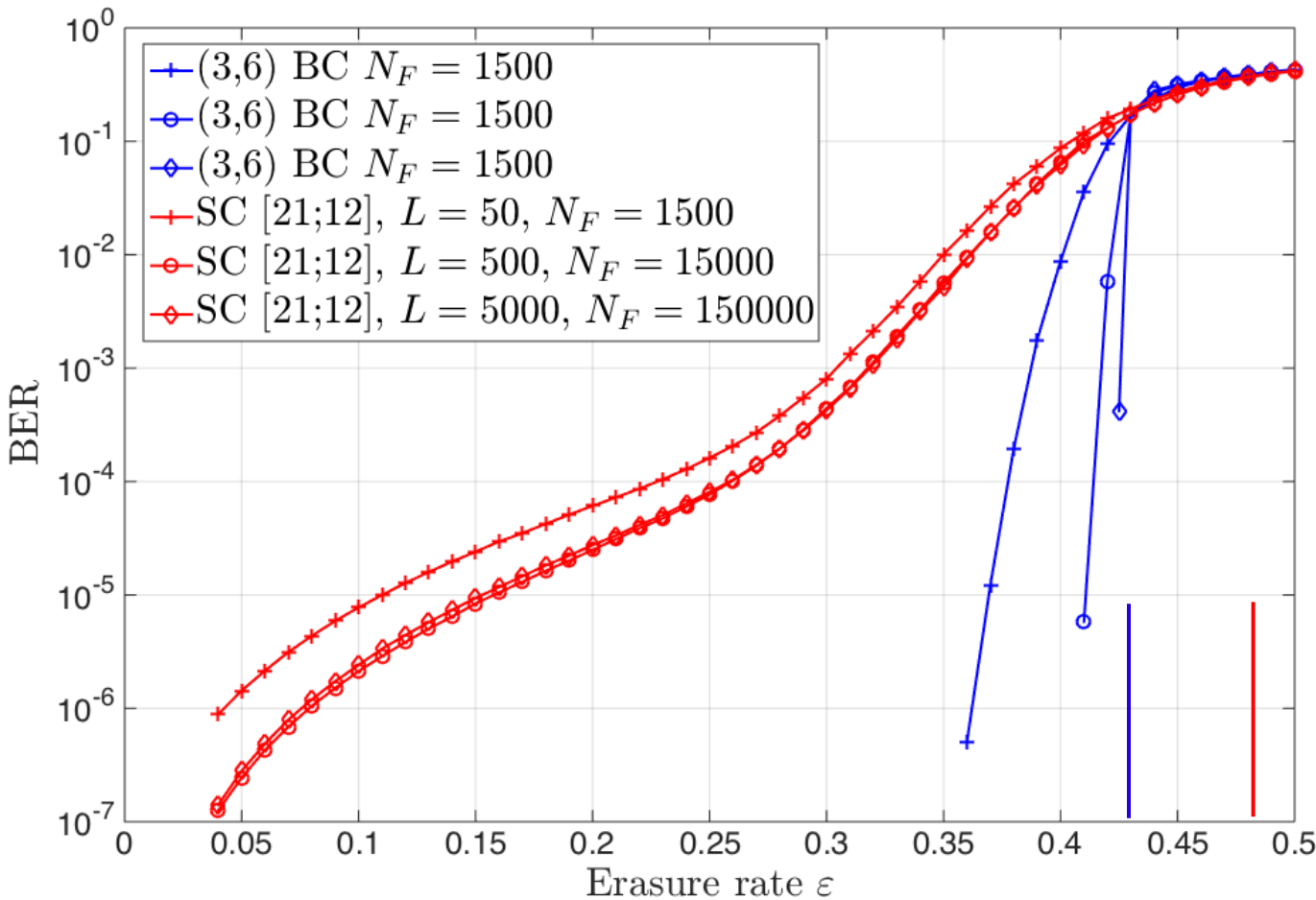
Consider LDPC-BCs and SC-LDPC codes with increasing frame length  $N_F$



We now consider increasing the frame length  $N_F = 2LM$  with fixed  $M = 15$  and increasing  $L$

# Flooding Decoding Scaling (3) – BEC

- Consider LDPC-BCs and SC-LDPC codes with increasing frame length  $N_F$

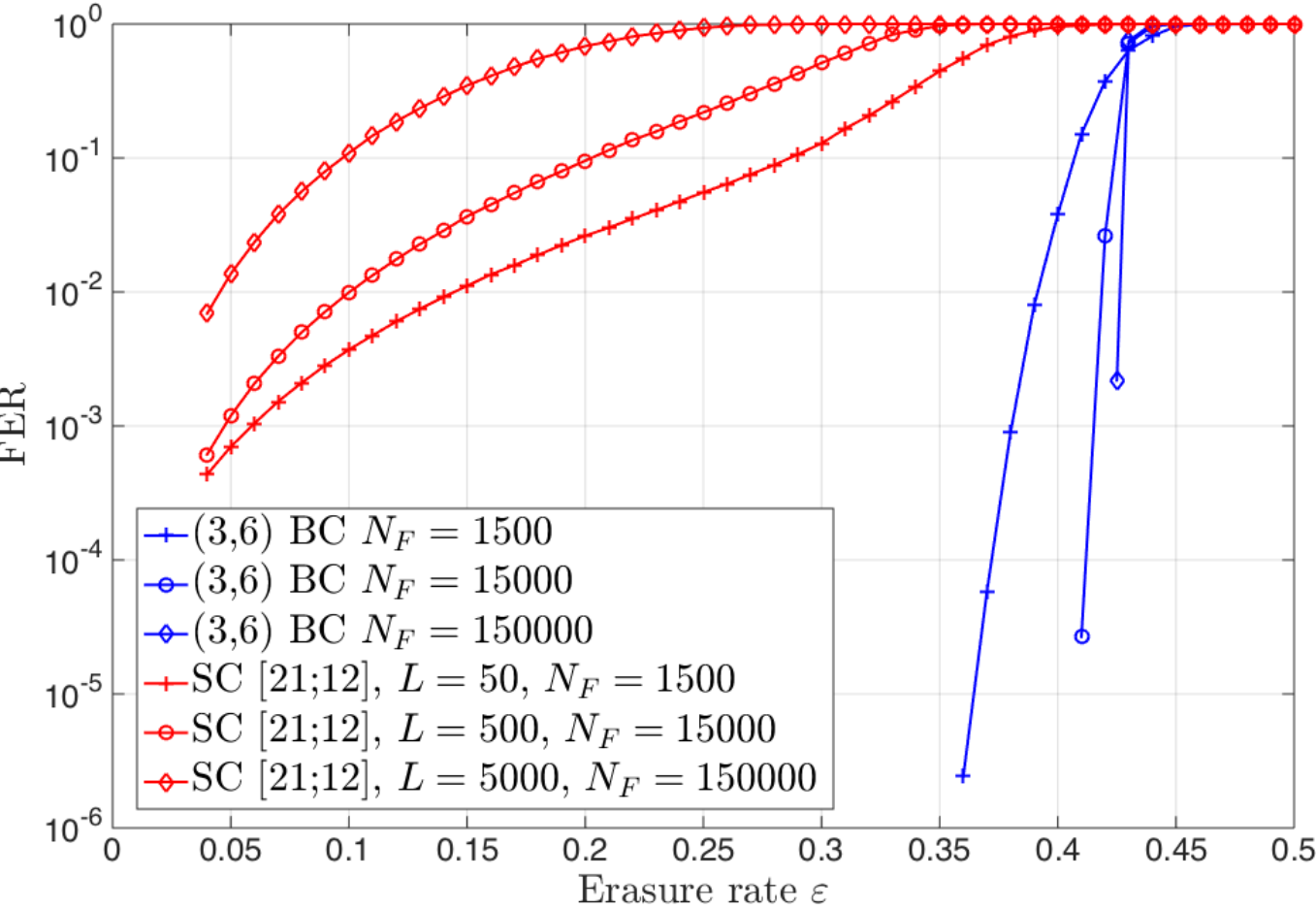


- We now consider increasing the frame length  $N_F = 2LM$  with fixed  $M = 15$  and increasing  $L$
- Note that the BER performance is poor for the SC-LDPC codes (small  $M$ ) and does not improve substantially with increasing  $L$

# Flooding Decoding Scaling (4) – BEC

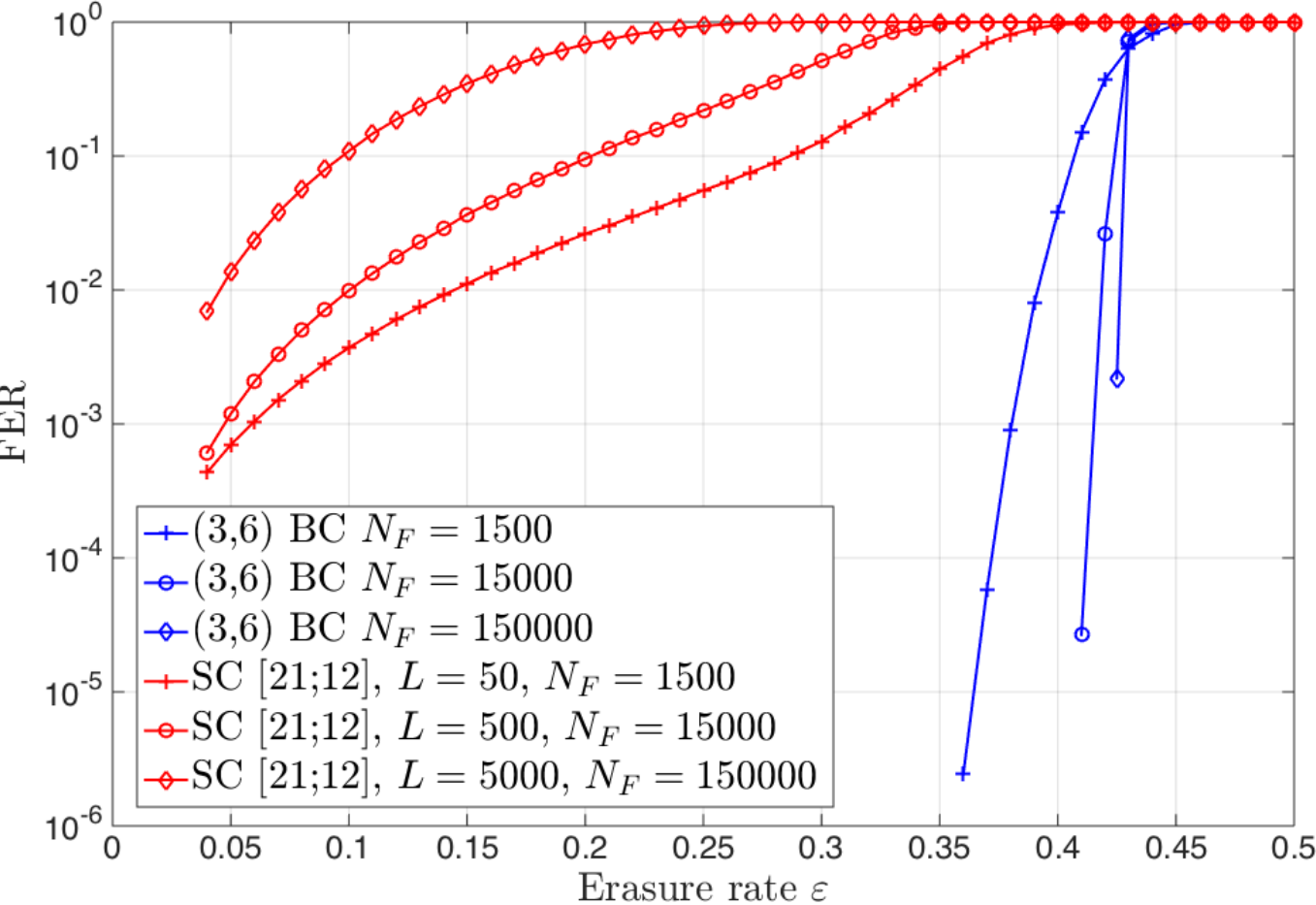


Consider LDPC-BCs and SC-LDPC codes with increasing frame length  $N_F$



# Flooding Decoding Scaling (4) – BEC

Consider LDPC-BCs and SC-LDPC codes with increasing frame length  $N_F$

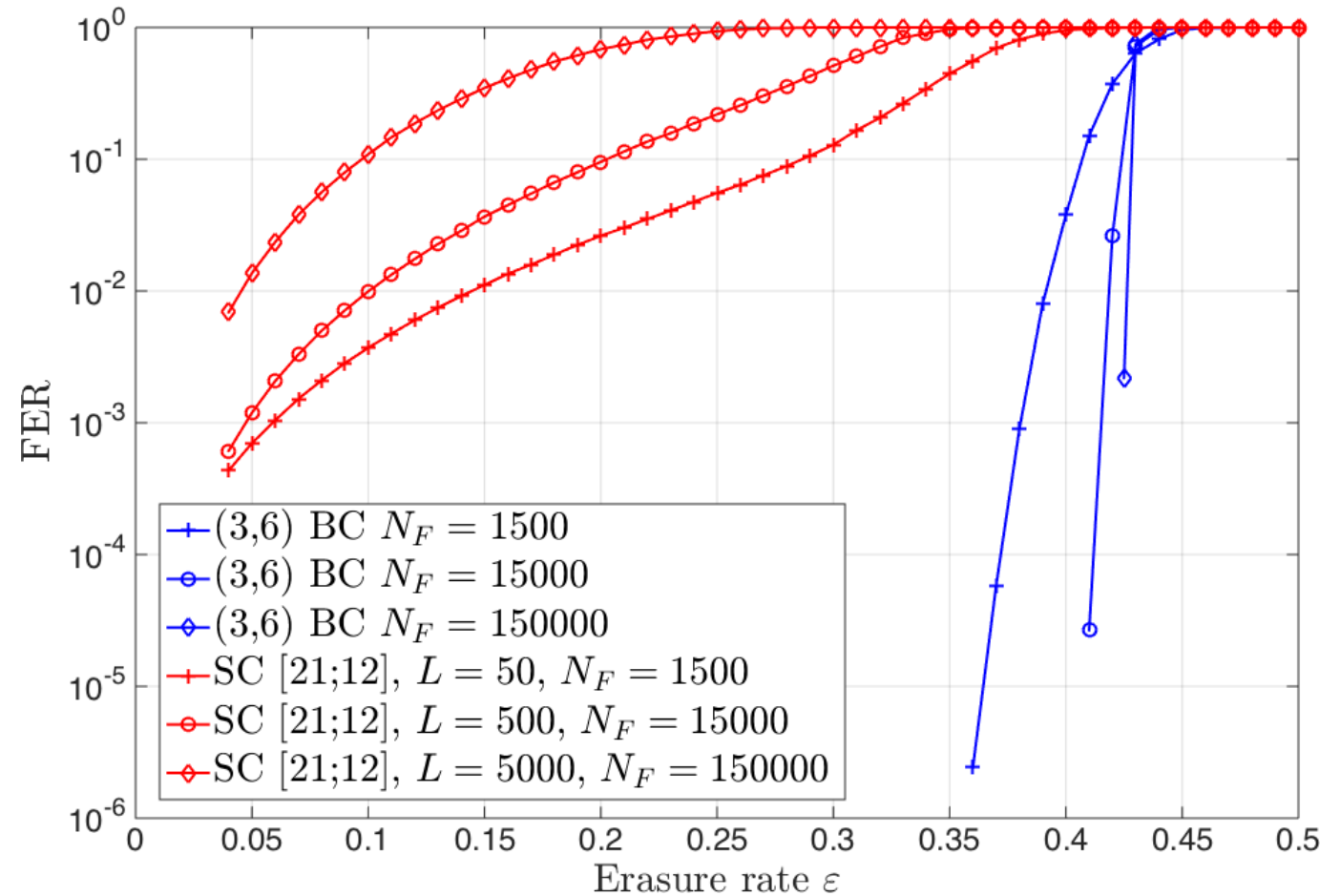


The FER performance is even more extreme since the FER depends on  $L$  ( $FER \xrightarrow{L \rightarrow \infty} 1$ )



# Flooding Decoding Scaling (4) – BEC

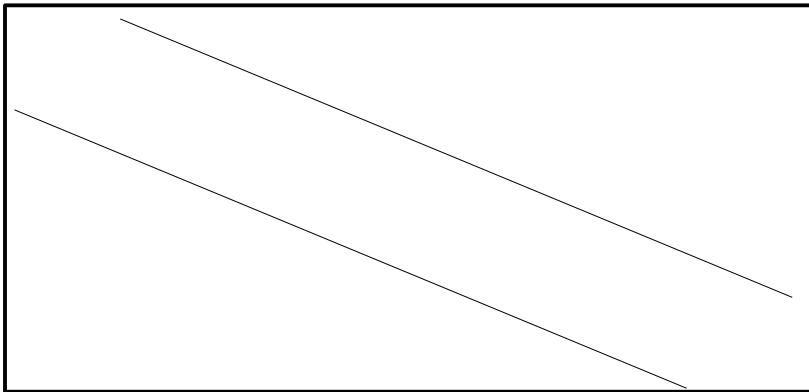
- Consider LDPC-BCs and SC-LDPC codes with increasing frame length  $N_F$



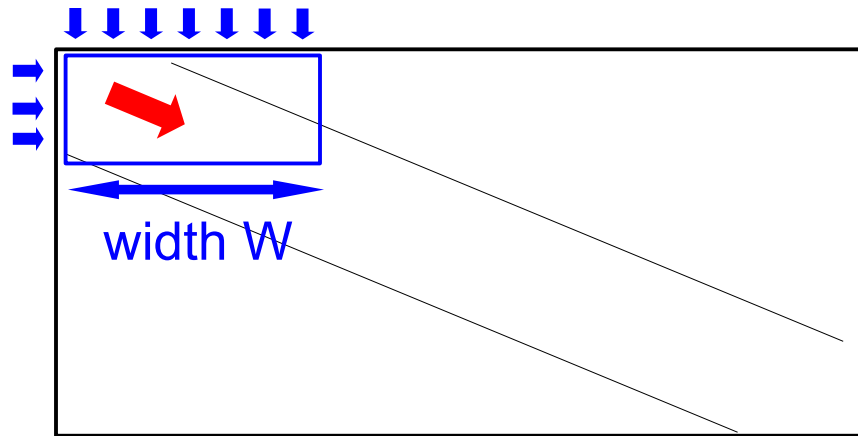
- The FER performance is even more extreme since the FER depends on  $L$  ( $\text{FER} \xrightarrow{L \rightarrow \infty} 1$ )

- Correspondingly, we note that the larger  $L$  codes perform worse (the order is reversed)

- The **highly localized (convolutional) structure** is well-suited for efficient decoding schedules that reduce memory and latency requirements.

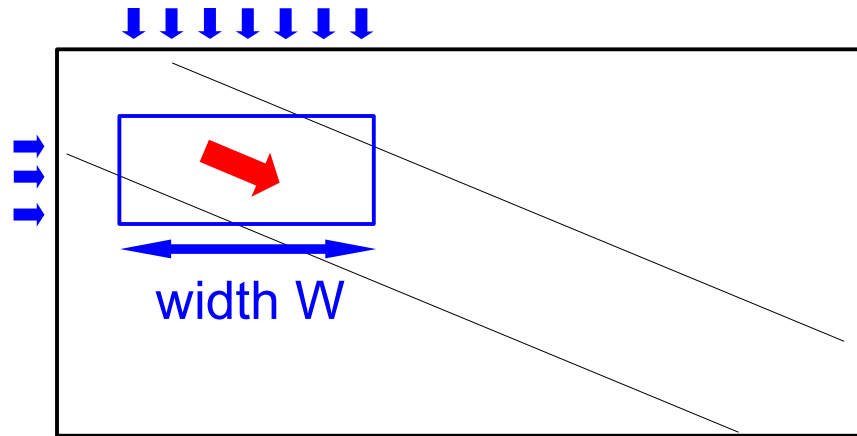


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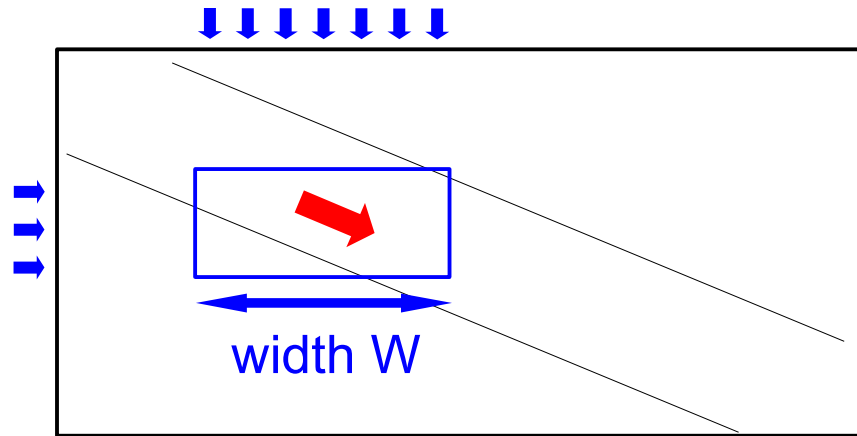
➔ **Sliding window decoding (WD)** updates nodes only within a localized window and then the window shifts across the graph [Lentmaier et al '10, Iyengar et al '12].

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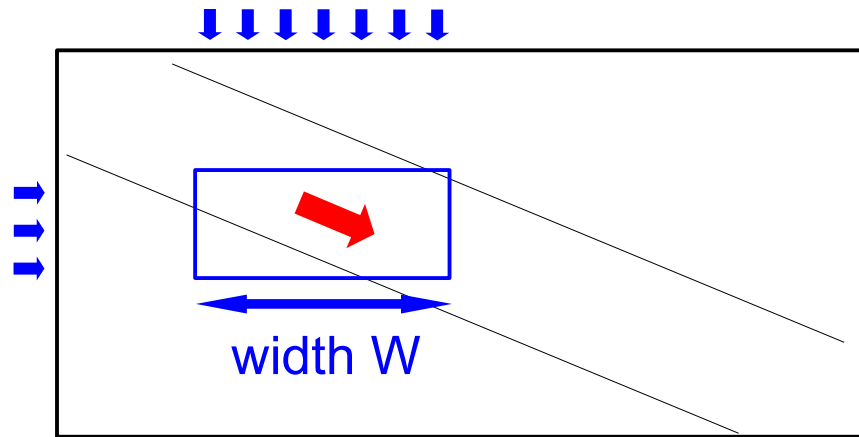
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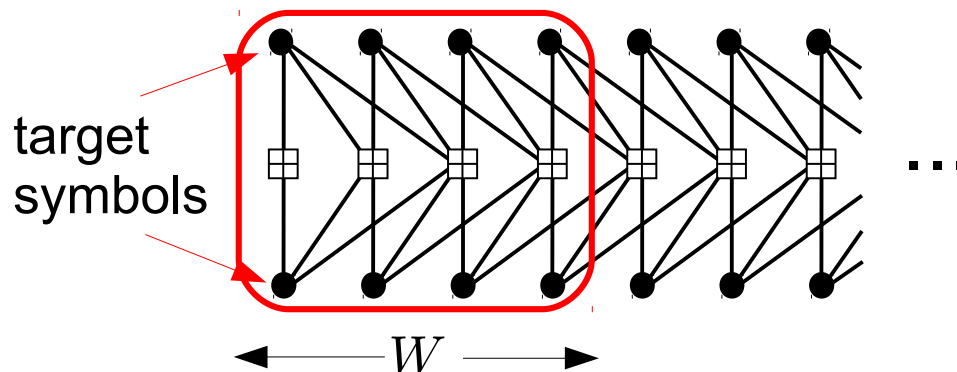


→ **Sliding window decoding (WD)** updates nodes only within a localized window and then the window shifts across the graph [Lentmaier et al '10, Iyengar et al '12].

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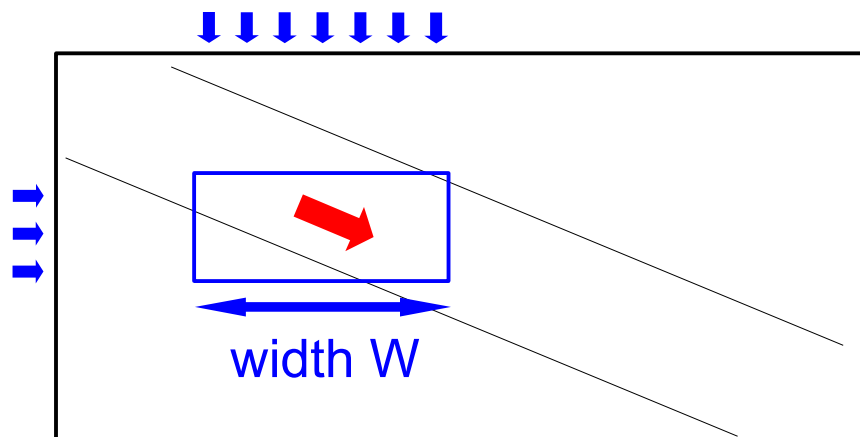


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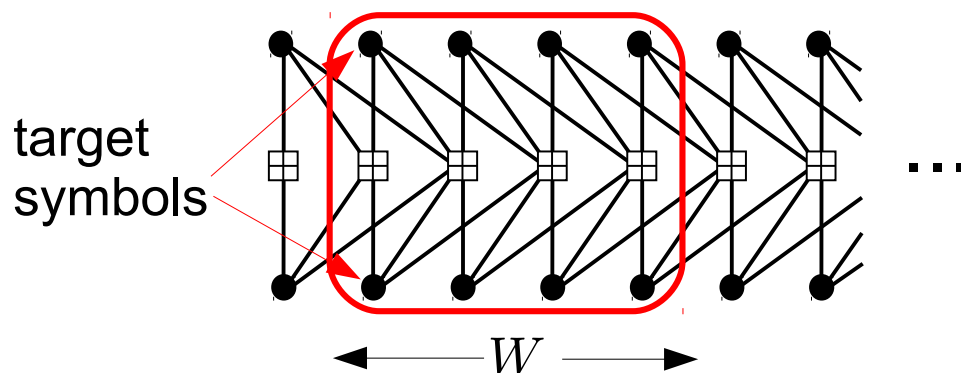


→ One block of  $cM$  **target symbols** is decoded in each window position

- The **highly localized (convolutional) structure** is well-suited for efficient decoding schedules that reduce memory and latency requirements.



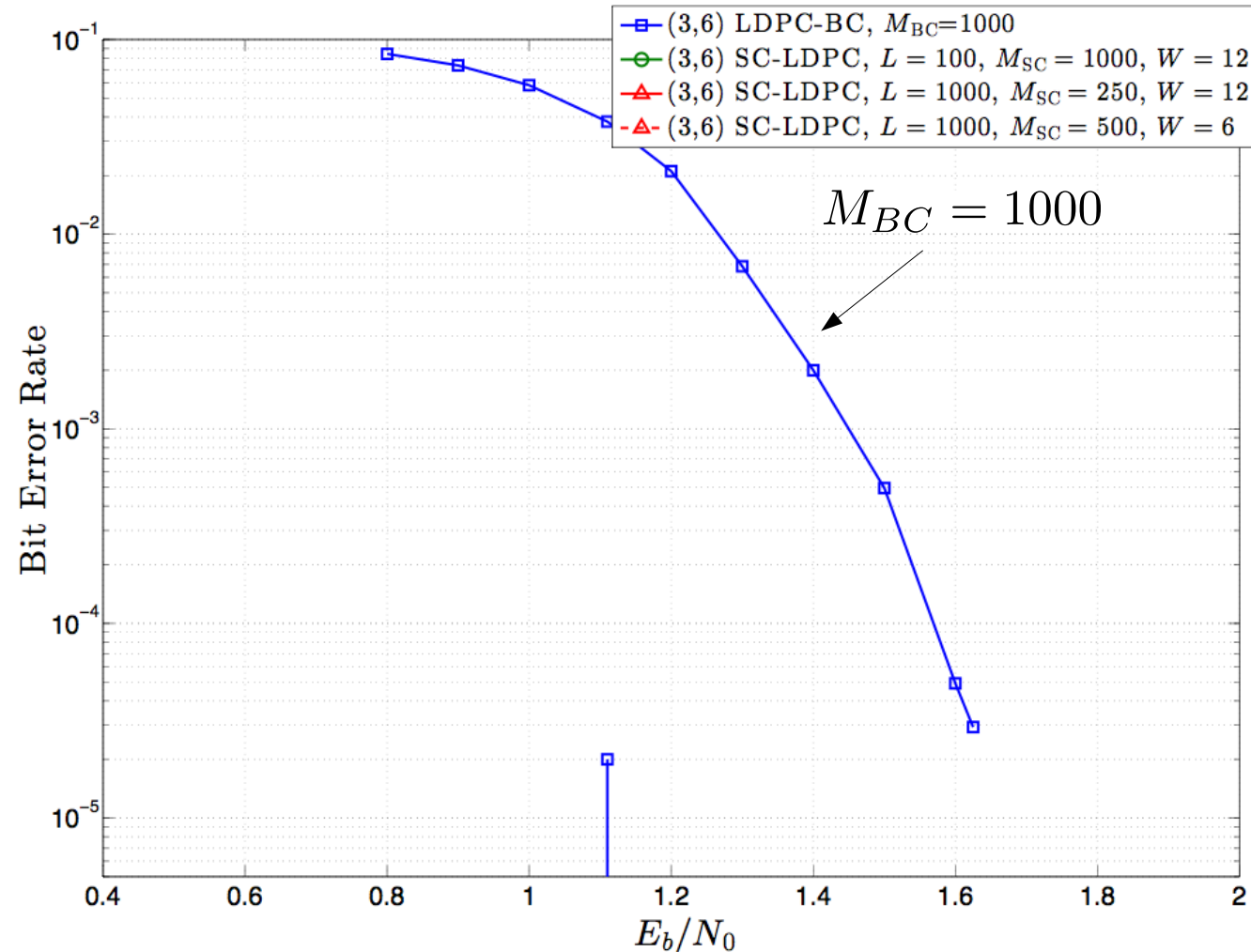
→ **Sliding window decoding (WD)** updates nodes only within a localized window and then the window shifts across the graph [Lentmaier et al '10, Iyengar et al '12].



→ One block of  $cM$  **target symbols** is decoded in each window position

→ The window then shifts to the right

# Window Decoding Performance



## Latencies:

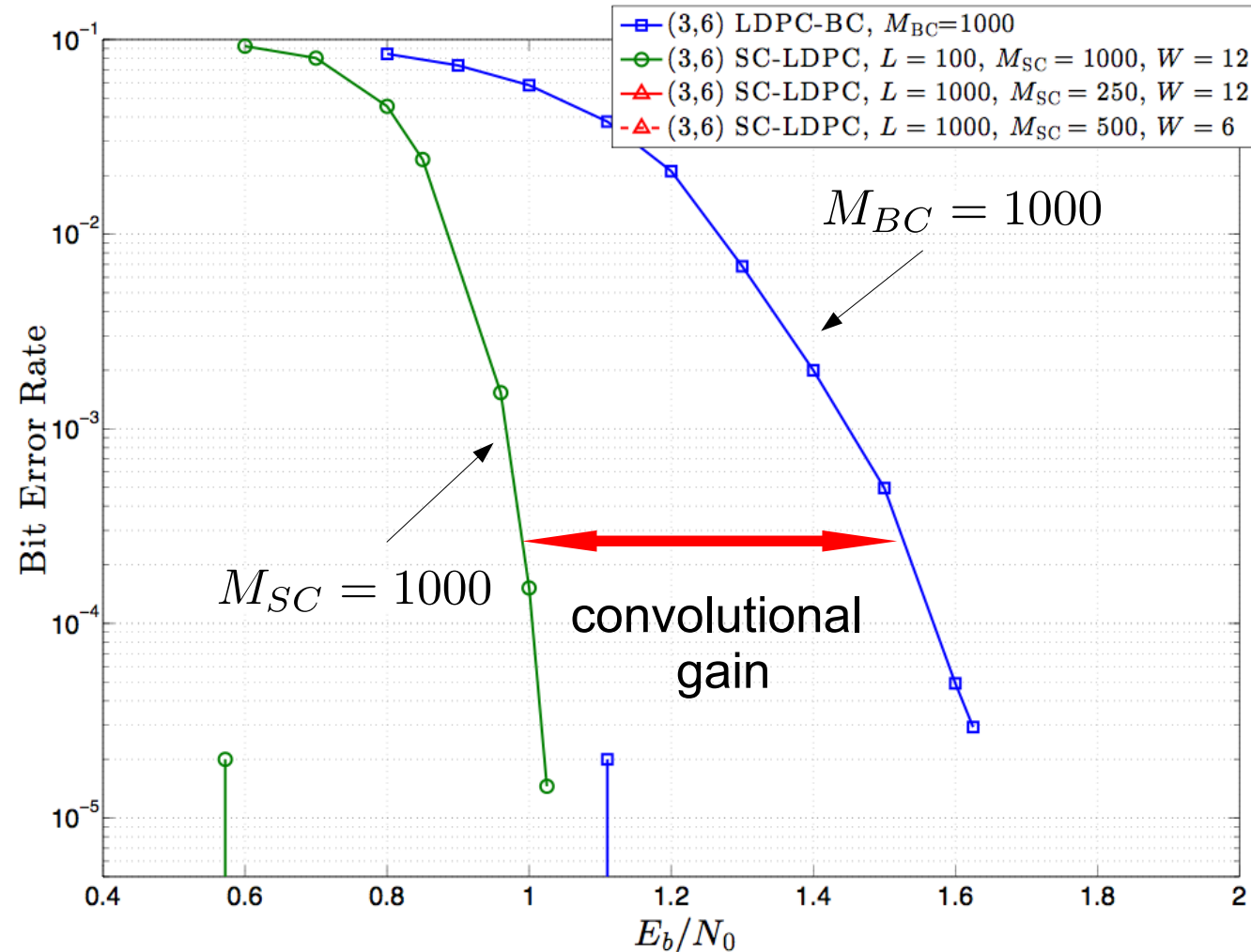
LDPC:  $6M_{BC}$

SC-LDPC:  $2M_{SC}W$

[LPF11] M. Lentmaier, M. M. Prenda, and G. Fettweis, "Efficient Message Passing Scheduling for Terminated LDPC Convolutional Codes", *Proc. IEEE ISIT*, St. Petersburg, Russia, July 2011.



# Window Decoding Performance



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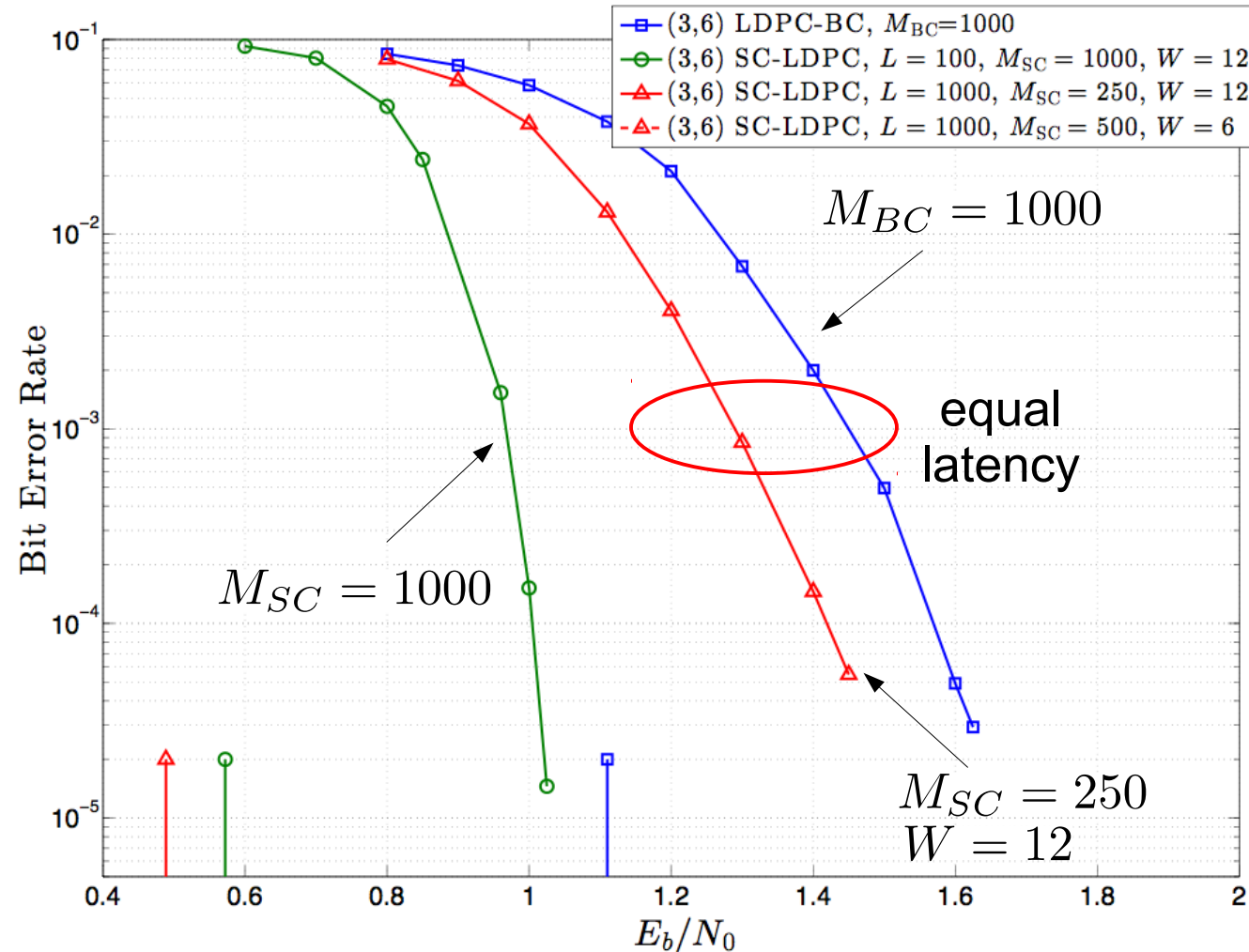
LDPC:  $6M_{BC}$

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➔ For **equal lifting factors**, SC-LDPC codes display a large **convolutional gain** at the cost of increased latency.

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# Window Decoding Performance



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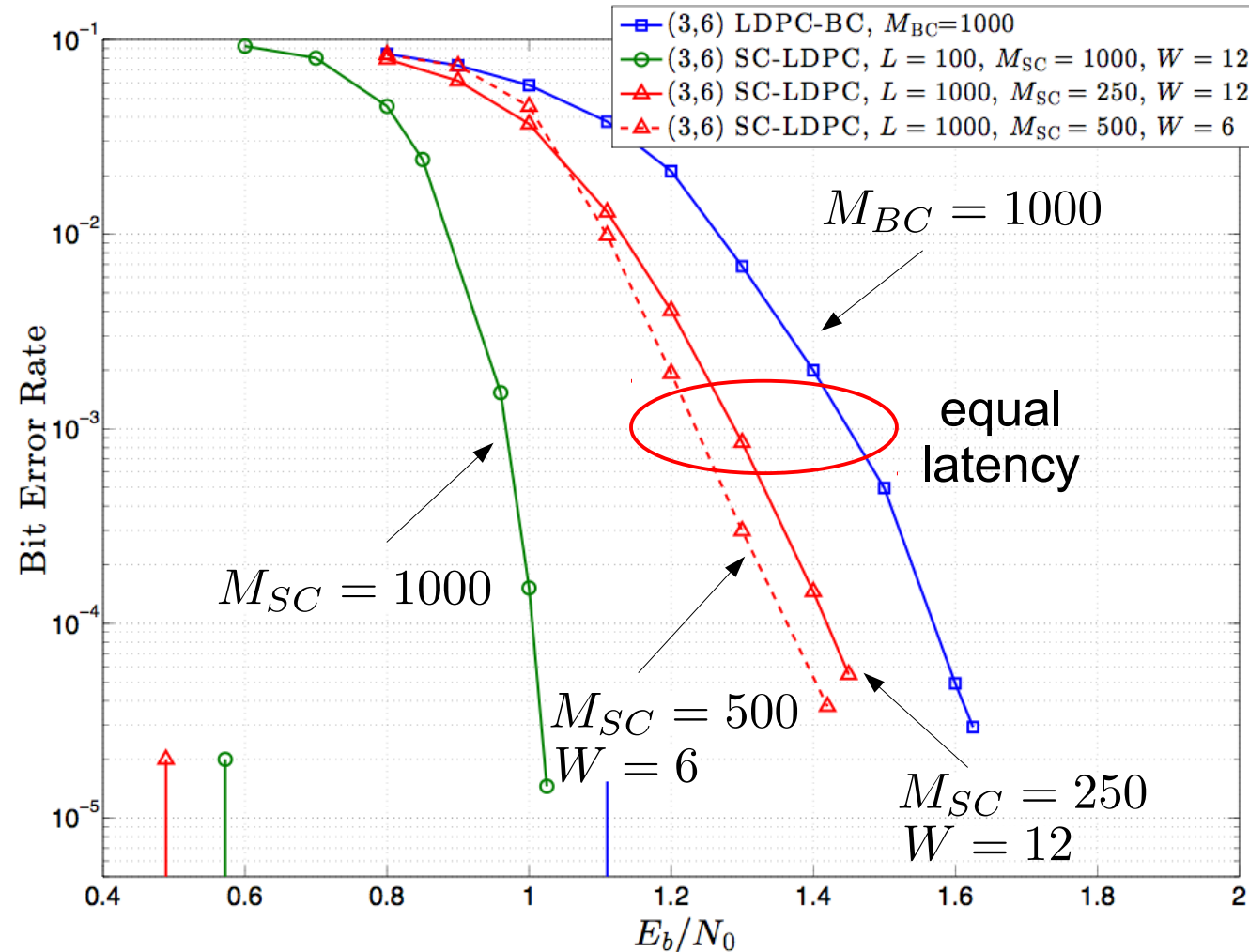
SC-LDPC:  $2M_{SC}W$

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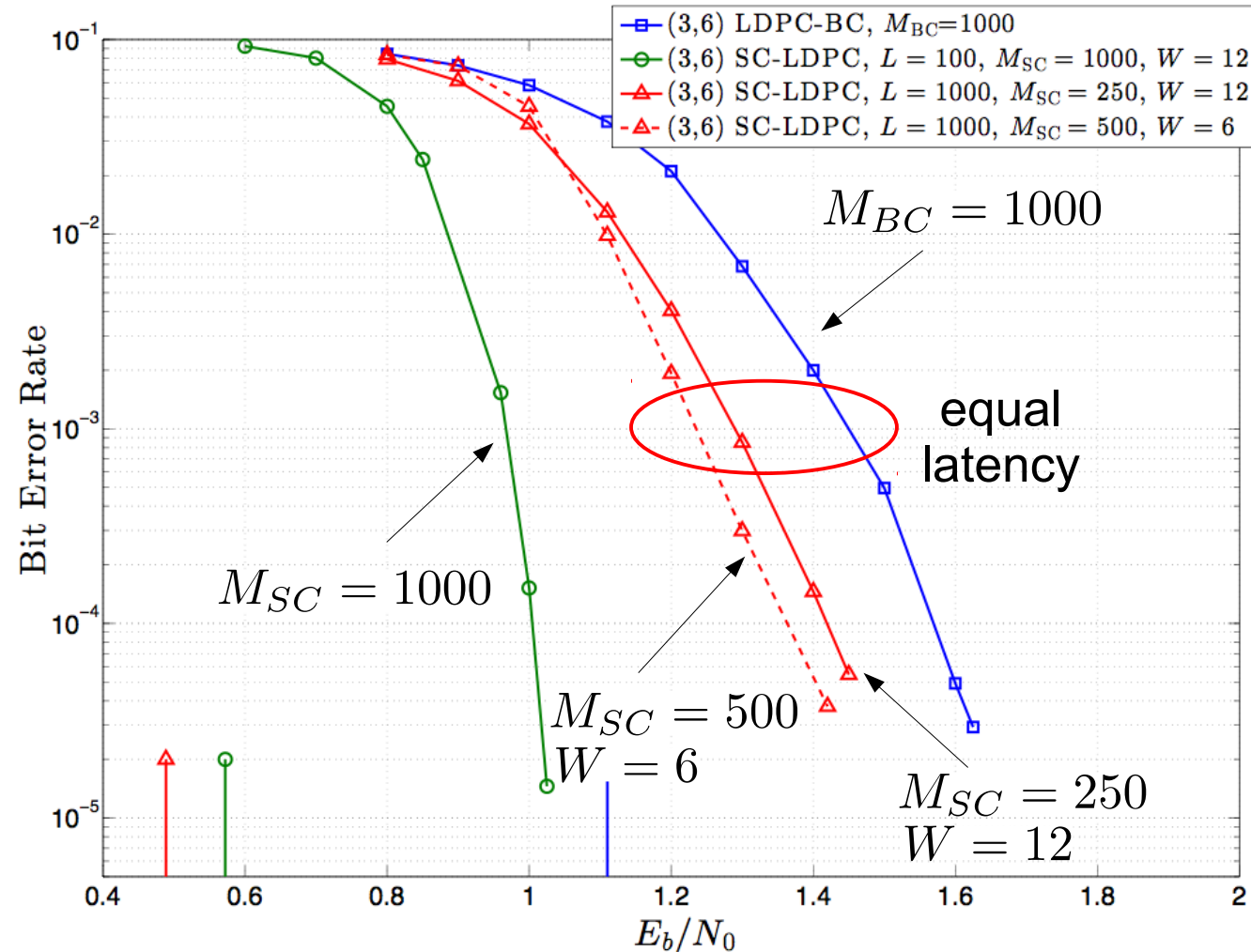
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# Window Decoding Performance



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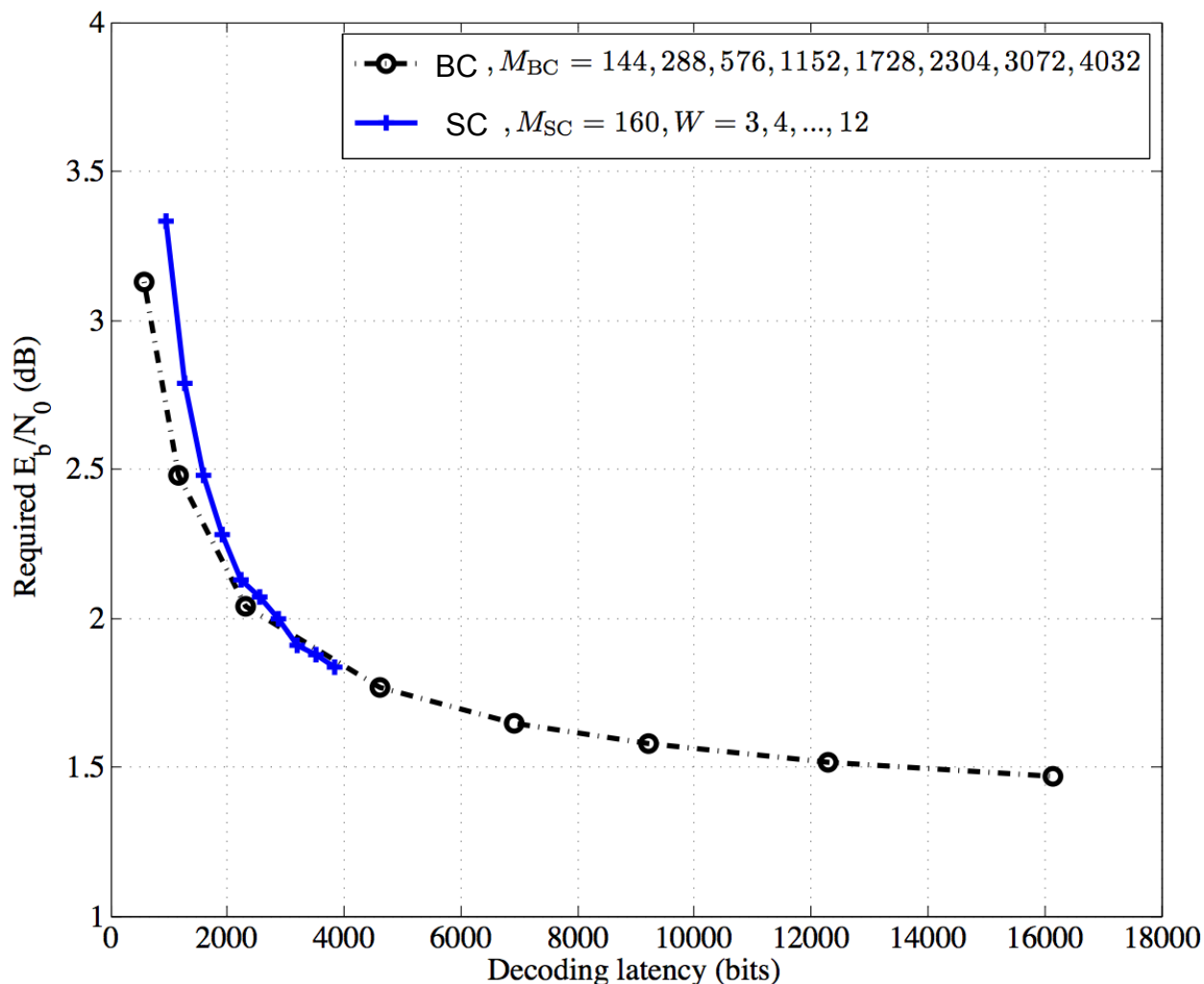
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- ➔ For **equal lifting factors**, SC-LDPC codes display a large **convolutional gain** at the cost of increased latency.
- ➔ For **equal latency**, SC-LDPC codes still display a significant **performance gain**.
- ➔ Trade-off in  $M$  vs  $W$

[LPF11] M. Lentmaier, M. M. Prenda, and G. Fettweis, "Efficient Message Passing Scheduling for Terminated LDPC Convolutional Codes", *Proc. IEEE ISIT*, St. Petersburg, Russia, July 2011.

# Equal Latency Comparison for (3,6)-Regular LDPC Codes

- Required  $E_b/N_0$  to achieve a BER of  $10^{-5}$  as a function of latency:



## Latencies:

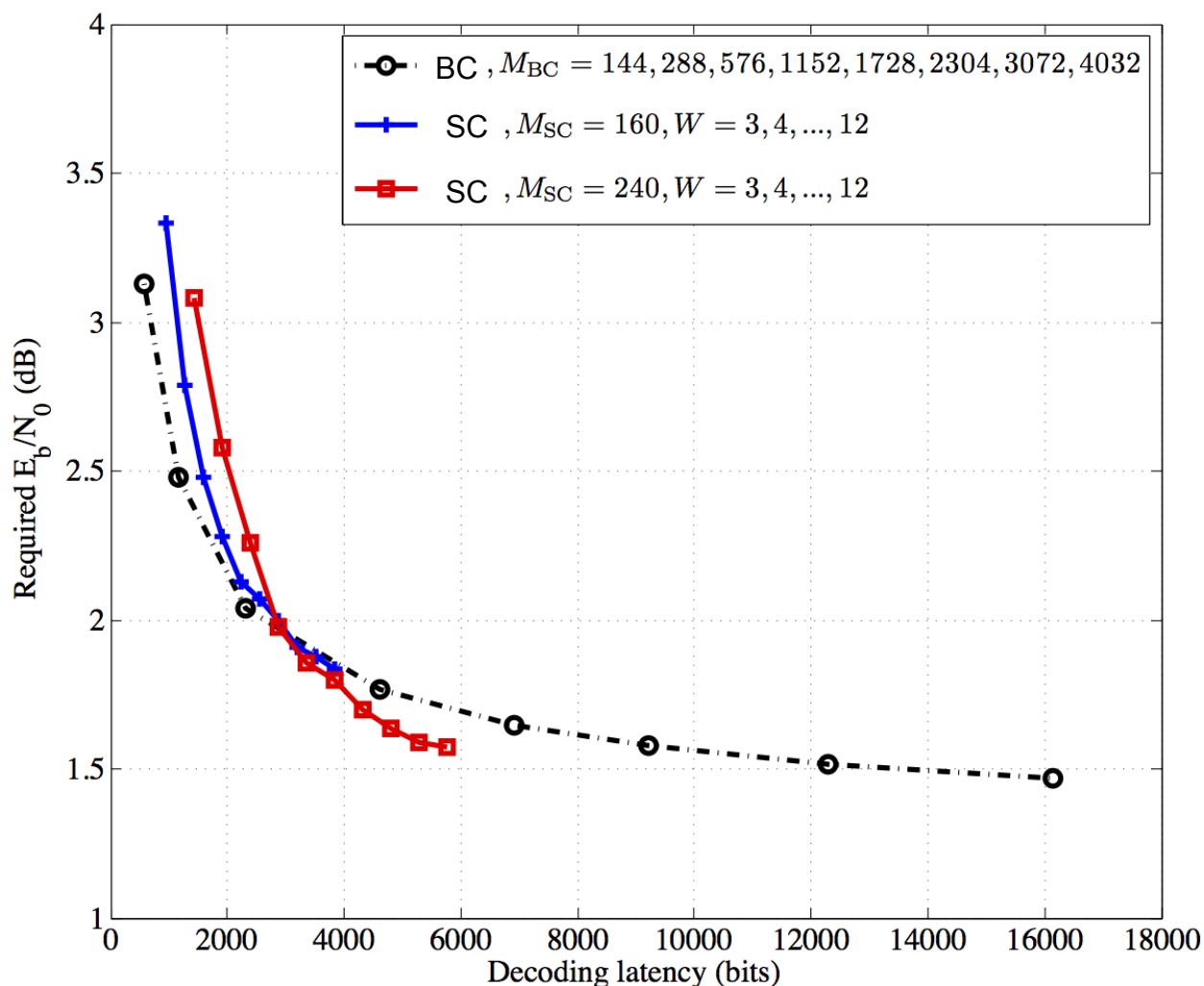
LDPC:  $4M_{BC}$

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- decreases as  $W$  (and thus the latency) increases.
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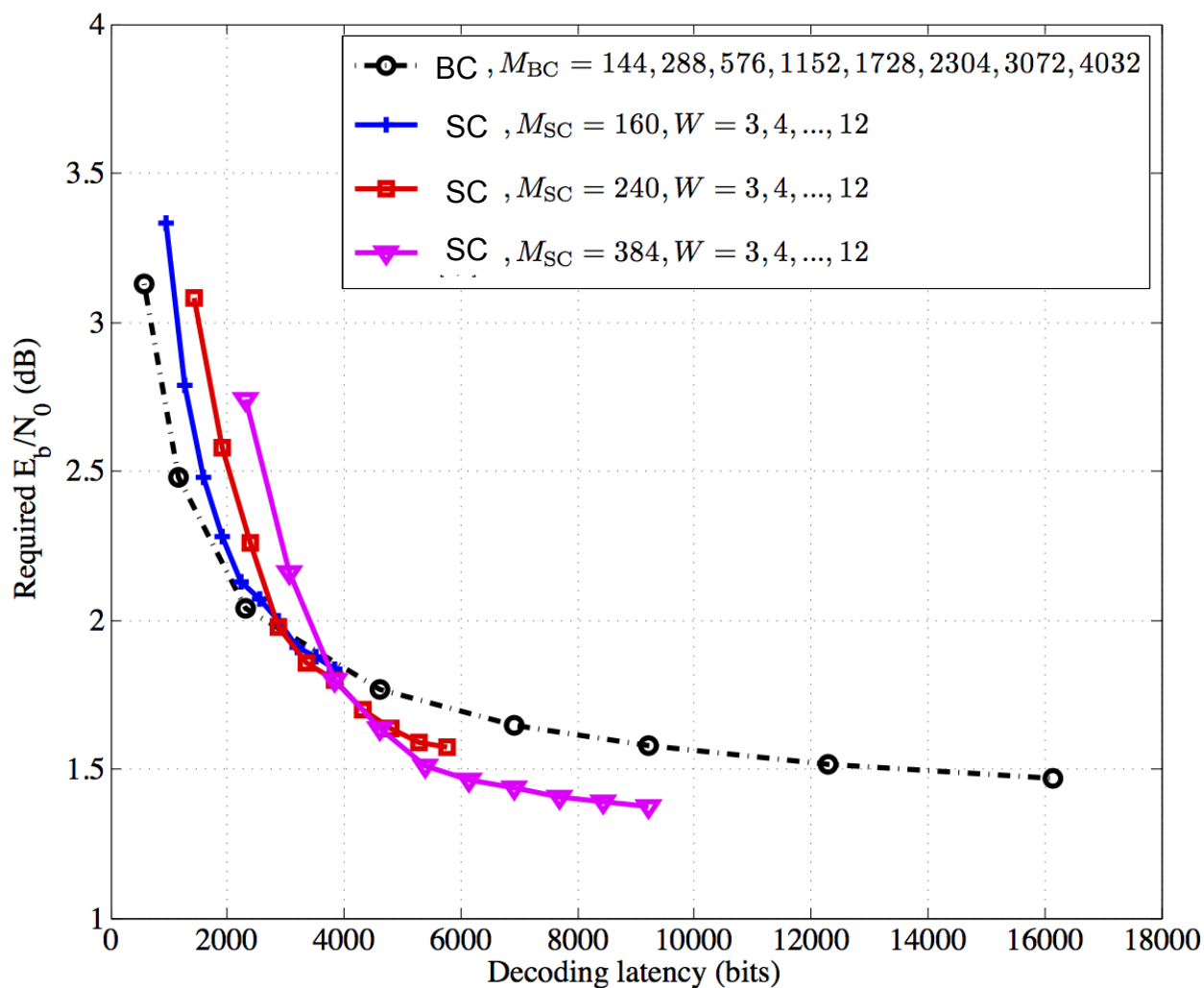
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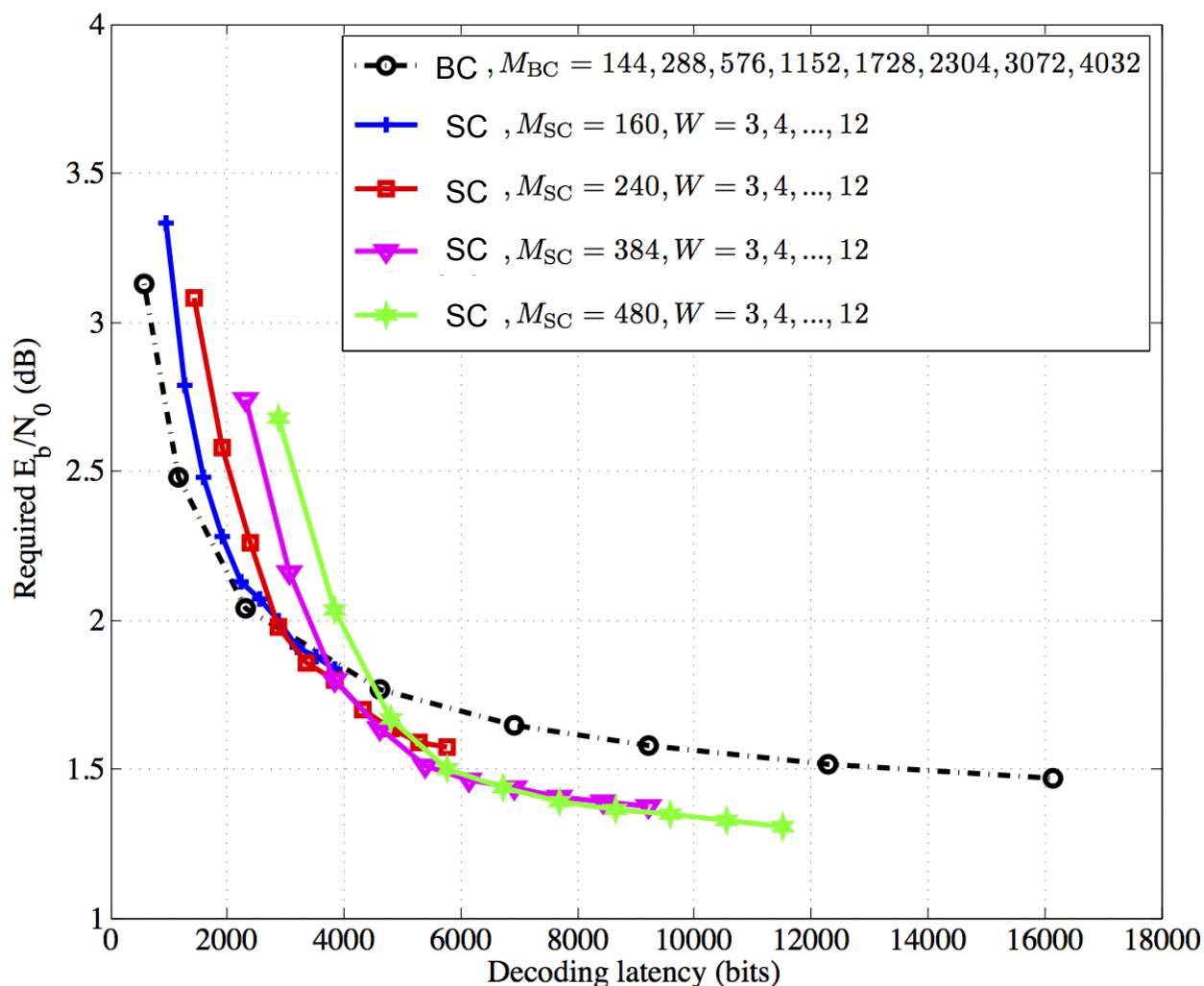
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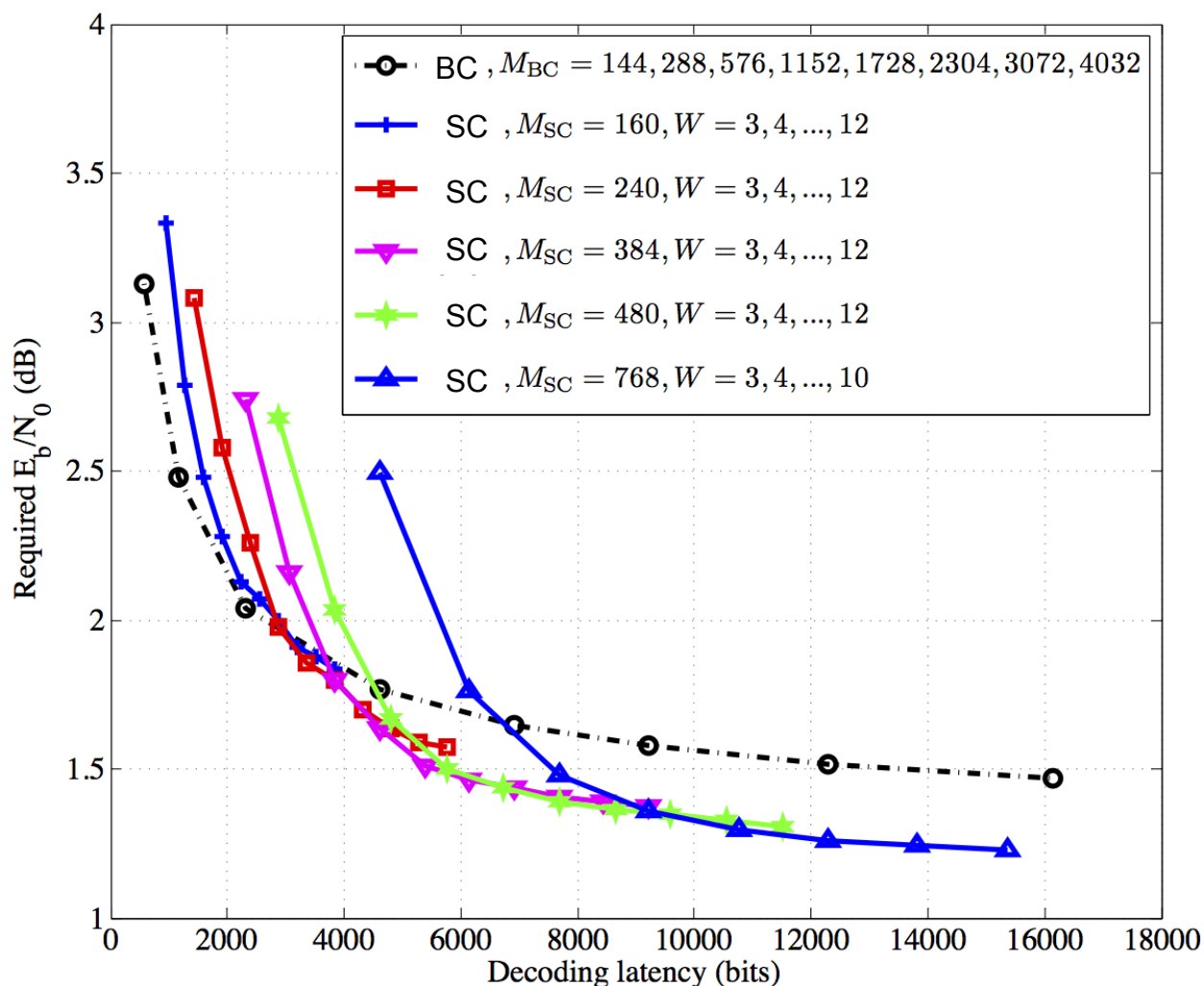
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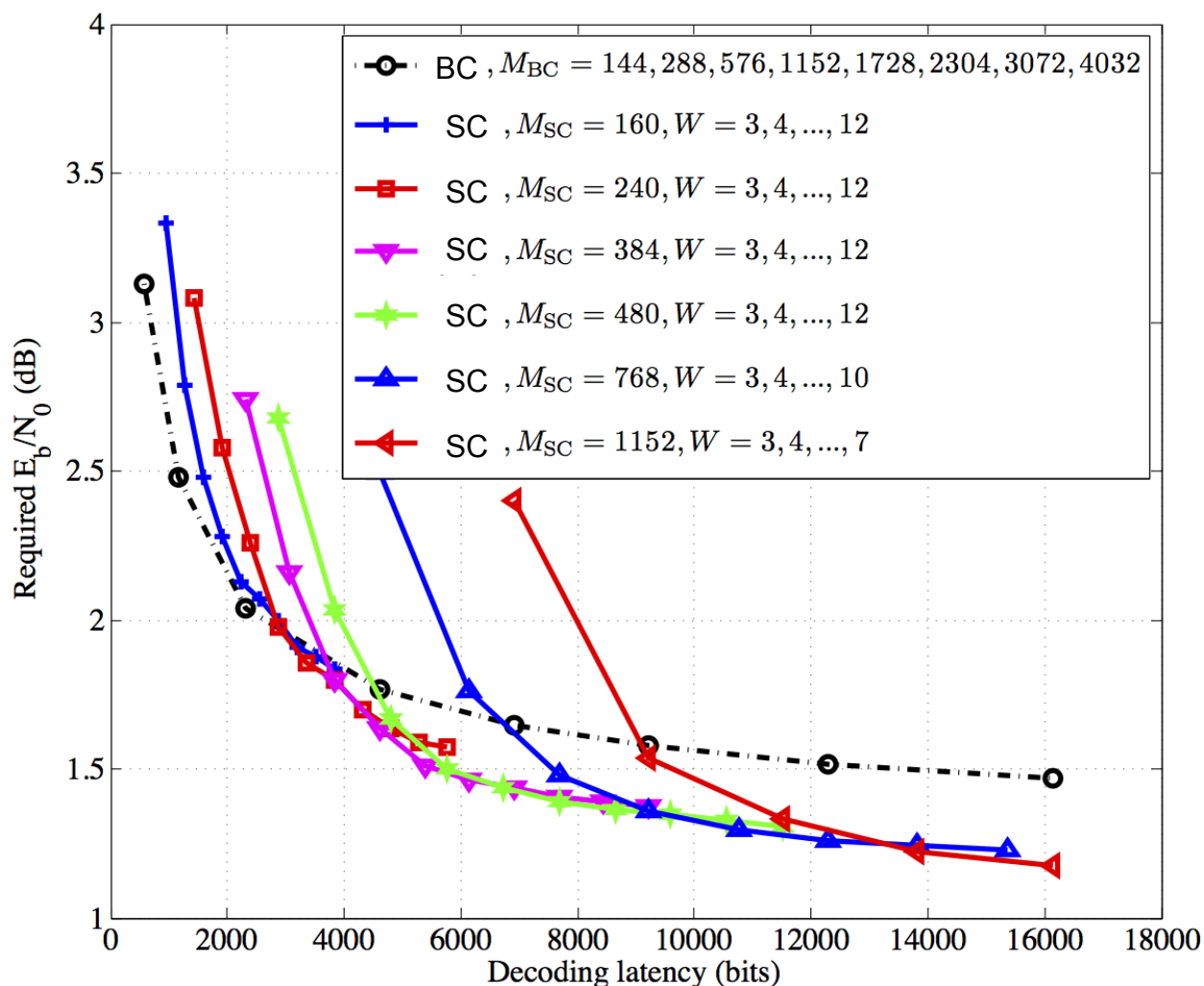


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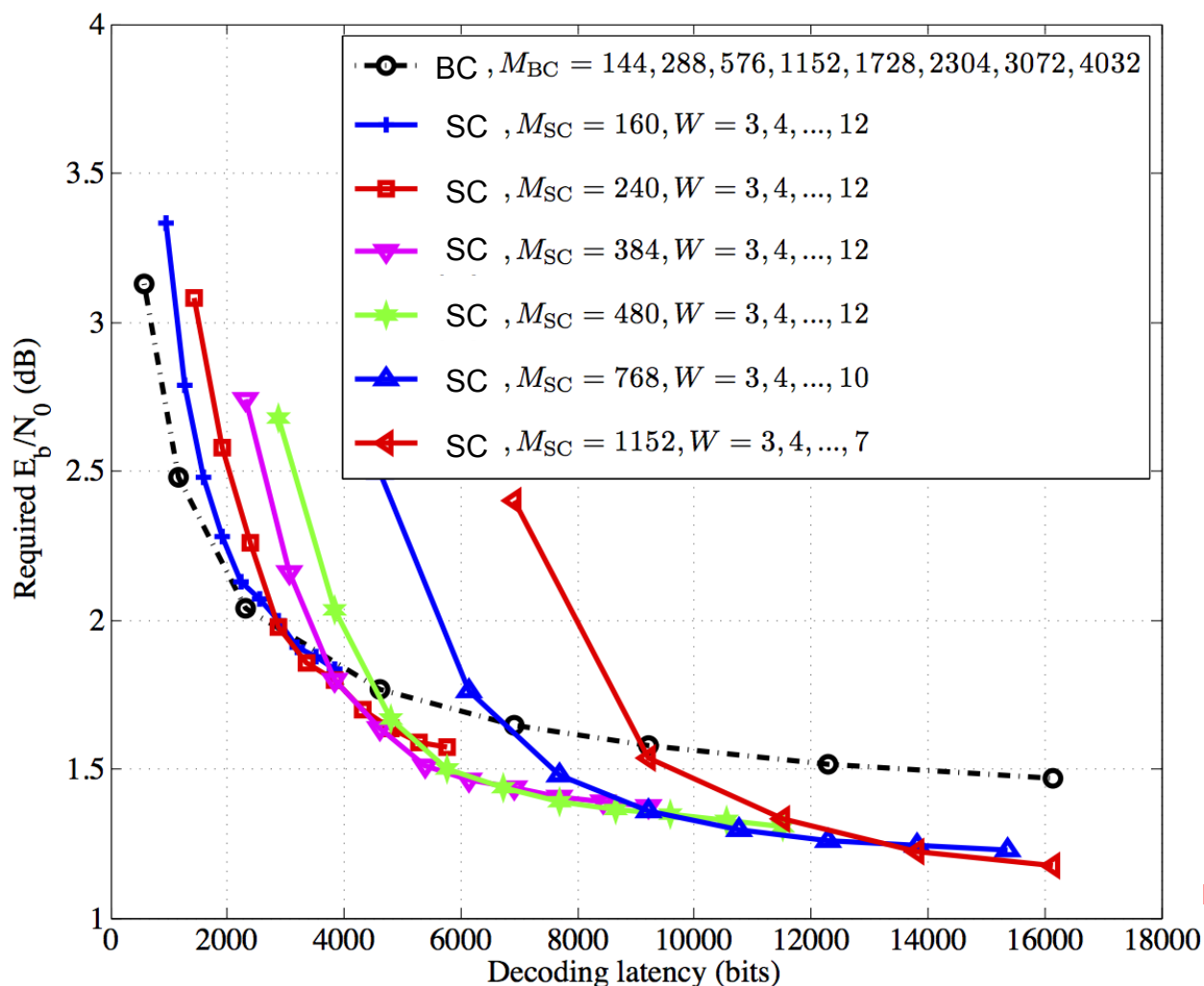
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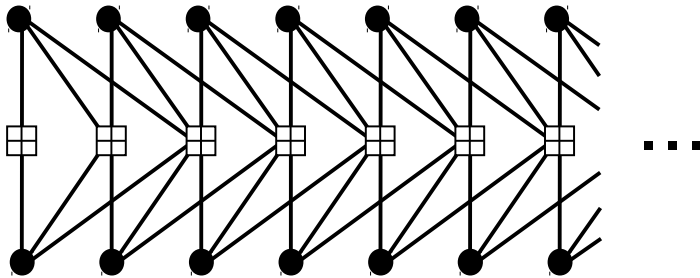
## Latencies:

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- decreases as  $W$  (and thus the latency) increases.
- does not decrease significantly beyond a certain  $W$  ( $W \approx 10$ ).
- When choosing parameters:
  - large  $M_{SC}$  improves code performance.
  - large  $W$  improves decoder performance.

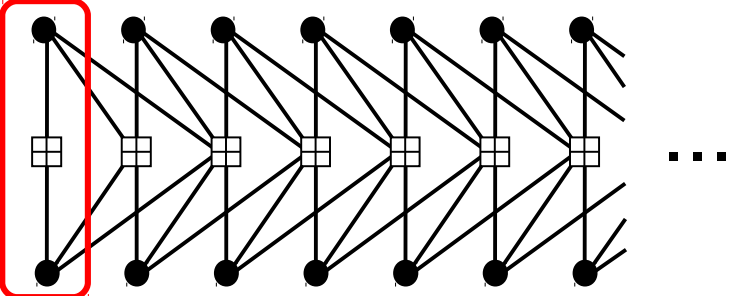
# Protograph design



# Protograph design

block size

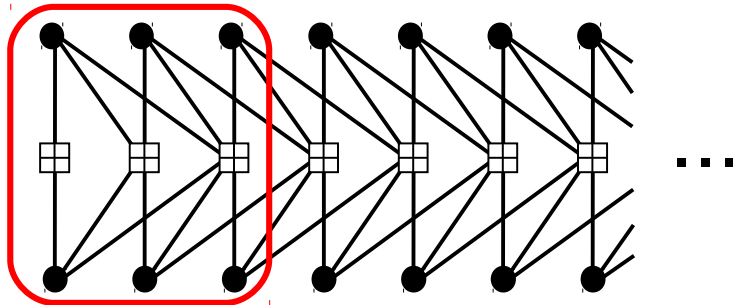
$$N_B = cM = 2M$$



# Protograph design

block size

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coupling  
width  $w=2$

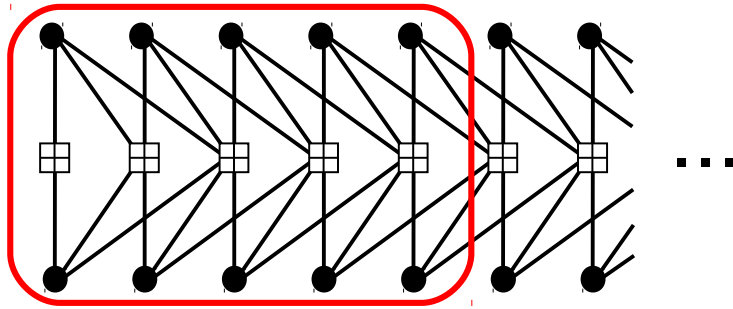


constraint length  
 $\nu = cM(w + 1) = 6M$

# Protograph design

block size

$$N_B = cM = 2M$$



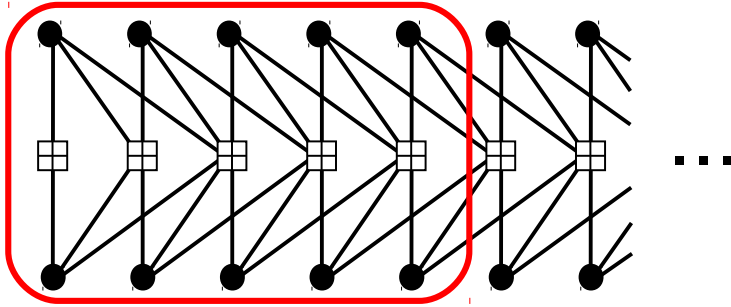
coupling width  $w=2$   $\longleftrightarrow$  constraint length  $\nu = cM(w+1) = 6M$



window span  $W = 5 \geq w + 1$   $\longleftrightarrow$  window size  $S = cMW = 10M$

block size

$$N_B = cM = 2M$$



- The **strength** of a convolutional code grows with **constraint length**  $\nu = cM(w + 1)$

coupling width  $w=2$   $\longleftrightarrow$  constraint length  $\nu = cM(w + 1) = 6M$

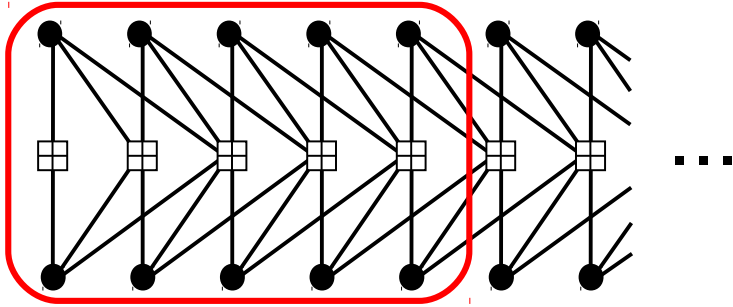
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# Protograph design

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window size

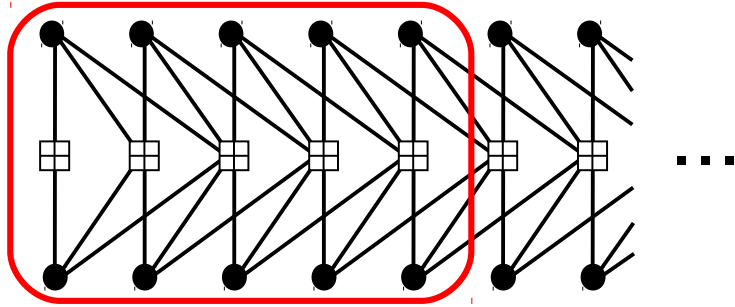
$$S = cMW = 10M$$

- The **strength** of a convolutional code grows with **constraint length**  $\nu = cM(w + 1)$
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$$S = cMW \geq cM(w + 1)$$

block size

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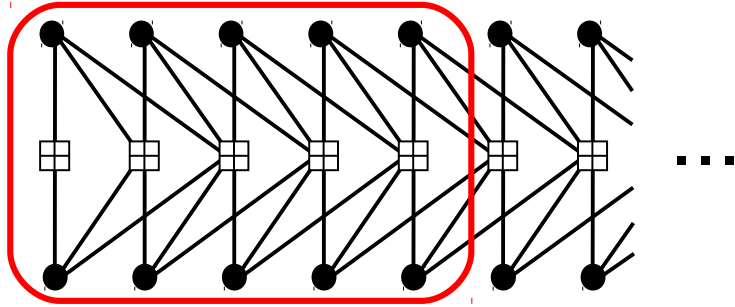
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- ➔ For fixed  $\nu$  or  $S$ , we obtain many small blocks or few large blocks by varying  $w$  and  $M$ .

block size

$$N_B = cM = 2M$$



coupling width  $w=2$   $\longleftrightarrow$  constraint length  $\nu = cM(w+1) = 6M$

window span  $W = 5 \geq w + 1$   $\longleftrightarrow$  window size  $S = cMW = 10M$

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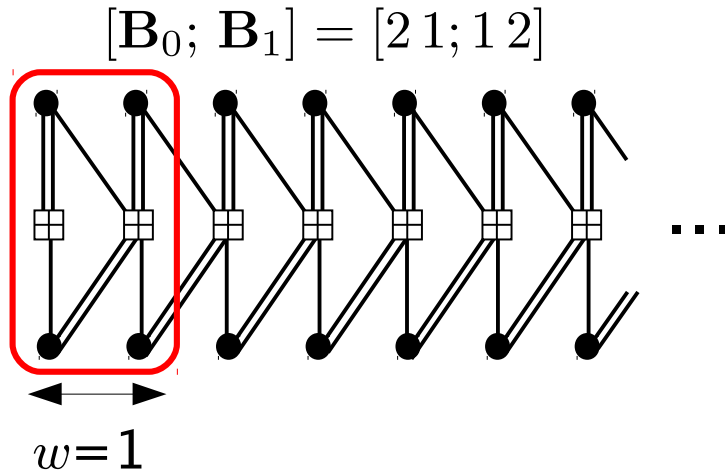
- The **latency** of the decoder is given by the **window size**  $S = cMW \geq cM(w+1)$

➔ For fixed  $\nu$  or  $S$ , we obtain many small blocks or few large blocks by varying  $w$  and  $M$ .

- Density evolution does not tell us how to choose these parameters to **optimize finite-length performance**

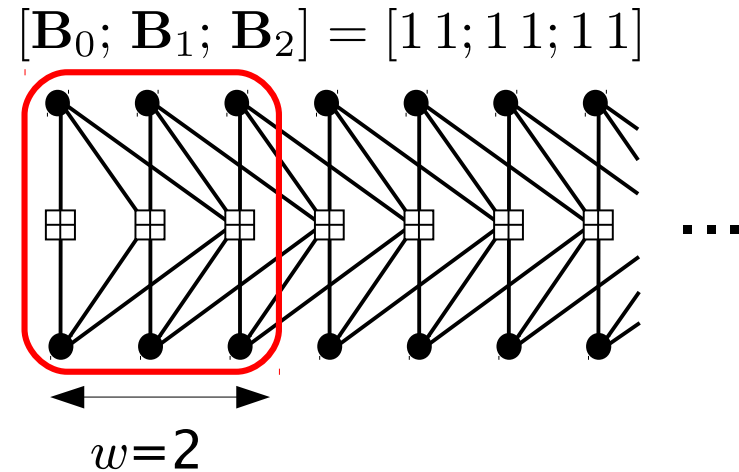
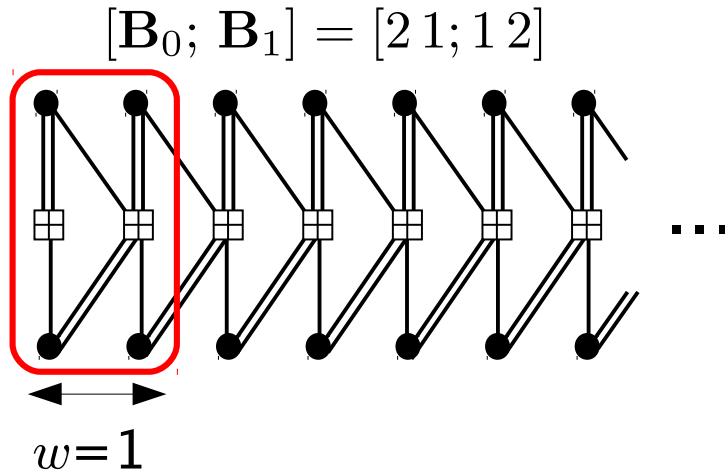
# Examples: Protograph design

- We start from  $\mathbf{B} = [3 \ 3]$  and spread the edges such that  $\sum_{i=0}^w \mathbf{B}_i = \mathbf{B}$



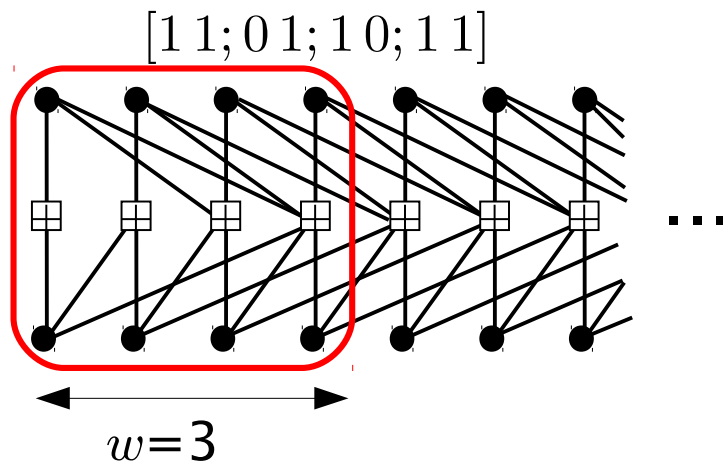
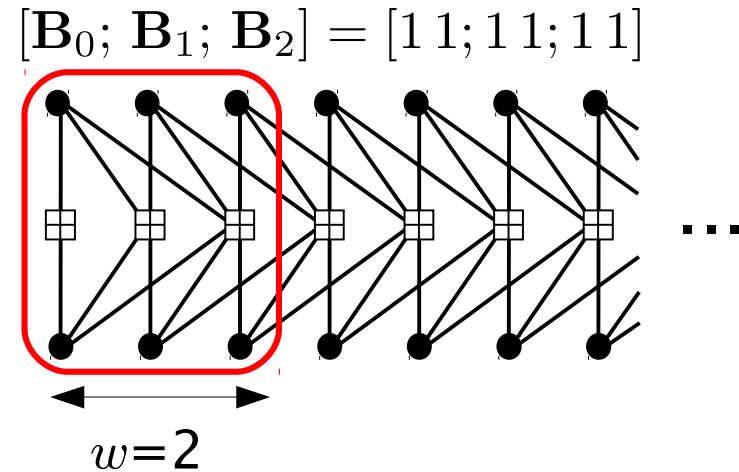
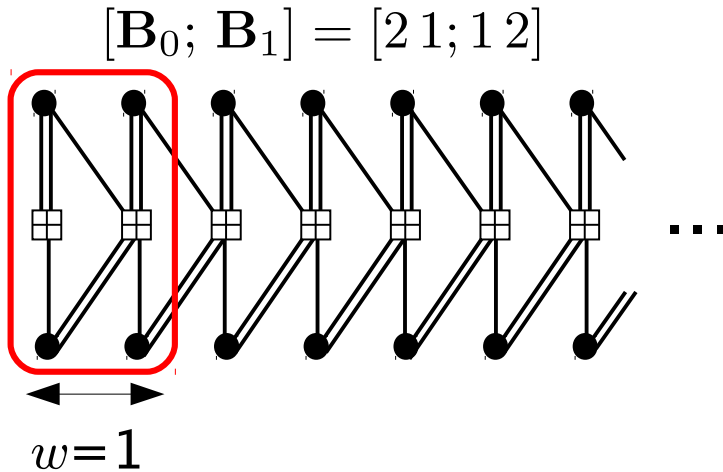
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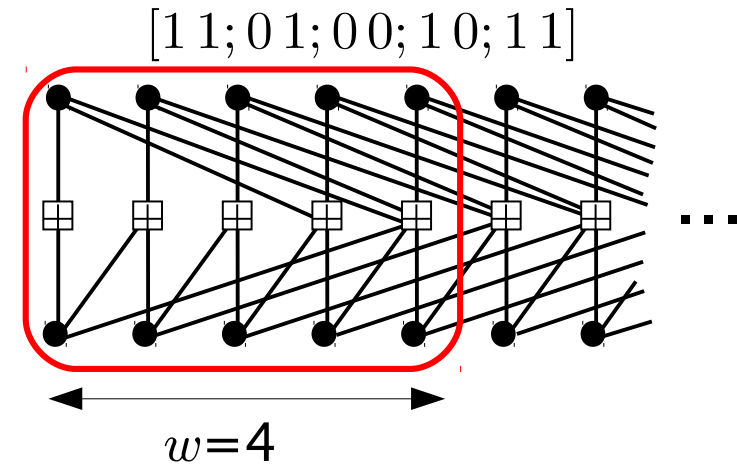
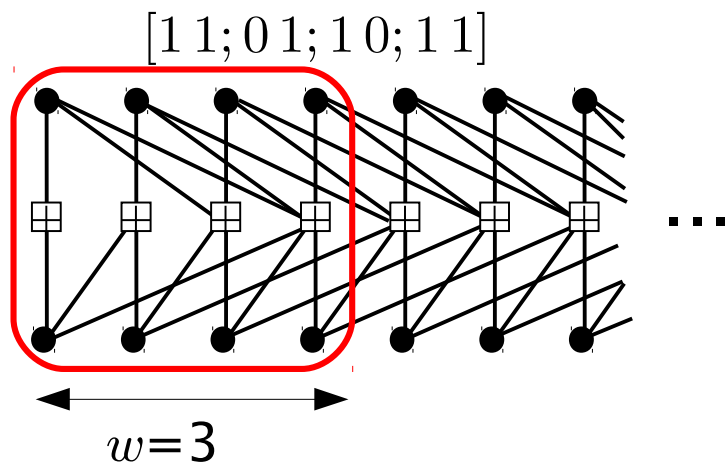
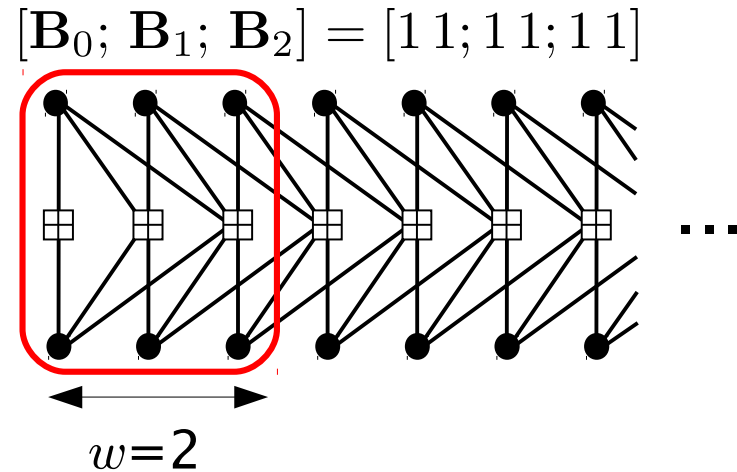
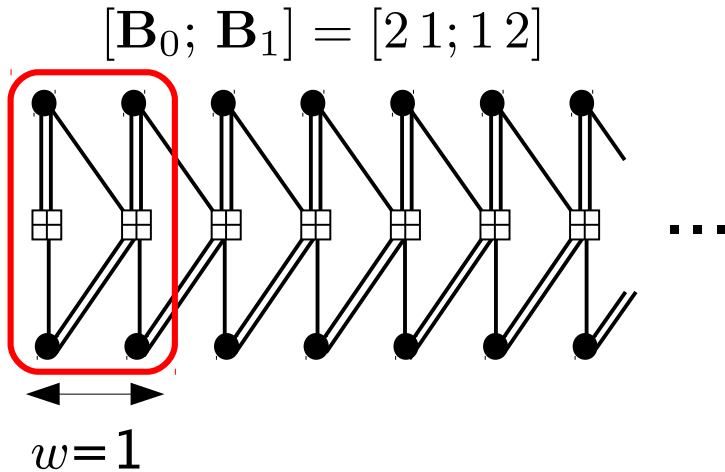
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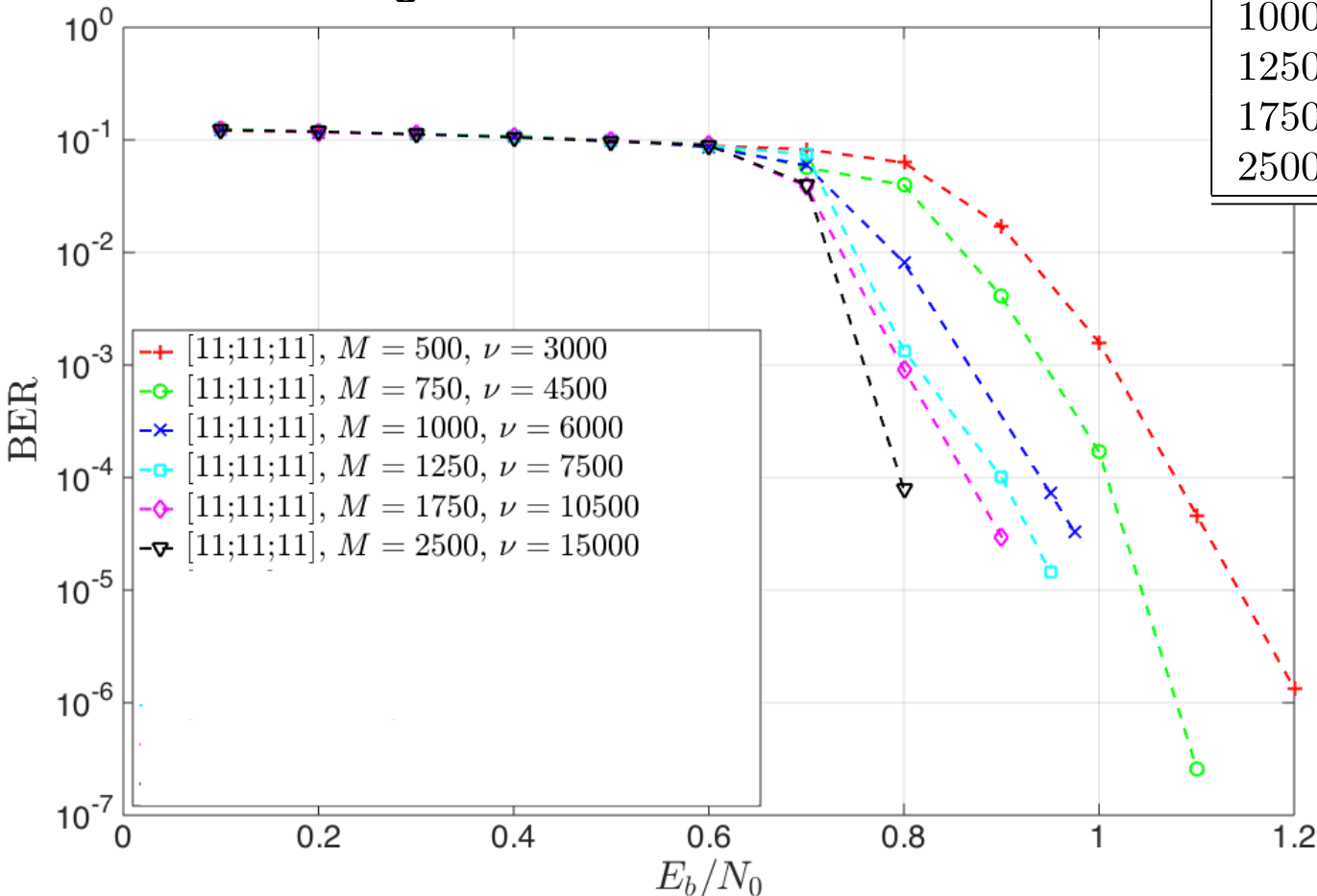
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# WD Scaling - AWGNC

- BER of the  $w = 2$ ,  $[11;11;11]$  codes demonstrating the scaling with  $\nu = 2M(w + 1)$  for increasing  $M$  and fixed  $w$

$M$	$N_B$	$\nu$	$S$
500	1000	3000	12,000
750	1500	4500	18,000
1000	2000	6000	24,000
1250	2500	7500	30,000
1750	3500	10500	42,000
2500	5000	15000	60,000

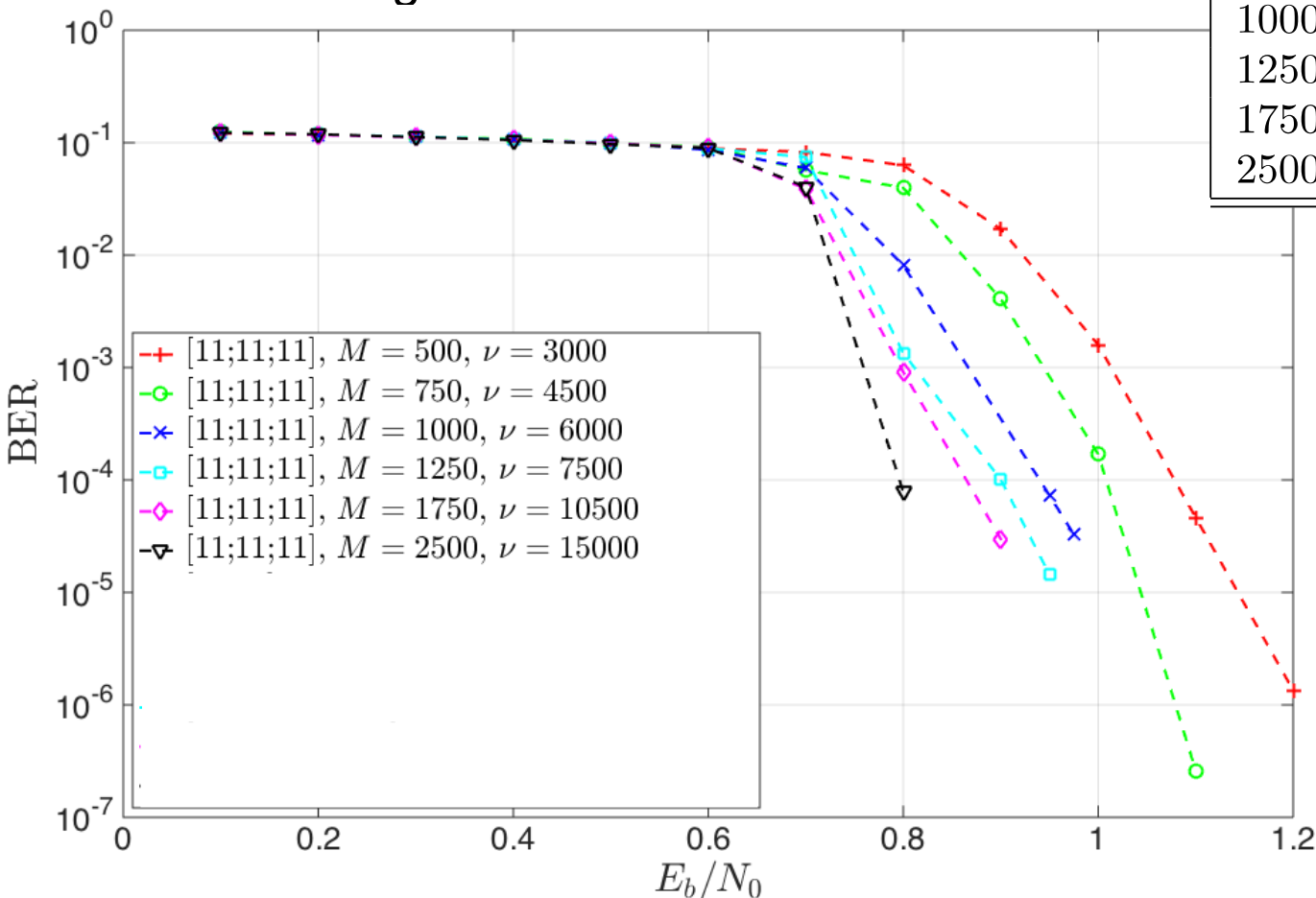




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750	1500	4500	18,000
1000	2000	6000	24,000
1250	2500	7500	30,000
1750	3500	10500	42,000
2500	5000	15000	60,000

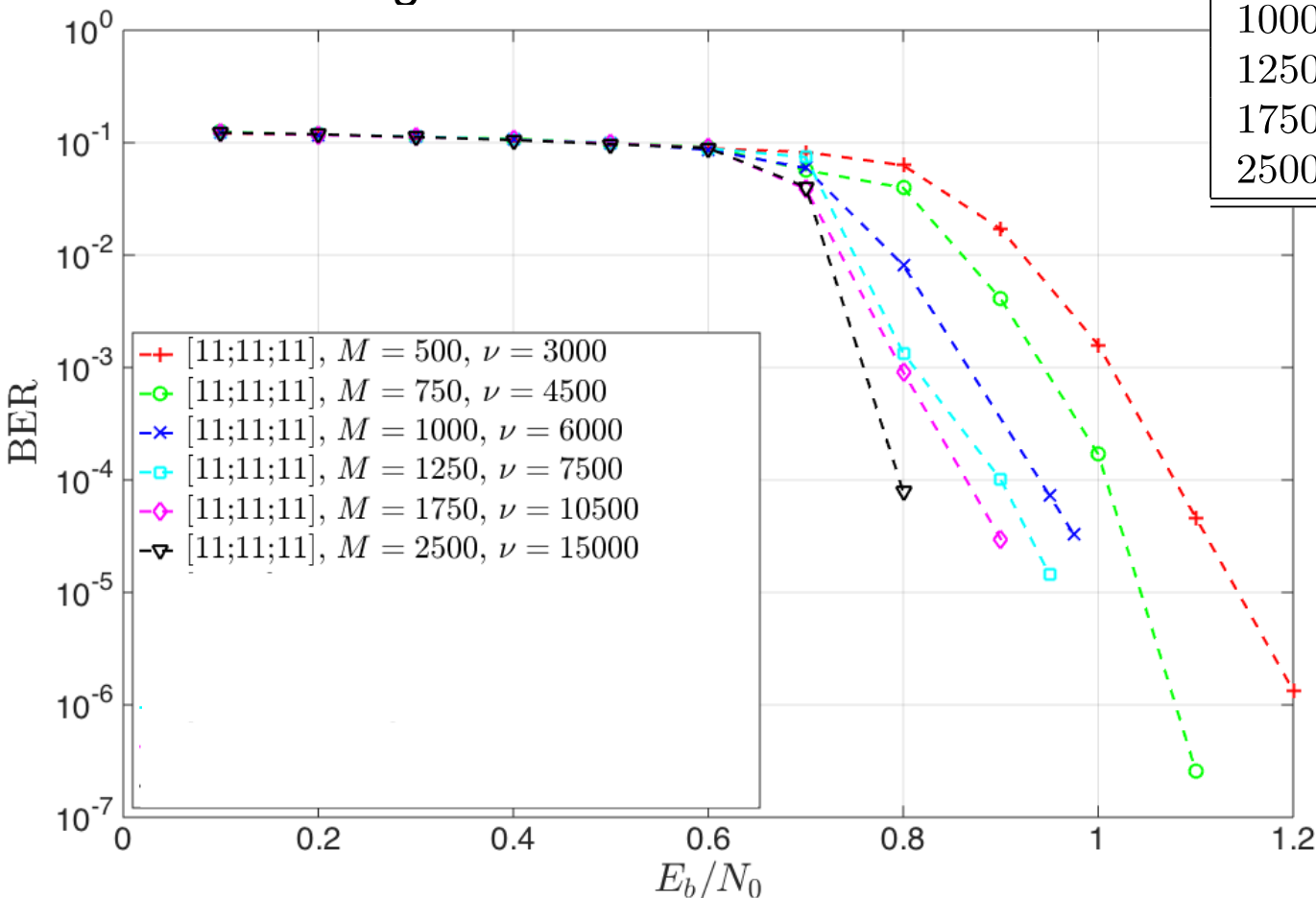


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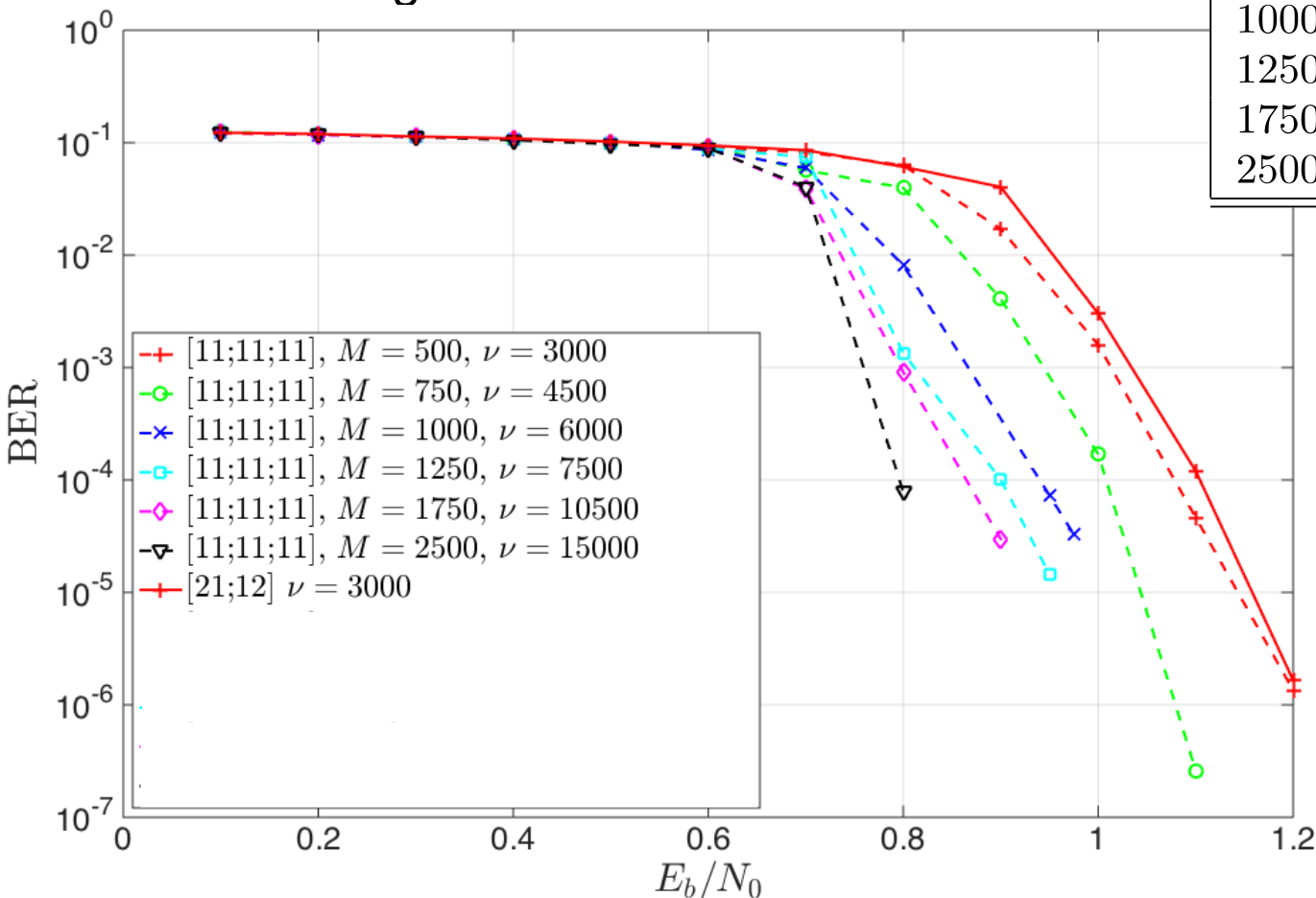


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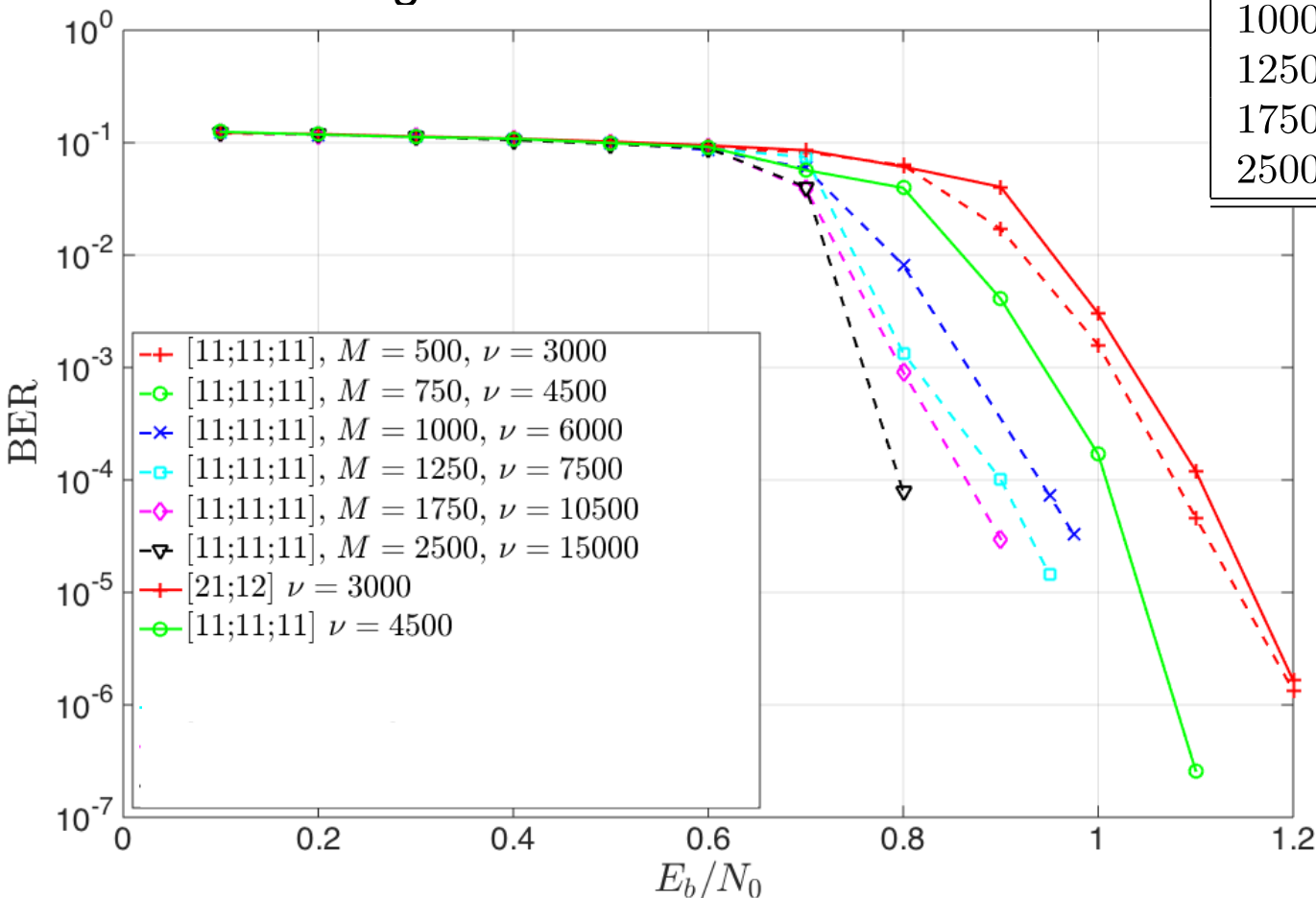


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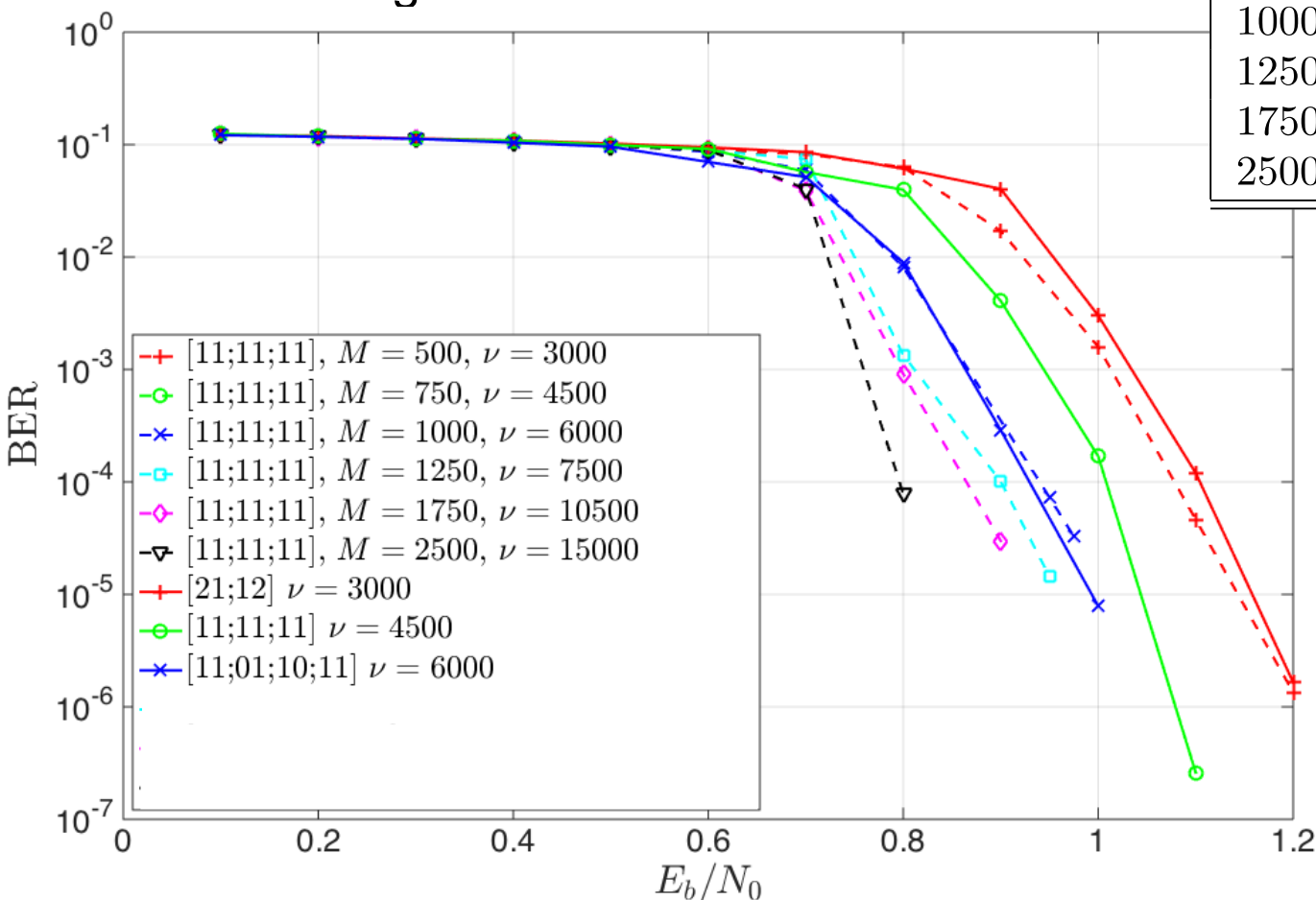


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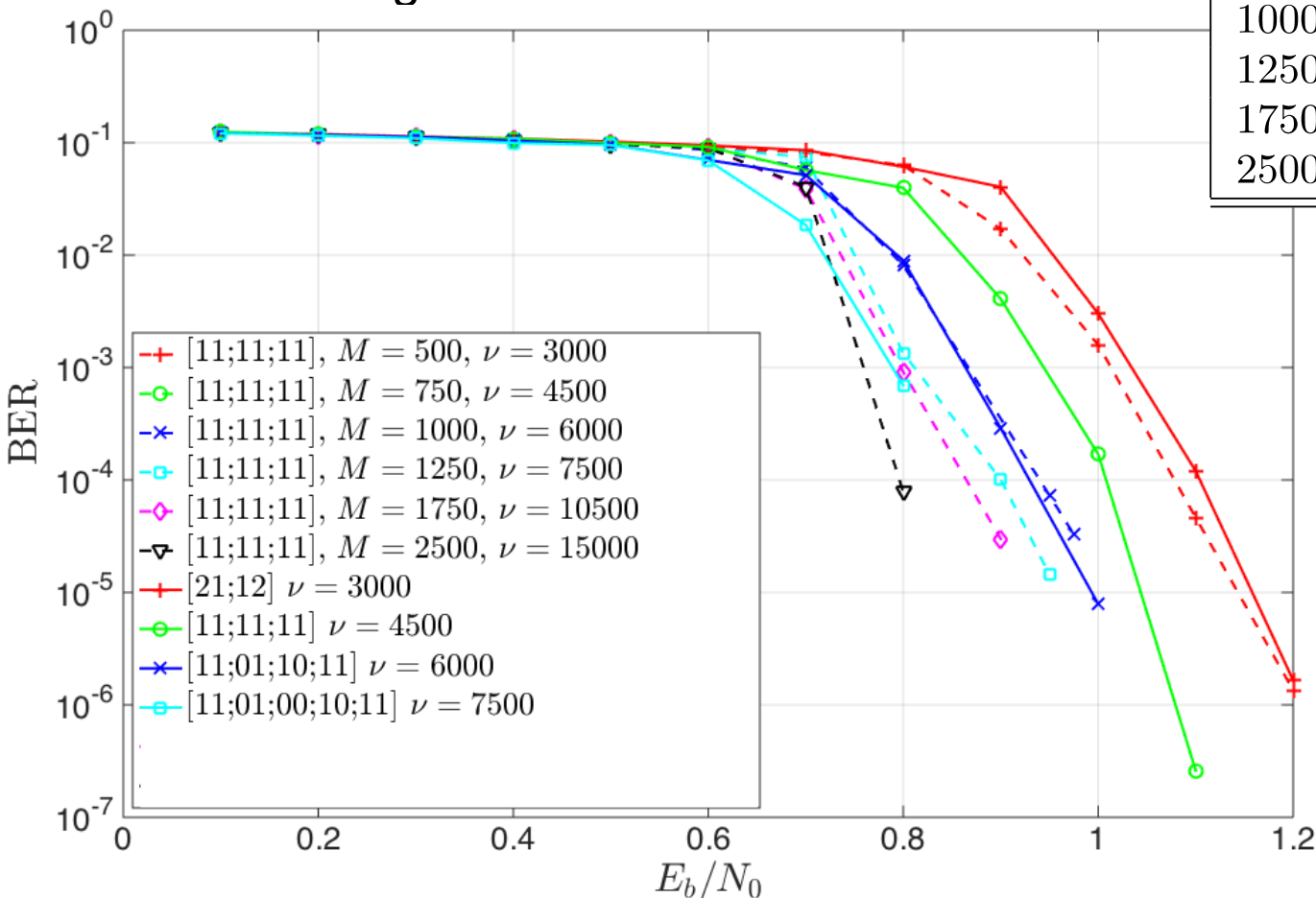
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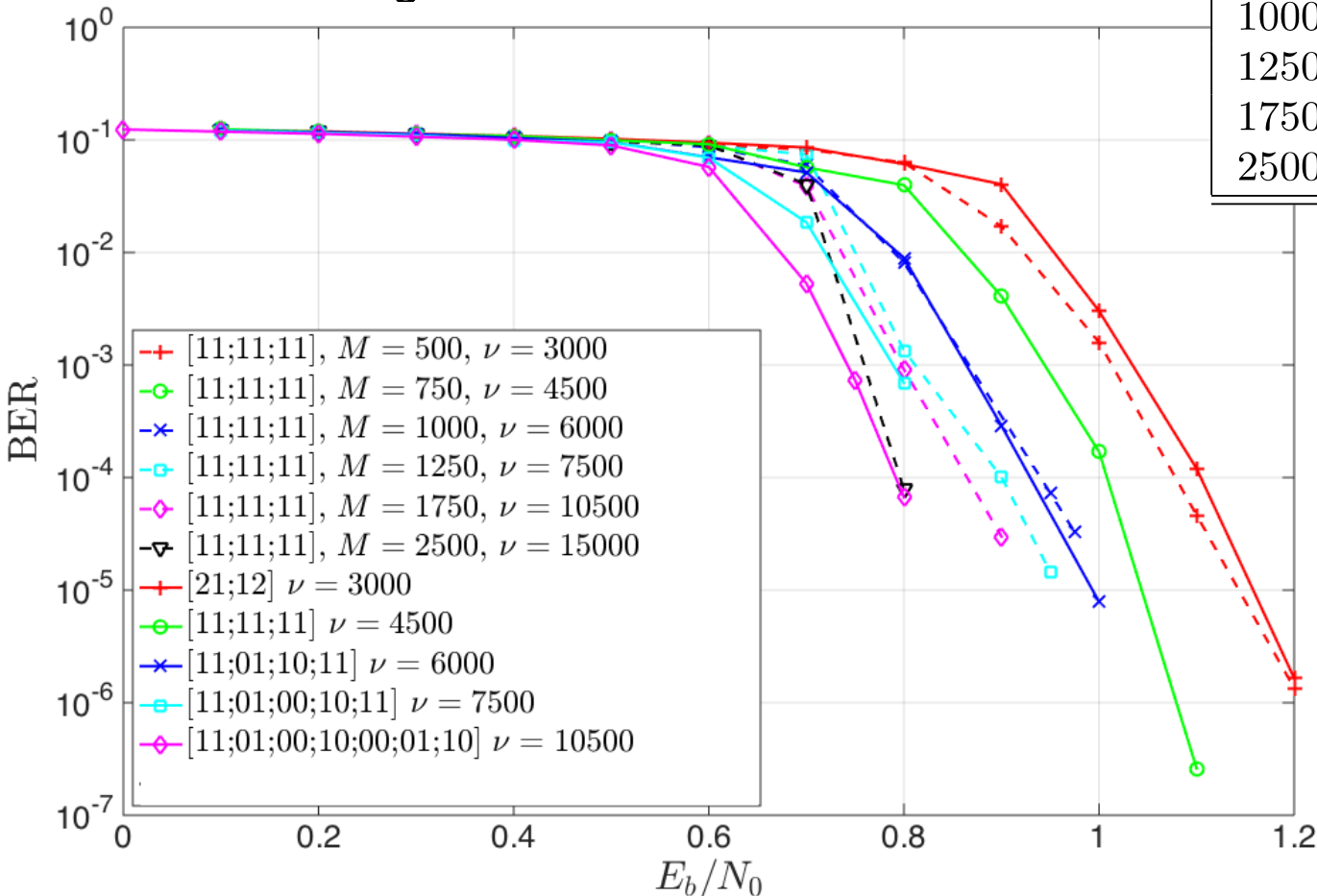


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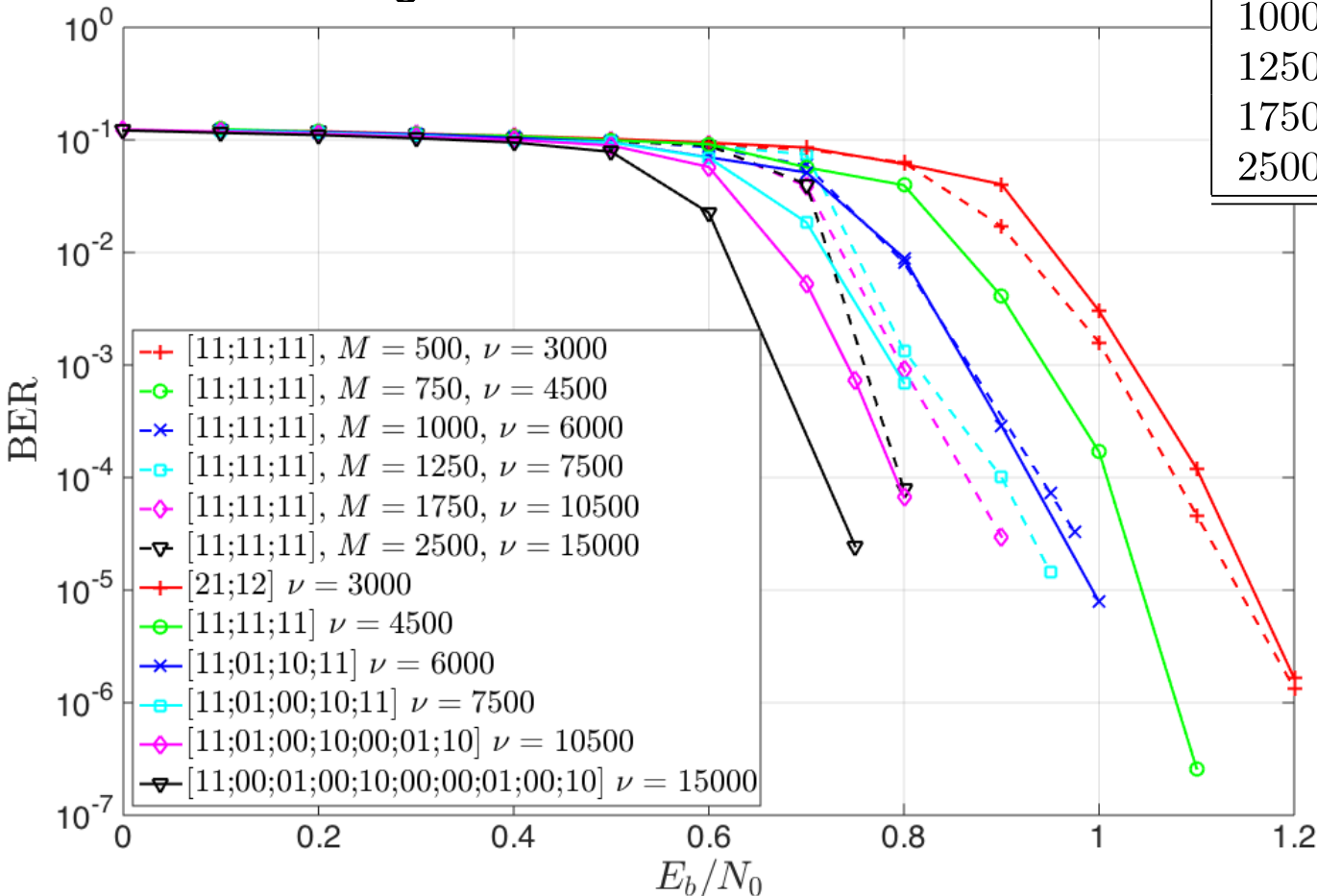


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  - ➔ Hardware advantages of QC designs obtained by circulant liftings
  - ➔ Hardware advantages of the 'asymptotically-regular' structure
  - ➔ Design advantages of flexible frame length and flexible rate obtained by varying  $M$ ,  $L$ , and/or puncturing

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  - ➔ Design advantages of flexible frame length and flexible rate obtained by varying  $M$ ,  $L$ , and/or puncturing
- Of particular importance for applications requiring extremely low decoded bit error rates (e.g., optical communication, data storage) is an investigation of error floor issues related to **stopping sets**, **trapping sets**, and **absorbing sets**.

- Spatially coupled LDPC code ensembles achieve **threshold saturation**, i.e., their iterative decoding thresholds (for large  $L$  and  $M$ ) approach the MAP decoding thresholds of the underlying LDPC block code ensembles.
- The threshold saturation and linear minimum distance growth properties of  $(J,K)$ -regular SC-LDPC codes combine the best asymptotic features of both regular and irregular LDPC-BCs.
- With window decoding, SC-LDPC codes also compare favorably to LDPC-BCs in the finite-length regime, providing flexible tradeoffs between BER performance, decoding latency, and decoding complexity.
- Flexible frame lengths and rates can be obtained by varying  $M$ ,  $L$ , and/or puncturing.